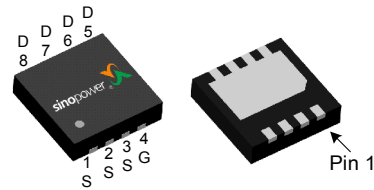


N-Channel Enhancement Mode MOSFET

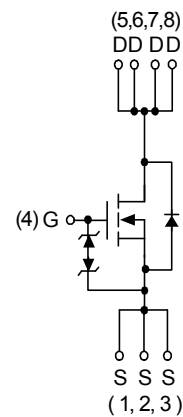
Features

- 20V/17.8A,
 $R_{DS(ON)} = 3.4m\Omega(max.) @ V_{GS} = 4.5V$
 $R_{DS(ON)} = 4.5m\Omega(max.) @ V_{GS} = 2.5V$
 $R_{DS(ON)} = 7m\Omega(max.) @ V_{GS} = 1.8V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available
 (RoHS Compliant)

Pin Description



DFN3.3x3.3C-8_EP



N-Channel MOSFET

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Ordering and Marking Information

SM3406NS □□□-□□□ Assembly Material Handling Code Temperature Range Package Code	Package Code QG : DFN3.3x3.3C-8_EP Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM3406NS QG :	SM 3406N XXXXX XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		20	V
V _{GSS}	Gate-Source Voltage		±12	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _C =25°C	25	A
I _D ^a	Continuous Drain Current	T _C =25°C	50	A
		T _C =100°C	40	
I _{DM} ^b	Pulsed Drain Current	T _C =25°C	200	
P _D	Maximum Power Dissipation	T _C =25°C	35	W
		T _C =100°C	14	
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	3.5	°C/W
I _D	Continuous Drain Current	T _A =25°C	17.8	A
		T _A =70°C	14.2	
P _D ^c	Maximum Power Dissipation	T _A =25°C	1.6	W
		T _A =70°C	1	
R _{θJA} ^d	Thermal Resistance-Junction to Ambient	Steady State	78	°C/W
I _{AS} ^e	Avalanche Current, Single pulse (L=0.1mH)		45	A
E _{AS} ^e	Avalanche Energy, Single pulse (L=0.1mH)		100	mJ

Note a: Package is limited by 50A

Note b: Pulse width limited by max. junction temperature.

Note c: $R_{\theta\text{JA}}$ steady state=999s.

Note d: $R_{\theta\text{JA}}$ steady state=999s. $R_{\theta\text{JA}}$ is measured with the device mounted on 1in2, Fr-4 board with 2oz.Copper.

Note e: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

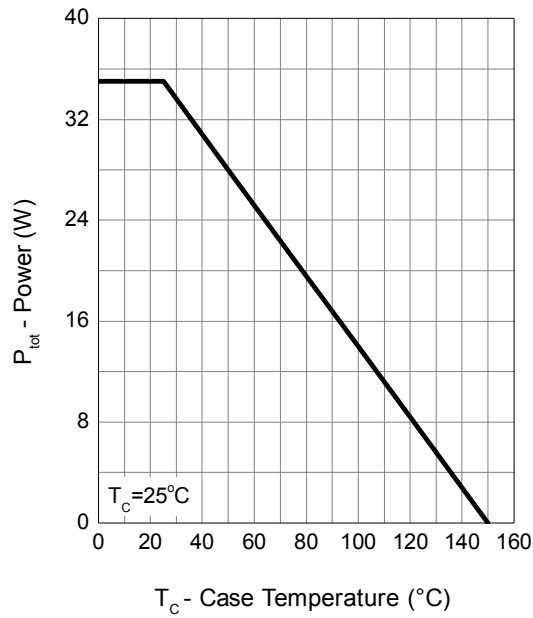
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V T _J =85°C	-	-	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	0.5	0.7	1	V
I _{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^g	Drain-Source On-state Resistance	V _{GS} =4.5V, I _{DS} =13.5A	-	2.7	3.4	mΩ
		V _{GS} =2.5V, I _{DS} =10A	-	3.3	4.5	
		V _{GS} =1.8V, I _{DS} =2A	-	4.6	7	
Gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	34	-	S
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =2A, V _{GS} =0V	-	0.7	1.1	V
t _{rr}	Reverse Recovery Time	I _{SD} =13.5A,	-	18	-	ns
Q _{rr}	Reverse Recovery Charge	dI _{SD} /dt=100A/μs	-	6.2	-	nC
Dynamic Characteristics ^g						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	2	3.6	Ω
C _{iss}	Input Capacitance	V _{GS} =0V,	-	3775	4910	pF
C _{oss}	Output Capacitance	V _{DS} =10V,	-	730	-	
C _{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	525	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =10V, R _L =10Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	14	26	ns
t _r	Turn-on Rise Time		-	14.5	27	
t _{d(OFF)}	Turn-off Delay Time		-	130	234	
t _f	Turn-off Fall Time		-	70	126	
Gate Charge Characteristics ^g						
Q _g	Total Gate Charge	V _{DS} =10V, V _{GS} =4.5V, I _{DS} =13.5A	-	35	50	nC
Q _{gs}	Gate-Source Charge		-	4.7	-	
Q _{gd}	Gate-Drain Charge		-	11.5	-	

Note f: Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

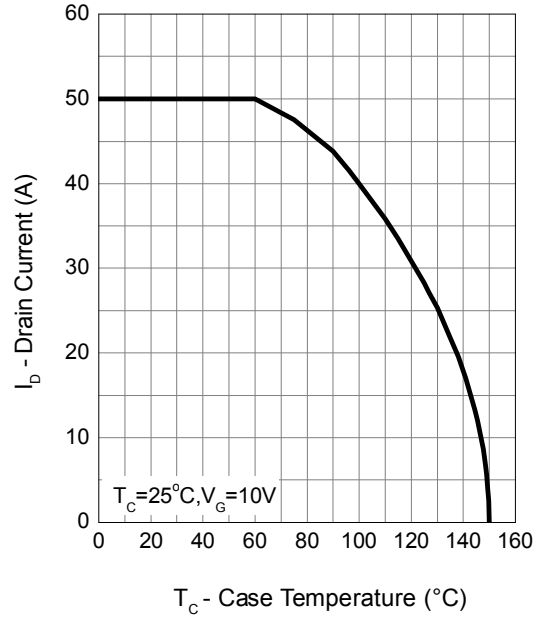
Note g: Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

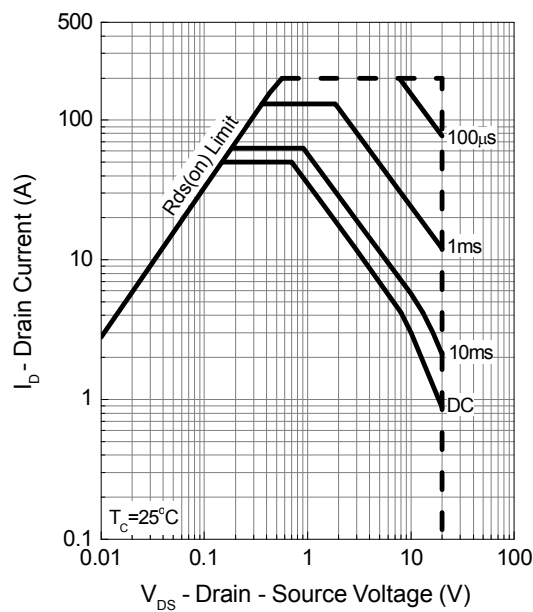
Power Dissipation



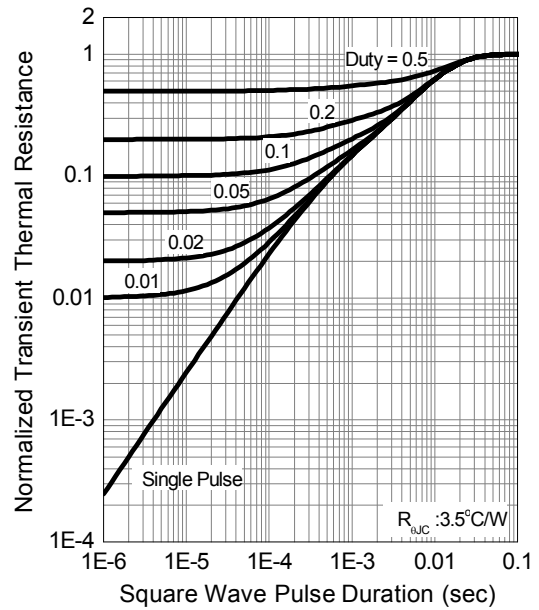
Drain Current



Safe Operation Area

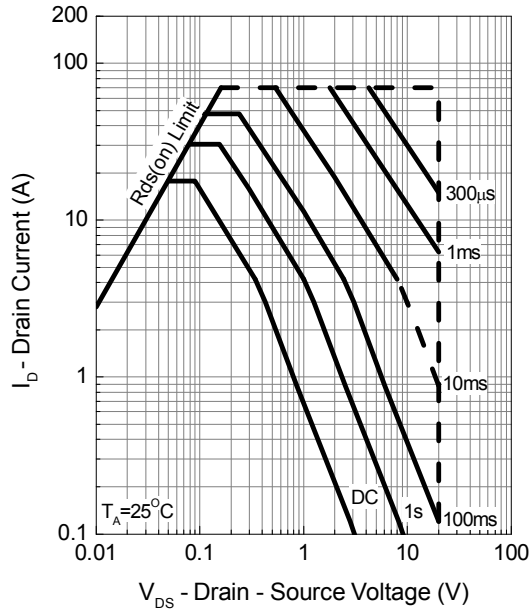


Thermal Transient Impedance

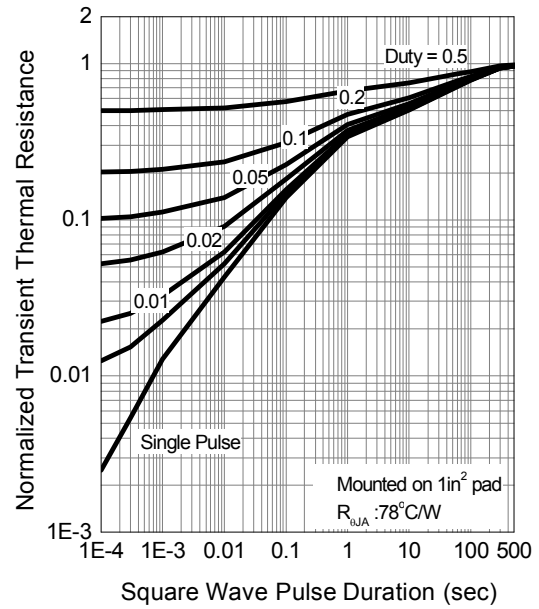


Typical Operating Characteristics (Cont.)

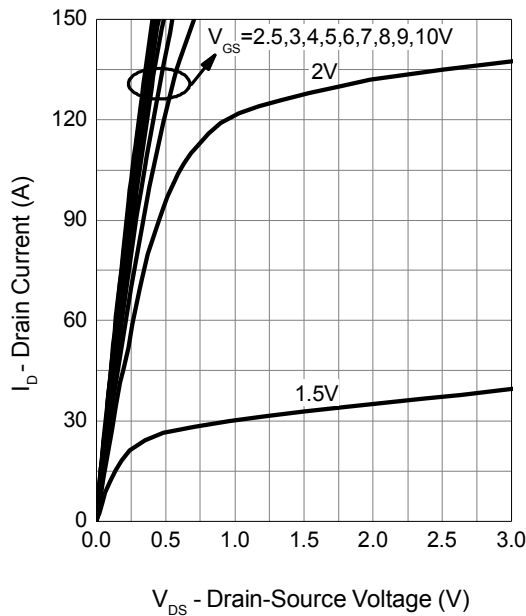
Safe Operation Area



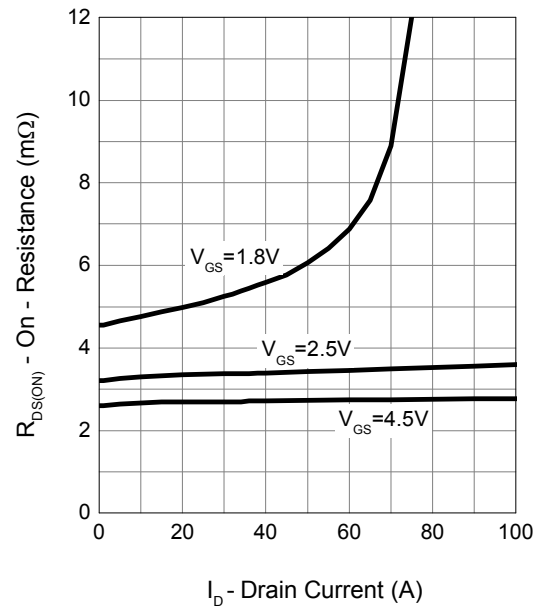
Thermal Transient Impedance



Output Characteristics

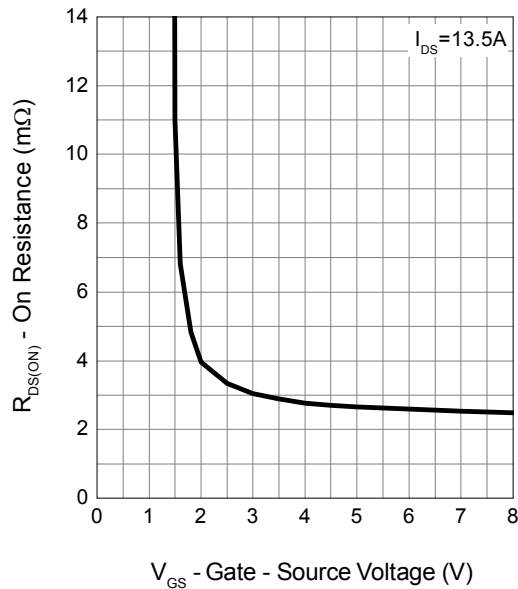


Drain-Source On Resistance

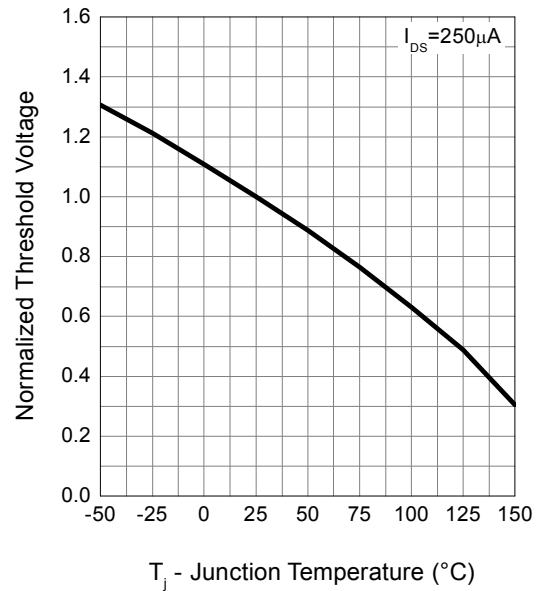


Typical Operating Characteristics (Cont.)

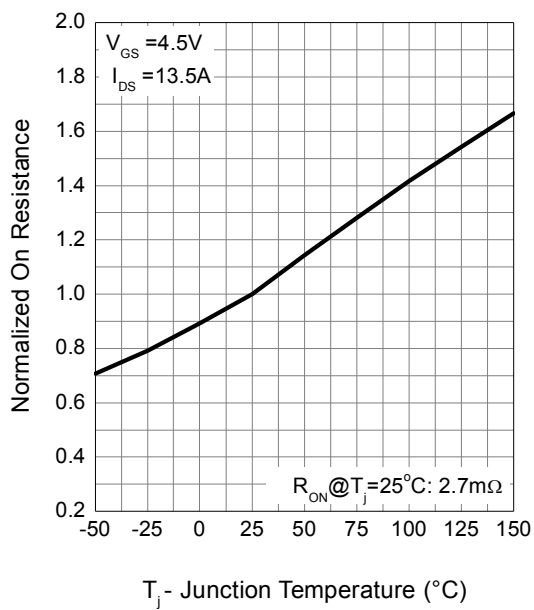
Gate-Source On Resistance



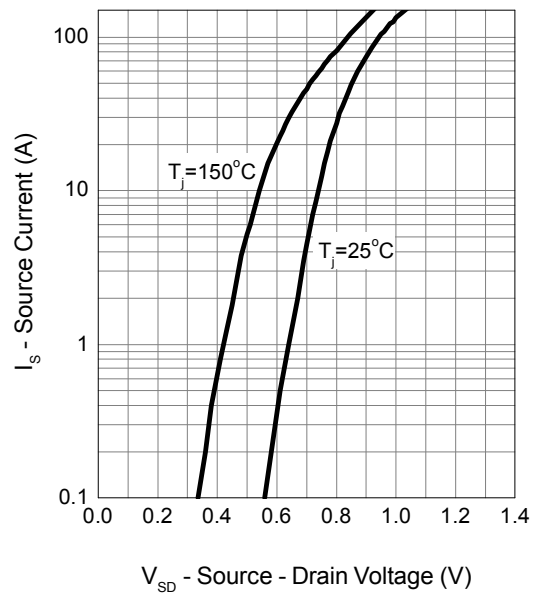
Gate Threshold Voltage



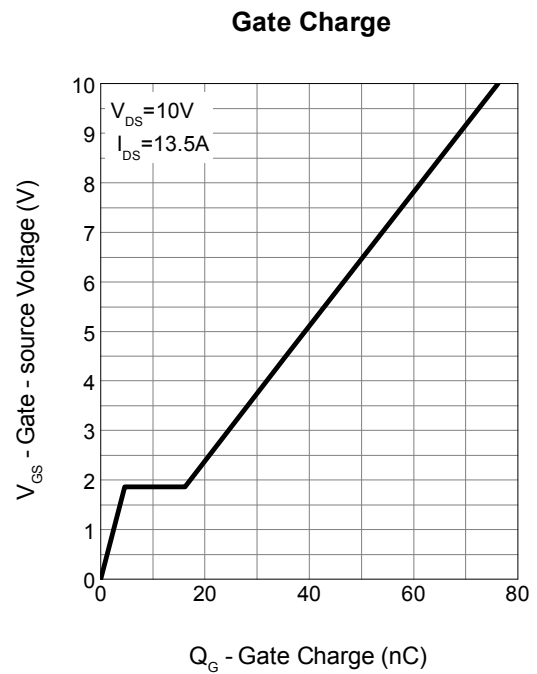
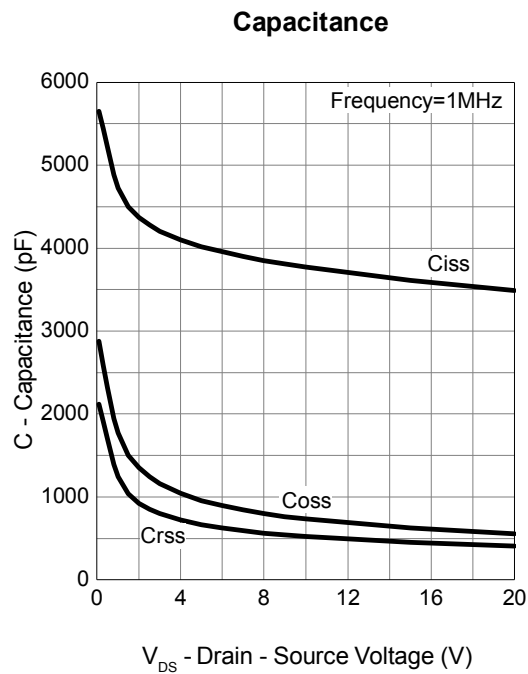
Drain-Source On Resistance



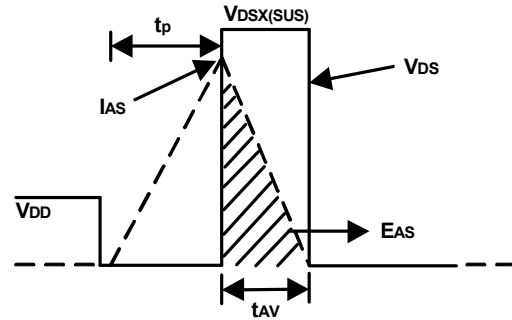
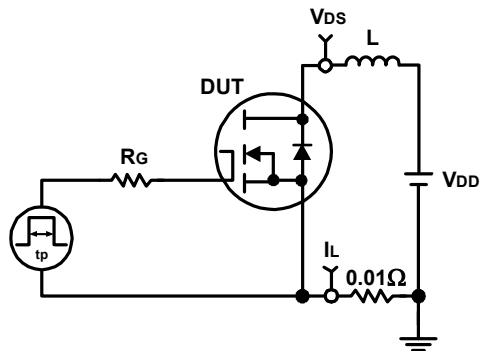
Source-Drain Diode Forward



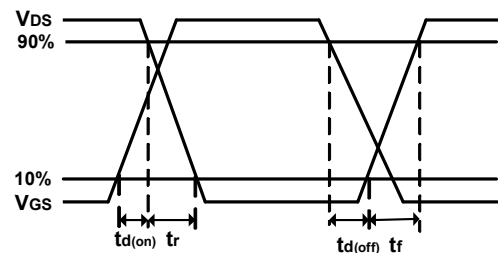
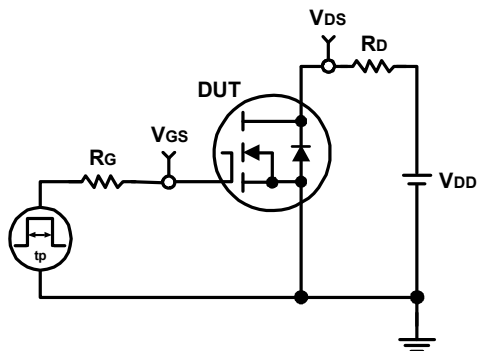
Typical Operating Characteristics (Cont.)



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Disclaimer

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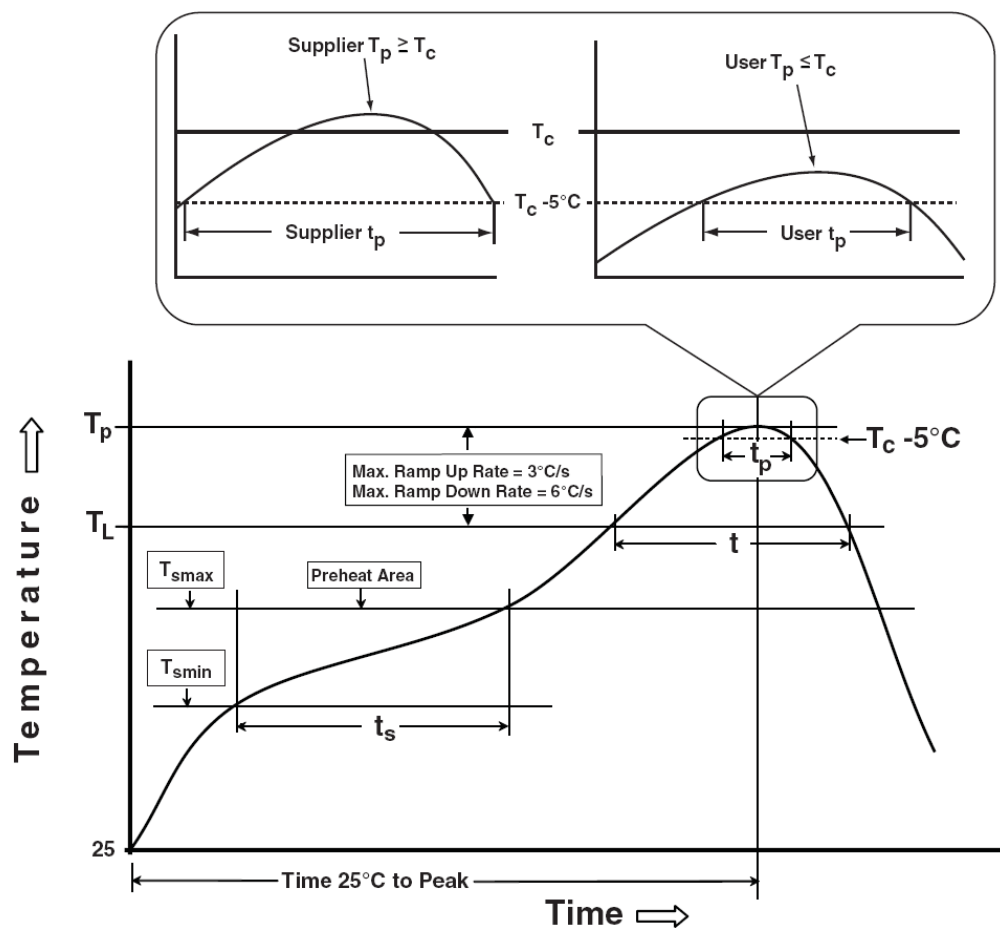
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Customer Service

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