

N-Channel Enhancement Mode MOSFET

Features

- 30V/0.35A ,
 $R_{DS(ON)} = 700m\Omega(max.) @ V_{GS}=4.5V$
 $R_{DS(ON)} = 1000m\Omega(max.) @ V_{GS}=2.5V$
 $R_{DS(ON)} = 1600m\Omega(max.) @ V_{GS}=1.8V$
 $R_{DS(ON)} = 5000m\Omega(max.) @ V_{GS}=1.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)
- ESD Protection

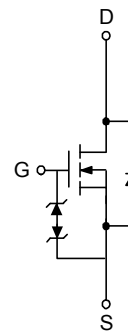
Applications

- High Speed Switching.
- Analog Switch.

Pin Description

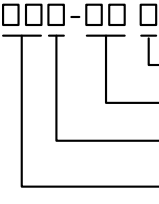


Top View of SOT-723



N-Channel MOSFET

Ordering and Marking Information

SM1202NS □□□-□□□  Assembly Material Handling Code Temperature Range Package Code	Package Code AS : SOT-723 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM1202NS AS : 02	

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Note a : Pulse width limited by max. junction temperature.

Note b : Surface Mounted on min. pad area.

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

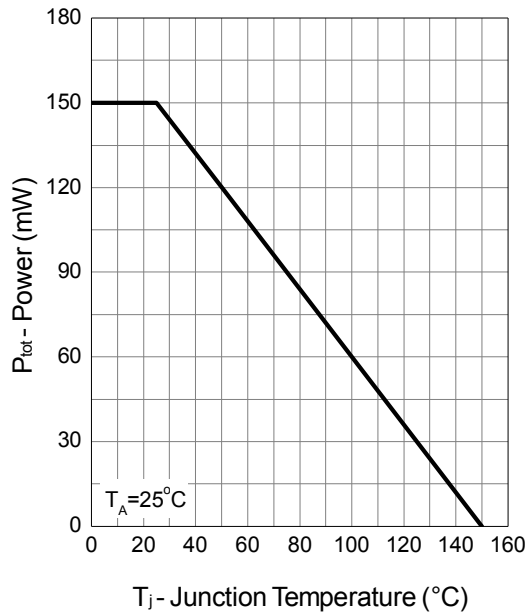
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ C$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	0.6	0.95	1.3	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 10	μA
$R_{DS(ON)}^d$	Drain-Source On-state Resistance	$V_{GS}=4.5V, I_{DS}=100mA$	-	550	700	$m\Omega$
		$V_{GS}=2.5V, I_{DS}=50mA$	-	700	1000	
		$V_{GS}=1.8V, I_{DS}=20mA$	-	1100	1600	
		$V_{GS}=1.5V, I_{DS}=10mA$	-	2000	5000	
Diode Characteristics						
V_{SD}^c	Diode Forward Voltage	$I_{SD}=250mA, V_{GS}=0V$	-	0.8	1.3	V
t_{rr}	Reverse Recovery Time	$I_{SD}=1A, dI_{SD}/dt=100A/\mu s$	-	8.2	-	ns
Q_{rr}	Reverse Recovery Charge		-	2.5	-	nC
Dynamic Characteristics ^c						
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Frequency}=1.0MHz$	-	28	37	pF
C_{oss}	Output Capacitance		-	8	-	
C_{rss}	Reverse Transfer Capacitance		-	3	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=30\Omega, I_{DS}=0.33A, V_{GEN}=10V, R_G=6\Omega$	-	1.4	2.6	ns
t_r	Turn-on Rise Time		-	9.9	17.9	
$t_{d(OFF)}$	Turn-off Delay Time		-	10.6	19.2	
t_f	Turn-off Fall Time		-	2.3	4.2	
Gate Charge Characteristics ^d						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V, I_{DS}=1A$	-	0.74	1.04	nC
Q_{gs}	Gate-Source Charge		-	0.44	-	
Q_{gd}	Gate-Drain Charge		-	0.1	-	

Note c : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

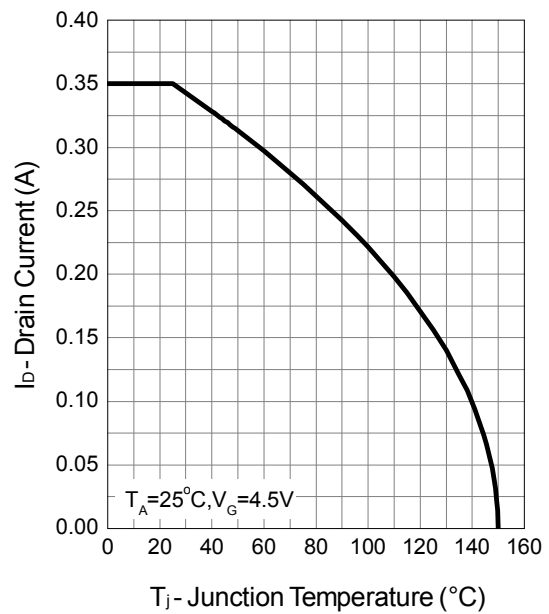
Note d : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

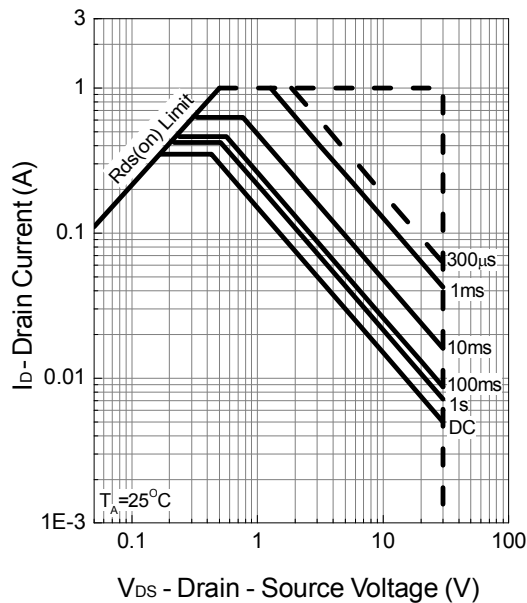
Power Dissipation



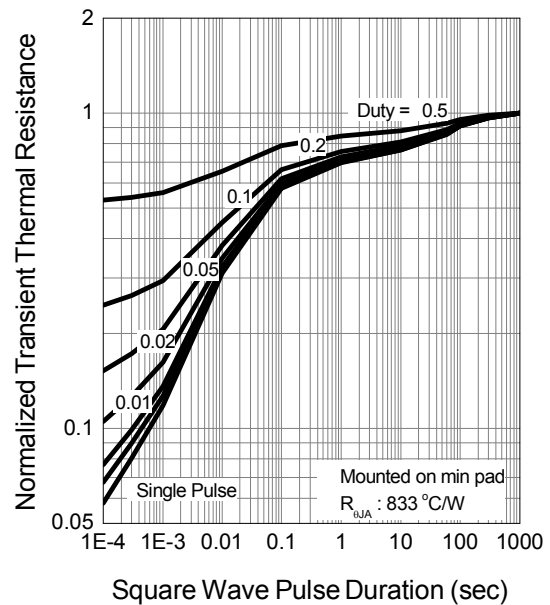
Drain Current



Safe Operation Area

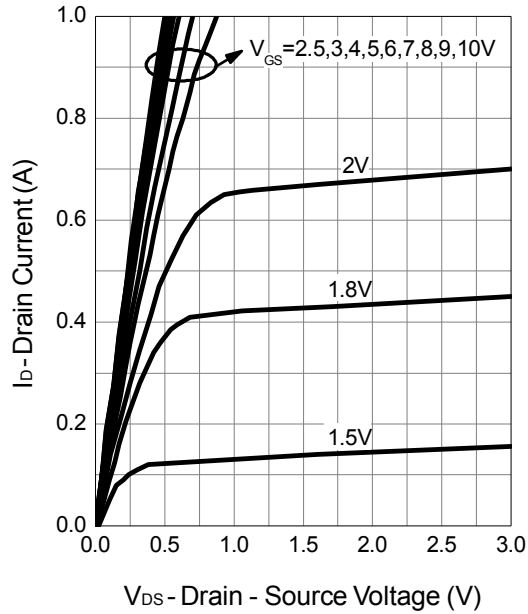


Thermal Transient Impedance

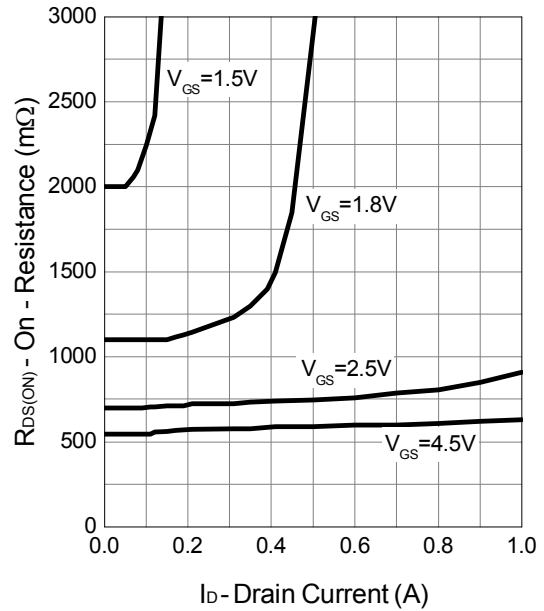


Typical Operating Characteristics (Cont.)

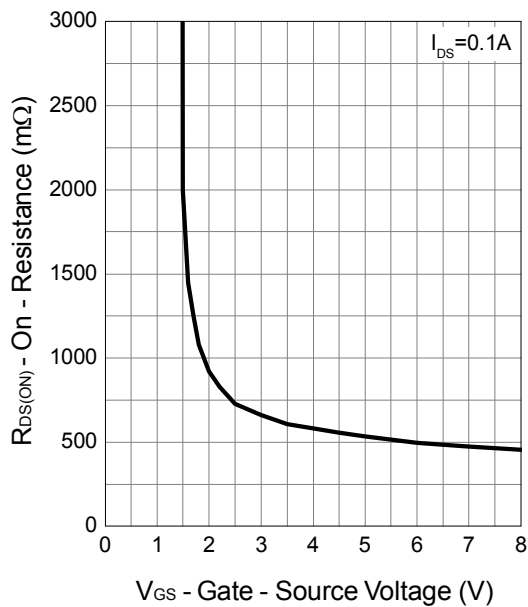
Output Characteristics



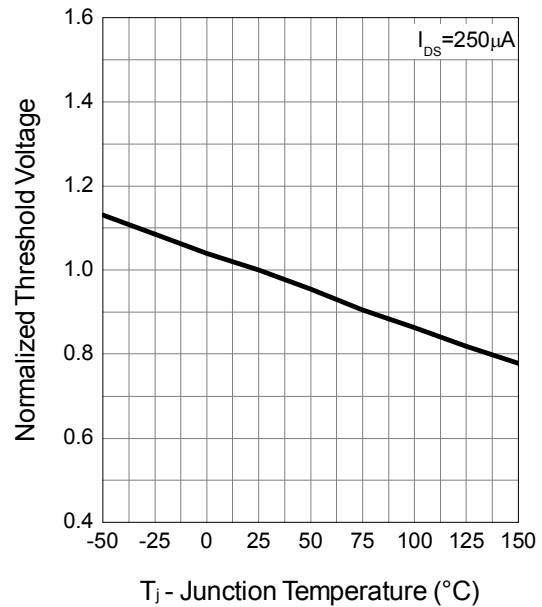
Drain-Source On Resistance



Gate-Source On Resistance

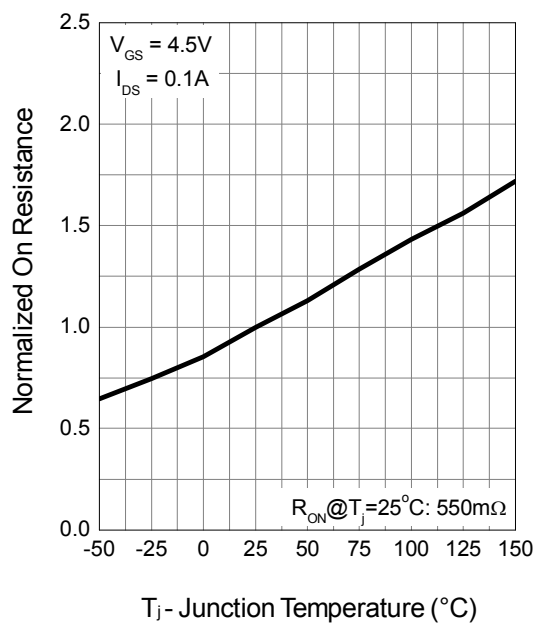


Gate Threshold Voltage

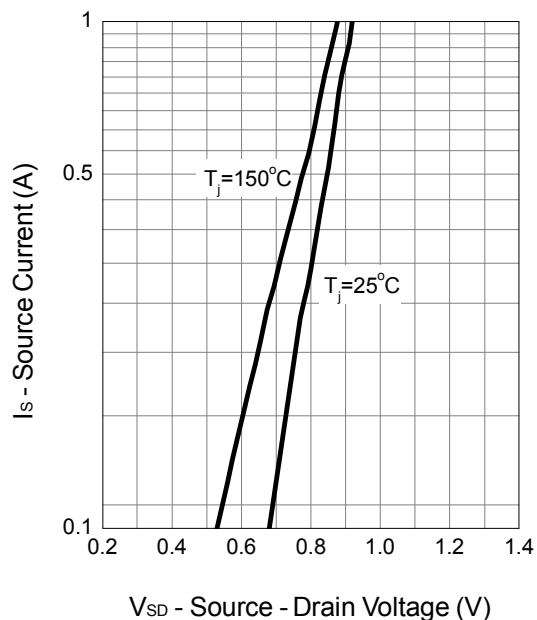


Typical Operating Characteristics (Cont.)

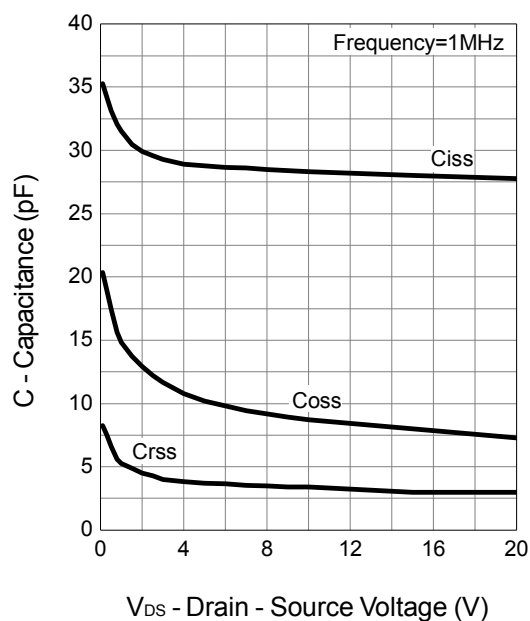
Drain-Source On Resistance



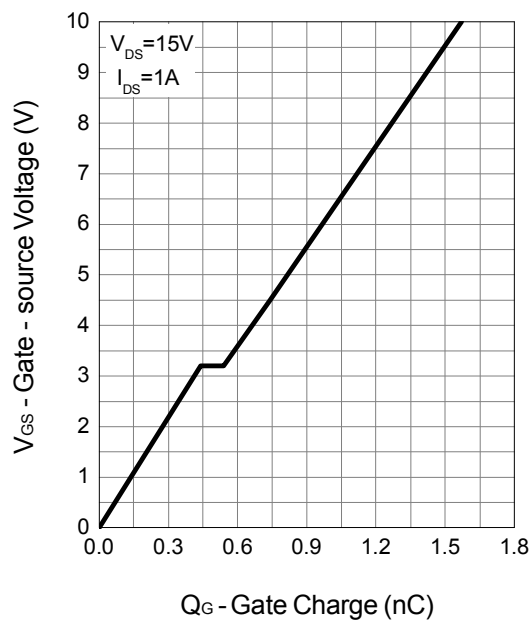
Source-Drain Diode Forward



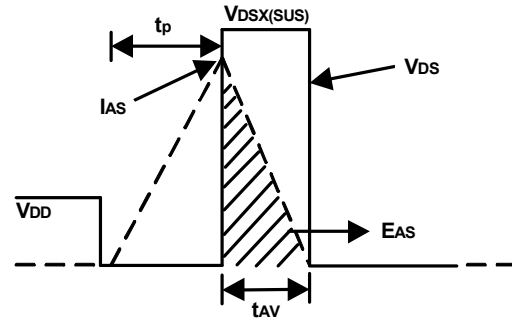
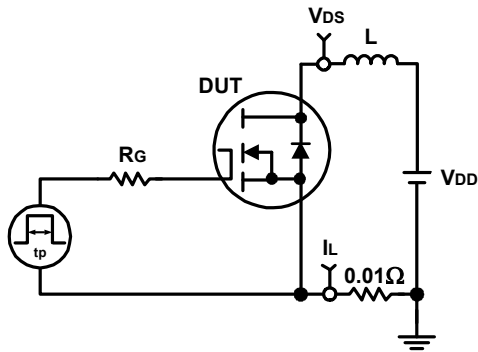
Capacitance



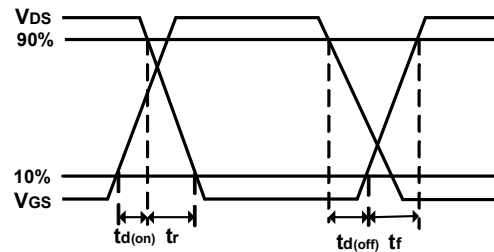
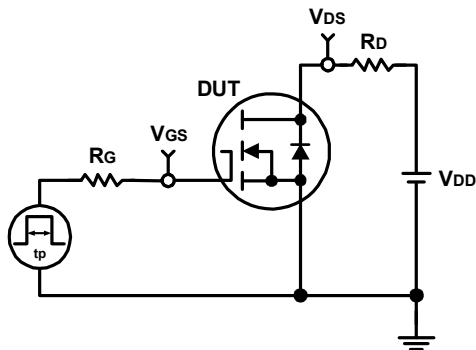
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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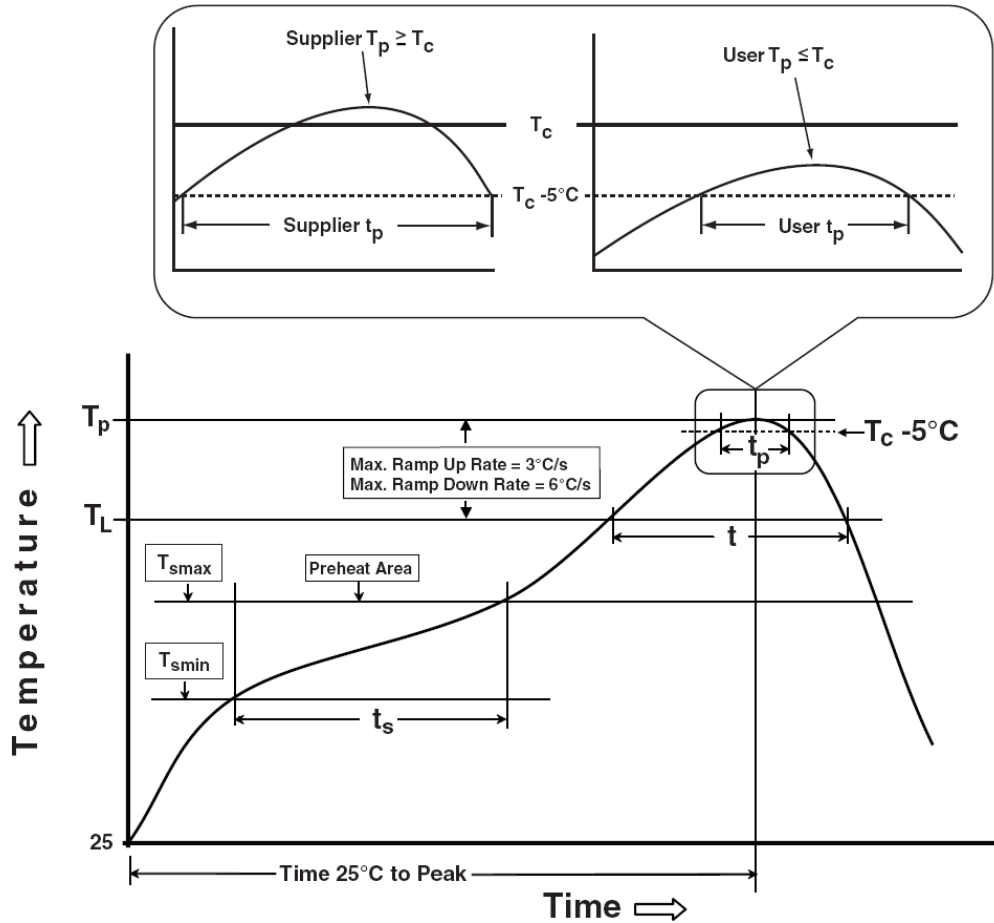
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

Sinopower Semiconductor, Inc.

5F, No. 6, Dusing 1St Rd., Hsinchu Science Park,

Hsinchu, 30078, Taiwan

TEL: 886-3-5635818 Fax: 886-3-5635080