

# DATA SHEET

## **PEMB4; PUMB4**

PNP/PNP resistor-equipped  
transistors; R1 = 10 k $\Omega$ , R2 = open

Product specification  
Supersedes data of 2001 Sep 14

2003 Oct 15

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### FEATURES

- Built-in bias resistors
- Simplified circuit design
- Reduction of component count
- Reduced pick and place costs.

### APPLICATIONS

- Low current peripheral drivers
- Replacement of general purpose transistors in digital applications
- Control of IC inputs.

### QUICK REFERENCE DATA

| SYMBOL           | PARAMETER                 | TYP. | MAX. | UNIT       |
|------------------|---------------------------|------|------|------------|
| V <sub>CEO</sub> | collector-emitter voltage | –    | –50  | V          |
| I <sub>O</sub>   | output current (DC)       | –    | –100 | mA         |
| TR1              | PNP                       | –    | –    | –          |
| TR2              | PNP                       | –    | –    | –          |
| R1               | bias resistor             | 10   | –    | k $\Omega$ |
| R2               | bias resistor             | open | –    | –          |

### DESCRIPTION

PNP/PNP resistor-equipped transistors (see “Simplified outline, symbol and pinning” for package details).

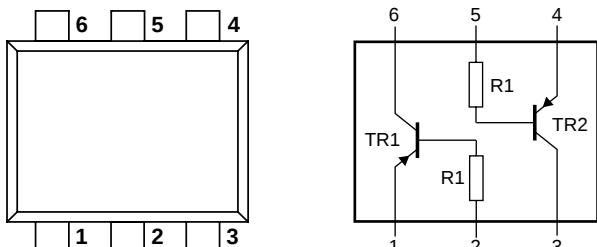
### PRODUCT OVERVIEW

| TYPE NUMBER | PACKAGE |       | MARKING CODE       | NPN/PNP COMPLEMENT | NPN/PNP COMPLEMENT |
|-------------|---------|-------|--------------------|--------------------|--------------------|
|             | PHILIPS | EIAJ  |                    |                    |                    |
| PEMB4       | SOT666  | –     | B4                 | PEMD4              | PEMH4              |
| PUMB4       | SOT363  | SC-88 | B*4 <sup>(1)</sup> | PUMD4              | PUMH4              |

### Note

- \* = p: Made in Hong Kong.  
\* = t: Made in Malaysia.  
\* = W: Made in China.

### SIMPLIFIED OUTLINE, SYMBOL AND PINNING

| TYPE NUMBER    | SIMPLIFIED OUTLINE AND SYMBOL                                                                                      | PINNING                    |                                                                                      |
|----------------|--------------------------------------------------------------------------------------------------------------------|----------------------------|--------------------------------------------------------------------------------------|
|                |                                                                                                                    | PIN                        | DESCRIPTION                                                                          |
| PEMB4<br>PUMB4 |  <p>Top view</p> <p>MAM452</p> | 1<br>2<br>3<br>4<br>5<br>6 | emitter TR1<br>base TR1<br>collector TR2<br>emitter TR2<br>base TR2<br>collector TR1 |

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## ORDERING INFORMATION

| TYPE NUMBER | PACKAGE |                                          |         |
|-------------|---------|------------------------------------------|---------|
|             | NAME    | DESCRIPTION                              | VERSION |
| PEMB4       | –       | plastic surface mounted package; 6 leads | SOT666  |
| PUMB4       | –       | plastic surface mounted package; 6 leads | SOT363  |

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL                | PARAMETER                     | CONDITIONS               | MIN. | MAX. | UNIT |
|-----------------------|-------------------------------|--------------------------|------|------|------|
| <b>Per transistor</b> |                               |                          |      |      |      |
| V <sub>CBO</sub>      | collector-base voltage        | open emitter             | –    | –50  | V    |
| V <sub>CEO</sub>      | collector-emitter voltage     | open base                | –    | –50  | V    |
| V <sub>EBO</sub>      | emitter-base voltage          | open collector           | –    | –5   | V    |
| I <sub>O</sub>        | output current (DC)           |                          | –    | –100 | mA   |
| I <sub>CM</sub>       | peak collector current        |                          | –    | –100 | mA   |
| P <sub>tot</sub>      | total power dissipation       | T <sub>amb</sub> ≤ 25 °C |      |      |      |
|                       | SOT363                        | note 1                   | –    | 200  | mW   |
|                       | SOT666                        | notes 1 and 2            | –    | 200  | mW   |
| T <sub>stg</sub>      | storage temperature           |                          | –65  | +150 | °C   |
| T <sub>j</sub>        | junction temperature          |                          | –    | 150  | °C   |
| T <sub>amb</sub>      | operating ambient temperature |                          | –65  | +150 | °C   |
| <b>Per device</b>     |                               |                          |      |      |      |
| P <sub>tot</sub>      | total power dissipation       | T <sub>amb</sub> ≤ 25 °C |      |      |      |
|                       | SOT363                        | note 1                   | –    | 300  | mW   |
|                       | SOT666                        | notes 1 and 2            | –    | 300  | mW   |

## Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
2. Reflow soldering is the only recommended soldering method.

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#### THERMAL CHARACTERISTICS

| SYMBOL                | PARAMETER                                   | CONDITIONS               | VALUE | UNIT |
|-----------------------|---------------------------------------------|--------------------------|-------|------|
| <b>Per transistor</b> |                                             |                          |       |      |
| R <sub>th j-a</sub>   | thermal resistance from junction to ambient | T <sub>amb</sub> ≤ 25 °C |       |      |
|                       | SOT363                                      | note 1                   | 625   | K/W  |
|                       | SOT666                                      | notes 1 and 2            | 625   | K/W  |
| <b>Per device</b>     |                                             |                          |       |      |
| R <sub>th j-a</sub>   | thermal resistance from junction to ambient | T <sub>amb</sub> ≤ 25 °C |       |      |
|                       | SOT363                                      | note 1                   | 416   | K/W  |
|                       | SOT666                                      | note 1                   | 416   | K/W  |

#### Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
2. Reflow soldering is the only recommended soldering method.

#### CHARACTERISTICS

T<sub>amb</sub> = 25 °C unless otherwise specified.

| SYMBOL             | PARAMETER                         | CONDITIONS                                                                 | MIN. | TYP. | MAX. | UNIT       |
|--------------------|-----------------------------------|----------------------------------------------------------------------------|------|------|------|------------|
| I <sub>CBO</sub>   | collector-base cut-off current    | V <sub>CB</sub> = -50 V; I <sub>E</sub> = 0                                | –    | –    | -100 | nA         |
| I <sub>CEO</sub>   | collector-emitter cut-off current | V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0                                | –    | –    | -1   | μA         |
|                    |                                   | V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0; T <sub>J</sub> = 150 °C       | –    | –    | -50  | μA         |
| I <sub>EBO</sub>   | emitter-base cut-off current      | V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0                                 | –    | –    | -100 | nA         |
| h <sub>FE</sub>    | DC current gain                   | V <sub>CE</sub> = -5 V; I <sub>C</sub> = -1 mA                             | 200  | –    | –    |            |
| V <sub>CEsat</sub> | saturation voltage                | I <sub>C</sub> = -10 mA; I <sub>B</sub> = -0.5 mA                          | –    | –    | -150 | mV         |
| R1                 | input resistor                    |                                                                            | 7    | 10   | 13   | k $\Omega$ |
| C <sub>c</sub>     | collector capacitance             | I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = -10 V;<br>f = 1 MHz | –    | –    | 3    | pF         |

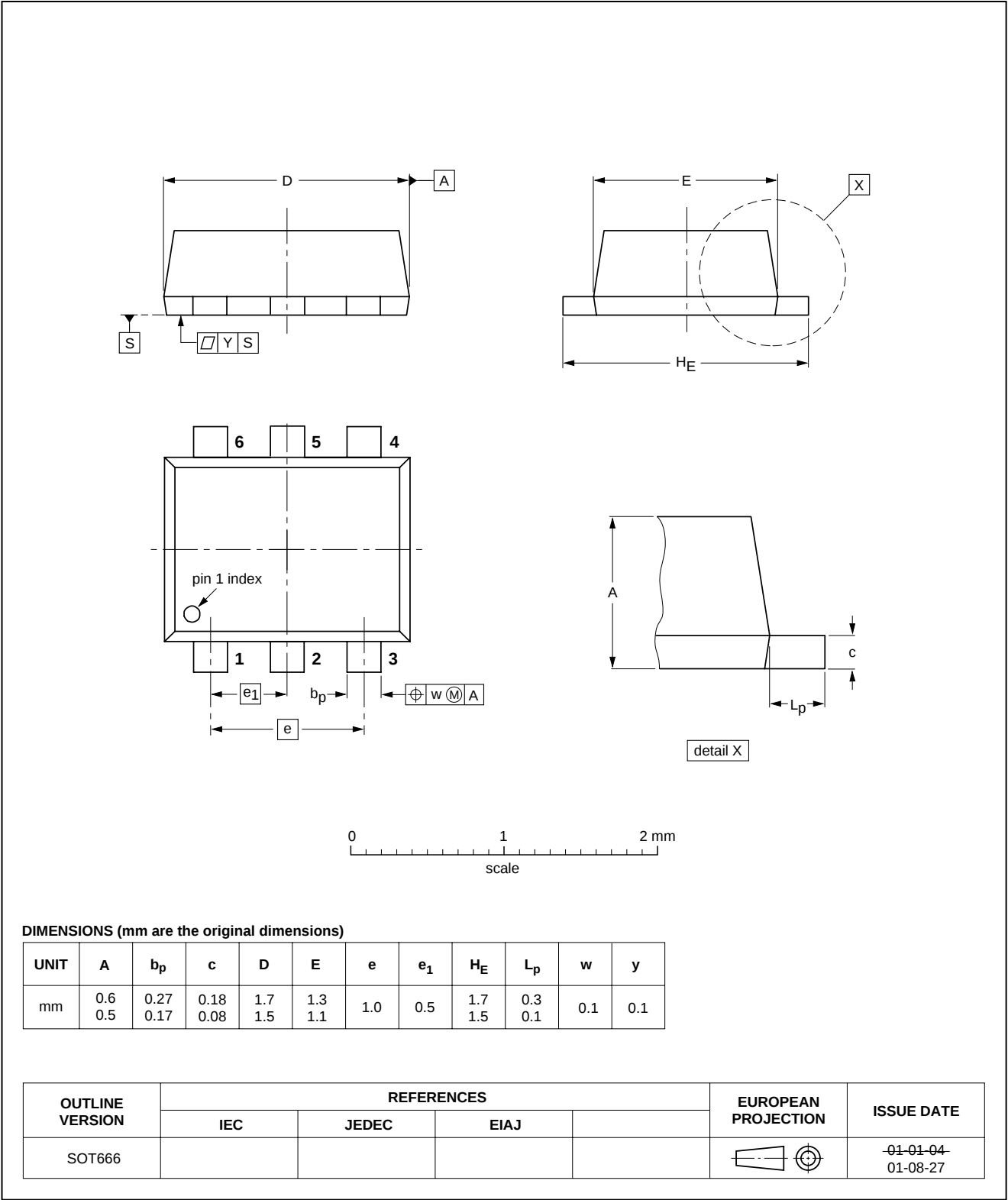
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PACKAGE OUTLINES

Plastic surface mounted package; 6 leads

SOT666

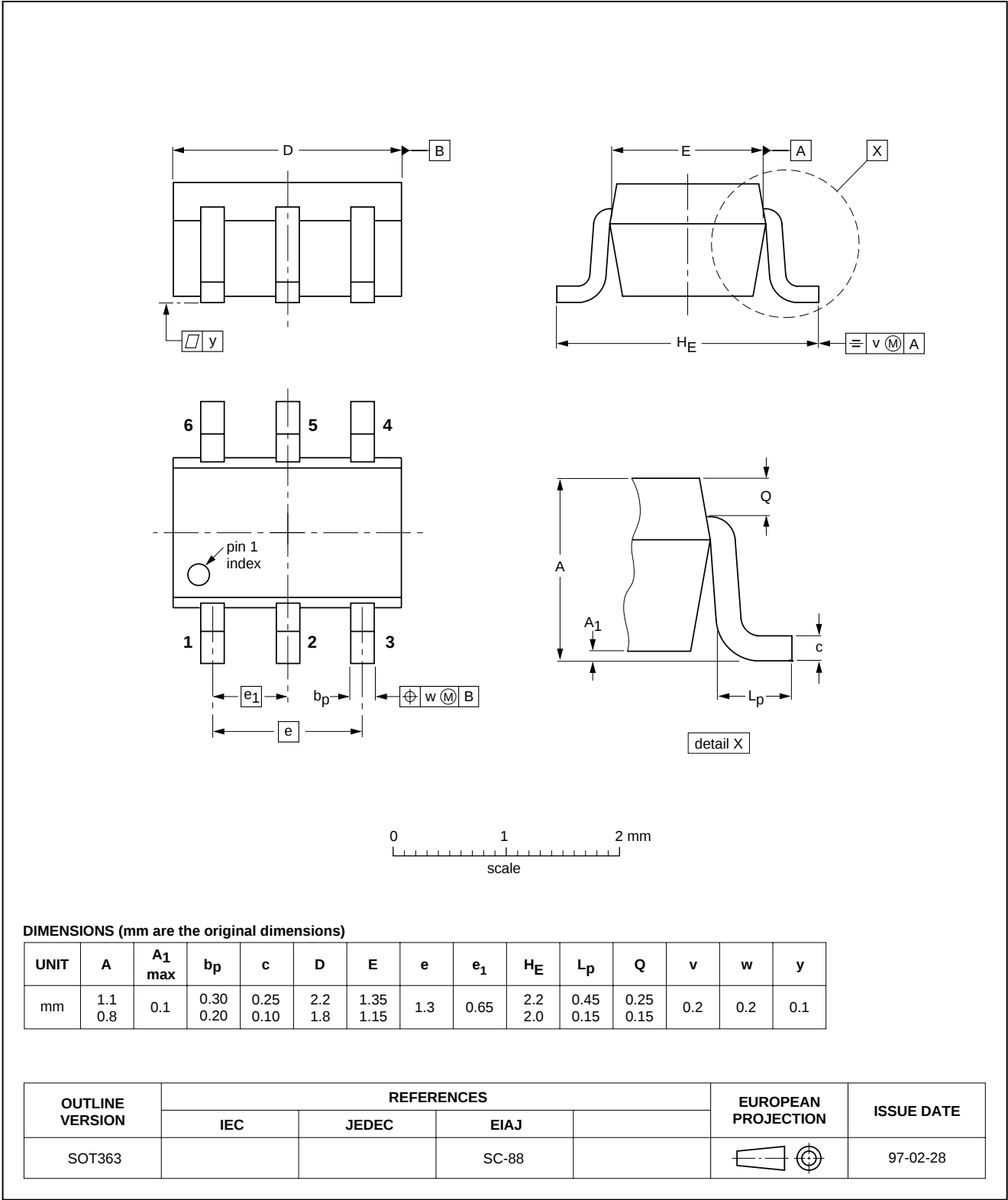


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#### DATA SHEET STATUS

| LEVEL | DATA SHEET STATUS <sup>(1)</sup> | PRODUCT STATUS <sup>(2)(3)</sup> | DEFINITION                                                                                                                                                                                                                                                                                     |
|-------|----------------------------------|----------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| I     | Objective data                   | Development                      | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.                                                                                                    |
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#### DEFINITIONS

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**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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## **Contact information**

For additional information please visit <http://www.semiconductors.philips.com>. Fax: **+31 40 27 24825**

For sales offices addresses send e-mail to: [sales.addresses@www.semiconductors.philips.com](mailto:sales.addresses@www.semiconductors.philips.com).

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Printed in The Netherlands

R75/04/pp8

Date of release: 2003 Oct 15

Document order number: 9397 750 11793

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