10A, 100V Trench Schottky Surface Mount Rectifier

FEATURES

- AEC-Q101 qualified
- Low power loss, high efficiency
- Ideal for automated placement
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

APPLICATIONS

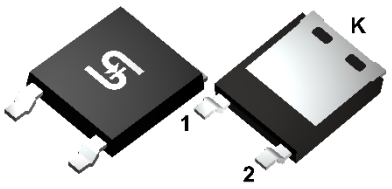
- Low voltage, high frequency
- DC/DC converter
- Freewheeling diodes
- Reverse battery protection
- Car lighting

MECHANICAL DATA

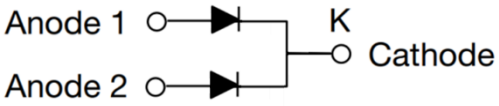
- Case: ThinDPAK
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.19g (approximately)

KEY PARAMETERS		
PARAMETER	**VALUE**	**UNIT**
I_F	10	A
V_{RRM}	100	V
I_{FSM}	135	A
$T_{J\ MAX}$	175	°C
Package	ThinDPAK	
Configuration	Common cathode	

Pb-Free RoHS COMPLIANT HALOGEN FREE ThinDPAK®



ThinDPAK



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)			
PARAMETER	**SYMBOL**	**VALUE**	**UNIT**
Repetitive peak reverse voltage	V_{RRM}	100	V
Reverse voltage, total rms value	$V_{R(RMS)}$	70	V
Forward current, per leg / per device	I_F	5 / 10	A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	135	A
Junction temperature	T_J	- 55 to +175	°C
Storage temperature	T_{STG}	- 55 to +175	°C

THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance per leg(1)	$R_{\Theta JL}$	1.8	°C/W
Junction-to-ambient thermal resistance per leg(2)	$R_{\Theta JA}$	7.4	°C/W
Junction-to-case thermal resistance per leg(2)	$R_{\Theta JC}$	2.9	°C/W

Thermal Performance Note:

1. With ideal heat sink
2. Mounted on Heat sink with 4" x 6" x 0.25" Al-Plate

ELECTRICAL SPECIFICATIONS (T_A = 25°C unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage per leg(1)	I_F = 2.5A, T_J = 25°C	V_F	0.60	-	V
	I_F = 5.0A, T_J = 25°C		0.69	0.76	V
	I_F = 2.5A, T_J = 125°C		0.51	-	V
	I_F = 5.0A, T_J = 125°C		0.58	0.65	V
Reverse current @ rated V_R per leg(2)	T_J = 25°C	I_R	-	10	μA
	T_J = 125°C		-	2	mA
Junction capacitance per leg	1MHz, V_R = 4.0V	C_J	338	-	pF

Notes:

1. Pulse test with PW = 0.3ms
2. Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TSAD10H100CH	ThinDPAK	4,500 / Tape & Reel

CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

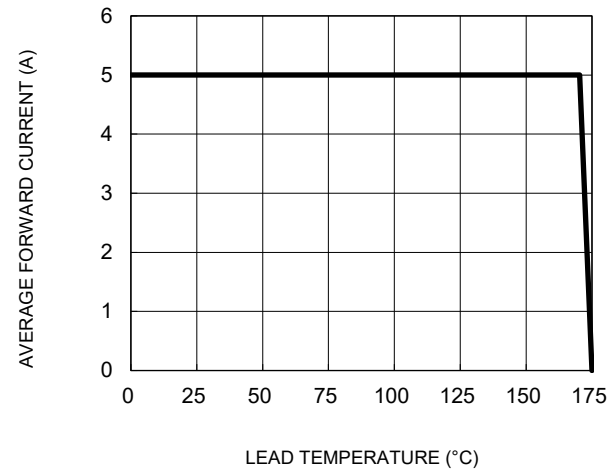


Fig.2 Typical Junction Capacitance

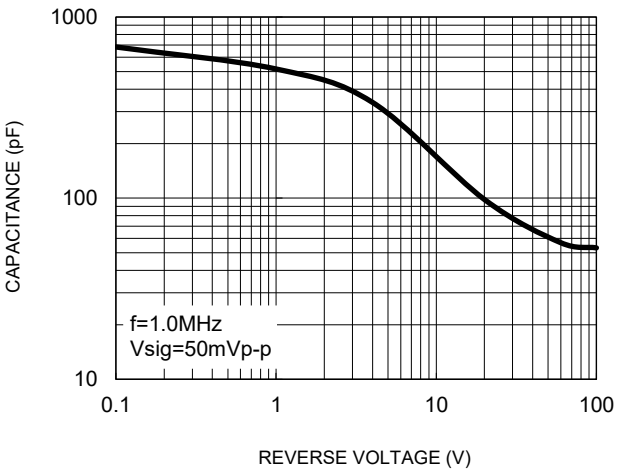


Fig.3 Typical Reverse Characteristics

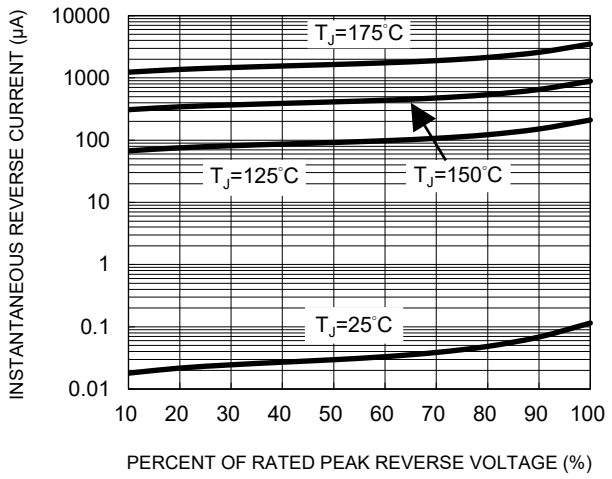


Fig.4 Typical Forward Characteristics

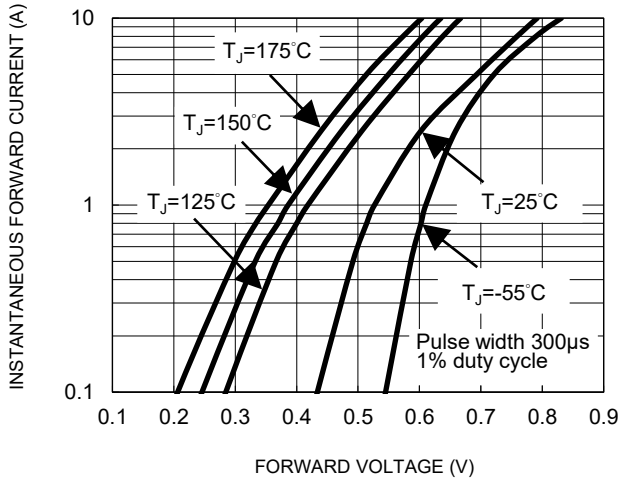
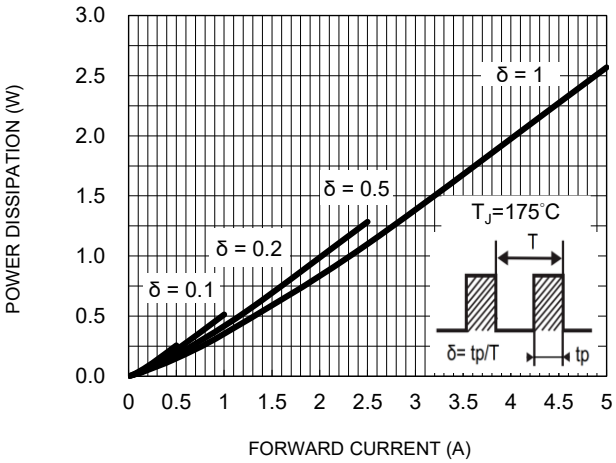


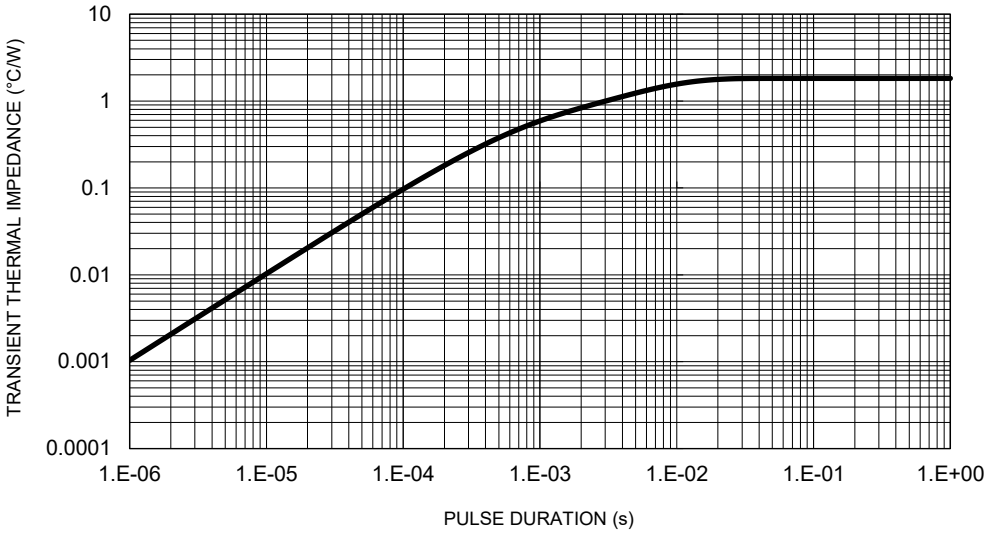
Fig.5 Typical Forward Power Dissipation vs. Forward Current



CHARACTERISTICS CURVES

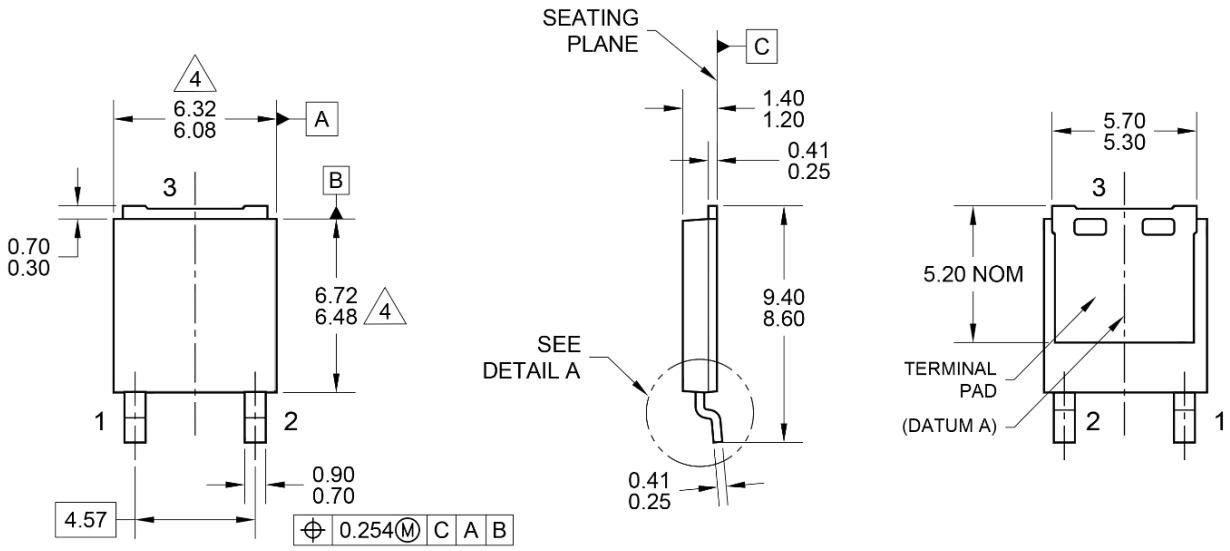
(T_A = 25°C unless otherwise noted)

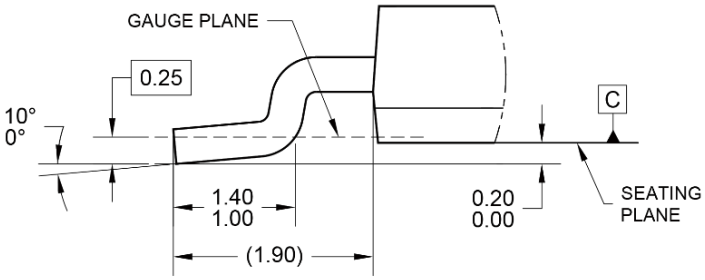
Fig.6 Typical Transient Thermal Characteristics



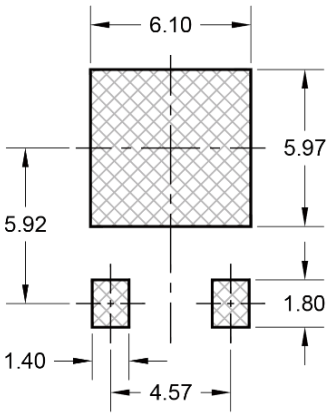
PACKAGE OUTLINE DIMENSIONS

ThinDPAK

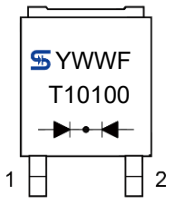




DETAIL A, ROTATED -90°
(SCALE 4:1)



SUGGESTED PAD LAYOUT



MARKING DIAGRAM

YWW = DATE CODE
F = FACTORY CODE

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. PACKAGE OUTLINE REFERENCE: JEDEC TO-252, VARIATION AE, ISSUE F.
4. MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSION, OR GATE BURRS.
5. DWG NO. REF: HQ2SD07-TDPAK-065 REV A.

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