

swissbit®

Product Fact Sheet

Industrial CFast Card

F-78 Series

Industrial Temperature Grade

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Product Summary

- **Capacities:** 32 GBytes, 64 GBytes, 128 GBytes
- **Form Factor:** CFast-Sized Solid State Drive (36.4 mm x 42.8 mm x 3.6 mm)
- **Interface¹:** SATA Gen3 – 6 Gbit/s (Gen2 – 3 Gbit/s and Gen1 – 1.5 Gbit/s backward compatible)
- **Command Sets:** Supports ATA/ATAPI-8 and ACS-4
- **CFast 2.0 compatible**
- **Performance:**
 - Burst Transfer Rate: Up to 600 MBytes/s in SATA Gen3 – 6.0 Gbit/s
 - Read Performance: Sequential Read up to 561 MBytes/s, Random Read 4K up to 72,900 IOPS
 - Write Performance: Sequential Write up to 482 MBytes/s, Random Write 4K up to 77,200 IOPS
- **Operating Temperature Range²:**
 - Industrial: -40 °C to 85 °C
- **Storage Temperature Range:** -40 °C to 85 °C
- **Operating Voltage:** 3.3 V ± 5%
- **Power (128 GBytes) typ:**
 - Read (Active): 1.3W
 - Write (Active): 1.6W
 - Idle: 314mW
 - Slumber: 40mW
 - DEVSLP: 2.7mW
- **Data Retention:** 10 Years @ Life Begin / 1 Year @ Life End
- **Endurance in Disk Writes Per Day (DWPD):**
 - JEDEC Enterprise Workload: up to 94.9
 - JEDEC Client Workload: up to 97.7
- **Shock/Vibration:** 500 *g* / 20 *g*
- **High-Performance Dual Core 32-Bit Processor with Integrated, Parallel Flash Interface Engines:**
 - Triple-Level Cell (TLC) 3D NAND Flash
 - LDPC ECC with up to 165 bit correction per 1 KByte page (BCH equivalent)
- **High Reliability:**
 - Mean Time Between Failure (MTBF): > 2,000,000 hours @ 25 °C
 - Data Reliability: < 1 non-recoverable error per 10¹⁶ bits read
 - 30 µinch Gold-Plated Connector

¹ The verification of host system and storage device compatibility is in customer's responsibility. Swissbit can provide guidance and support on request.

² Adequate airflow is required to ensure the temperature, as reported in the S.M.A.R.T. data, does not exceed 120 °C (industrial temperature drive) and 105 °C (commercial temperature drive) respectively.

1 Product Features

- Pseudo SLC Flash with Reduced Write Amplification for High Endurance
- Dynamic and Static Wear Leveling
- Subpage Mode Flash Translation Layer (FTL)
- Optimized FW algorithms especially for high read access and long data retention applications:
 - Proven power fail management for highest reliability
 - Near Miss ECC technology
Minimize the risk of uncorrectable bit failure over the product life time. Each read command analyzes the ECC margin level and refreshes data if necessary.
 - Read Disturb Management
Read commands are monitored and the data is refreshed when critical numbers are reached.
 - Wear Leveling technology
Equal wear leveling of static and dynamic data. The wear leveling assures that dynamic data as well as static data is balanced evenly across the memory. This guarantees the maximum write endurance of the device.
 - Data Care Management
An interruptible background process controls the user data for read disturb effects or high temperature related retention degradation and refreshes data if necessary.
- Lifetime Enhancements
 - Dynamic Bad Block Remapping
 - Write Amplification Reduction
- TRIM and NCQ Support
- ATA Security Feature Set Support
- DEVSLP Compatible
- In-Field Firmware Update
- Enterprise-Grade Self-Monitoring, Analysis, and Reporting Technology (S.M.A.R.T.)
- End-to-End (E2E) Data Path Protection
- Advanced protection against radiation and soft-errors by SRAM ECC and low-alpha package
- AES-256 encryption
- TCG Opal 2.0 and secure boot feature
- On-die temperature sensor
- Life Cycle Management
- Controlled "Locked" BOM
- RoHS 6 compliance
- Swissbit Device Manager (SBDM) Tool and SDK



Why Swissbit?

Swissbit is focused on the design, development, manufacture, and support of leading edge memory and storage solutions for the worldwide OEM/ODM marketplace. As a global supplier, Swissbit recognizes and addresses the higher level of application requirements of today's industrial, Netcom, and automotive customers by providing best-in-class products and services, with uncompromised attention to driving overall value and quality.