

## Crystal Oscillator (SPXO)

- Package size (2.0 mm × 1.6 mm × 0.63 mm)
- Fundamental mode SPXO
- Output: HCSL
- Reference weight Typ. 7.6 mg

### [ 1 ] Product Number / Product Name / Marking

(1-1) Product Number / Ordering Code

**X1G0062210003xx**

Last 2 digits code(xx) defines Quantity.

The standard is "15", 2 000 pcs/Reel.

(1-2) Product Name / Model Name

**SG2016HGN 100.000000MHz CDHPZA**

### [ 2 ] Operating Range

| Parameter                   | Symbol            | Specifications |      |                 | Unit | Conditions        |
|-----------------------------|-------------------|----------------|------|-----------------|------|-------------------|
|                             |                   | Min.           | Typ. | Max.            |      |                   |
| Supply voltage              | V <sub>CC</sub>   | 3.135          | 3.3  | 3.465           | V    | -                 |
|                             | GND               | 0              | -    | 0               | V    | -                 |
| Input voltage               | V <sub>IN</sub>   | 0              | -    | V <sub>CC</sub> | V    | -                 |
| Operating temperature range | T <sub>use</sub>  | -40            | -    | +105            | °C   | -                 |
| HCSL load condition         | L <sub>HCSL</sub> | -              | 50   | -               | Ω    | Terminated to GND |

### [ 3 ] Frequency Characteristics

(Unless stated otherwise [ 2 ] Operating Range)

| Parameter              | Symbol           | Specifications |            |      | Unit              | Conditions       |
|------------------------|------------------|----------------|------------|------|-------------------|------------------|
|                        |                  | Min.           | Typ.       | Max. |                   |                  |
| Output frequency       | f <sub>o</sub>   | -              | 100.000000 | -    | MHz               | -                |
| Frequency tolerance *1 | f <sub>tol</sub> | -25            | -          | +25  | ×10 <sup>-6</sup> | T <sub>use</sub> |

\*1 Frequency tolerance includes Initial frequency tolerance, Frequency / temperature characteristics, Frequency / voltage coefficient and aging (10 years, +25 °C).

### [ 4 ] Electrical Characteristics

(Unless stated otherwise [ 2 ] Operating Range)

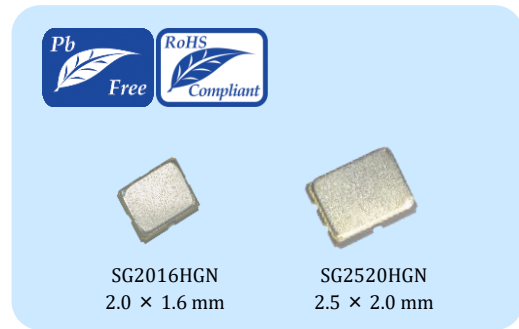
| Parameter                                           | Symbol           | Specifications       |      |                      | Unit | Conditions                                          |
|-----------------------------------------------------|------------------|----------------------|------|----------------------|------|-----------------------------------------------------|
|                                                     |                  | Min.                 | Typ. | Max.                 |      |                                                     |
| Start-up time                                       | t <sub>str</sub> | -                    | -    | 10                   | ms   | t = 0 at 90 % V <sub>CC</sub>                       |
| Current consumption                                 | I <sub>CC</sub>  | -                    | -    | 35                   | mA   |                                                     |
| Disable current                                     | I <sub>dis</sub> | -                    | -    | 25                   | mA   | OE = GND                                            |
| Output voltage                                      | V <sub>OH</sub>  | 0.5                  | -    | 0.7                  | V    |                                                     |
|                                                     | V <sub>OL</sub>  | -0.15                | -    | 0.15                 | V    |                                                     |
| Differential swing                                  | V <sub>SW</sub>  | 0.7                  | -    | 1.4                  | V    | Differential output peak to peak voltage            |
| Rise time / Fall time                               | tr/tf            | -                    | -    | 0.7                  | ns   | 20 % - 80 % of (V <sub>OH</sub> - V <sub>OL</sub> ) |
| Differential output rise slew rate / fall slew rate | Rr/Rf            | 2                    | -    | 10                   | V/ns | Between -0.15 V and 0.15 V of differential output   |
| Symmetry                                            | SYM              | 45                   | 50   | 55                   | %    | At output crossing point                            |
| Input voltage                                       | V <sub>IH</sub>  | 70 % V <sub>CC</sub> | -    | -                    | V    | OE terminal                                         |
|                                                     | V <sub>IL</sub>  | -                    | -    | 30 % V <sub>CC</sub> | V    | OE terminal                                         |
| Output disable time (OE)                            | tstp_oe          | -                    | -    | 100                  | ns   | OE terminal HIGH → LOW                              |
| Output enable time (OE)                             | tsta_oe          | -                    | -    | 500                  | ns   | OE terminal LOW → HIGH                              |
| Phase jitter                                        | t <sub>PJ</sub>  | -                    | -    | 90                   | fs   | Offset freq.: 12 kHz to 20 MHz                      |
| Jitter                                              | t <sub>C-C</sub> | -                    | -    | 60                   | ps   | Cycle to cycle jitter (Peak to Peak)                |
| PCIe jitter limits for CC architecture              | -                | -                    | -    | 0.1                  | ps   | For PCIe Gen5                                       |
|                                                     |                  | -                    | -    | 0.06                 | ps   | For PCIe Gen6                                       |

[ For other general specifications, please refer to the attached Full Data Sheet below ]

## Low Phase Jitter Crystal Oscillator: SG2016HGN/SG2520HGN

### Features

- Crystal oscillator (SPXO)
- Frequency range (fo): 25 MHz to 500 MHz
- Output: HCSL
- Supply voltage: 2.5 V Typ. / 3.3 V Typ.
- Frequency tolerance:  $\pm 25 \times 10^{-6}$  /  $\pm 50 \times 10^{-6}$
- Operating temperature: -40 °C to +105 °C
- Low phase jitter: 60 fs Typ. (fo = 100 MHz)
- PCIe Gen5, 6 jitter specification compliant



### Applications

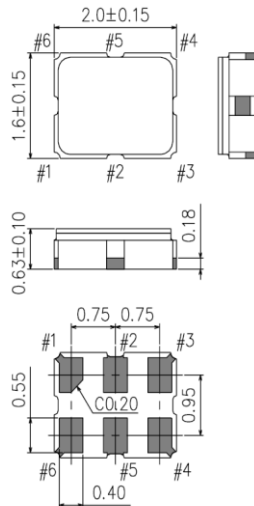
- Communication equipment using PCIe Gen5 or 6 (SSD, Network card, etc)

### Description

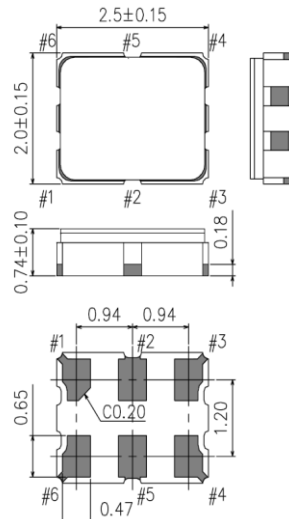
Epson's SG2016HGN / SG2520HGN, SG2016HHN / SG2520HHN product family supports HCSL (High Speed Current Steering Logic) and meets timing compliance jitter requirements up to PCIe Gen6. With 90 ps maximum phase jitter, and supporting frequencies from 25 MHz to 500 MHz, the product group covers the system requirements of most networking, data center and communication applications.

### Outline Drawing and Terminal Assignment

SG2016HGN



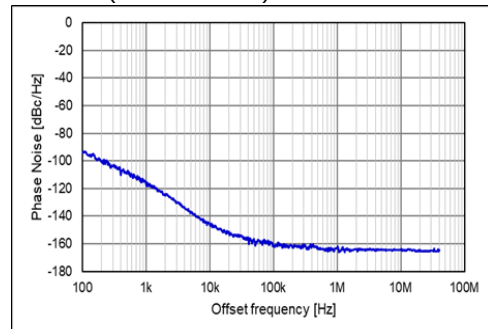
SG2520HGN



| Pin | Connection                      |
|-----|---------------------------------|
| 1   | OE/ST                           |
| 2   | N.C. (Open or V <sub>CC</sub> ) |
| 3   | GND                             |
| 4   | OUT                             |
| 5   | OUT                             |
| 6   | V <sub>CC</sub>                 |

### Typical Performance

Phase Noise (fo = 100 MHz)



Phase Jitter (12 kHz to 20 MHz): 60 fs Typ.

## [ 1 ] Product Number / Product Name

## (1-1) Product Number

SG2016HGN: X1G006221xxxx15

SG2520HGN: X1G005891xxxx15

(Please contact Epson for details)

## (1-2) Product Name (Standard Form)

|                    |                       |          |          |          |          |          |          |
|--------------------|-----------------------|----------|----------|----------|----------|----------|----------|
| <u>SG</u> 2016 HGN | <u>100.000000</u> MHz | <u>C</u> | <u>J</u> | <u>G</u> | <u>P</u> | <u>Z</u> | <u>A</u> |
| ①                  | ②                     | ③        | ④        | ⑤        | ⑥        | ⑦        | ⑧ ⑨      |

① Model

② Output (H: HCSL)

③ Frequency

④ Supply voltage

⑤ Frequency tolerance

⑥ Operating temperature

⑦ Function

⑧ Output disable status

(Z: High impedance)

⑨ Output option

④ Supply voltage

D 2.5 V Typ.

C 3.3 V Typ.

⑤ Frequency tolerance

D  $\pm 25 \times 10^{-6}$ J  $\pm 50 \times 10^{-6}$ 

⑥ Operating temperature

G -40 °C to +85 °C

H -40 °C to +105 °C

⑦ Function

P Output Enable

S Standby

⑨ Output option

A  $V_{SW} = 0.7 \text{ V to } 1.4 \text{ V}$ B  $V_{SW} = 0.8 \text{ V to } 1.6 \text{ V}$ 

## [ 2 ] Absolute Maximum Ratings

| Parameter                 | Symbol    | Specification |      |                | Unit | Conditions                   |
|---------------------------|-----------|---------------|------|----------------|------|------------------------------|
|                           |           | Min.          | Typ. | Max.           |      |                              |
| Maximum supply voltage    | $V_{CC}$  | -0.3          | -    | 4.0            | V    |                              |
| Input voltage             | $V_{in}$  | -0.3          | -    | $V_{CC} + 0.5$ | V    | OE/ $\overline{ST}$ terminal |
| Storage temperature range | $T_{stg}$ | -55           | -    | +125           | °C   |                              |

## [ 3 ] Operating Range

| Parameter                   | Symbol     | Specification |      |       | Unit     | Conditions |
|-----------------------------|------------|---------------|------|-------|----------|------------|
|                             |            | Min.          | Typ. | Max.  |          |            |
| Supply voltage              | $V_{CC}$   | 2.375         | 2.5  | 2.625 | V        |            |
|                             |            | 3.135         | 3.3  | 3.465 | V        |            |
| Supply voltage              | GND        | 0.0           | 0.0  | 0.0   | V        |            |
| Operating temperature range | $T_{use}$  | -40           | +25  | +85   | °C       |            |
|                             |            | -40           | +25  | +105  | °C       |            |
| HCSL load condition         | $L_{HCSL}$ | 50            |      |       | $\Omega$ |            |

\* Power supply startup time (0 % $V_{CC}$ →90 % $V_{CC}$ ) should be more than 150  $\mu$ s\* A 0.1  $\mu$ F and a 10  $\mu$ F bypass capacitor should be connected between  $V_{CC}$  and GND pins located close to the device

## [ 4 ] Frequency Characteristics (Unless stated otherwise [ 3 ] Operating Range)

| Parameter              | Symbol | Specification |      |      | Unit             | Conditions |
|------------------------|--------|---------------|------|------|------------------|------------|
|                        |        | Min.          | Typ. | Max. |                  |            |
| Output frequency *1    | fo     | 25            | -    | 500  | MHz              |            |
| Frequency tolerance *2 | f_tol  | -25           | -    | +25  | $\times 10^{-6}$ |            |
|                        |        | -50           | -    | +50  | $\times 10^{-6}$ |            |

\*1 Please contact Epson for available frequencies

\*2 Frequency tolerance includes Initial frequency tolerance, Frequency / temperature characteristics, Frequency / voltage coefficient and aging (10 years, +25 °C).

\* Aging is estimated from environmental reliability tests; expected amount of the frequency variation. This does not intend to guarantee the product-life cycle.

## [ 5 ] Electrical Characteristics (Unless stated otherwise [ 3 ] Operating Range)

| Parameter                                                                                         | Symbol           | Specification        |      |                      | Unit | Conditions                                                 |
|---------------------------------------------------------------------------------------------------|------------------|----------------------|------|----------------------|------|------------------------------------------------------------|
|                                                                                                   |                  | Min.                 | Typ. | Max.                 |      |                                                            |
| Startup time                                                                                      | t_str            | -                    | -    | 10                   | ms   | t = 0 at 90 % V <sub>CC</sub>                              |
| Current consumption                                                                               | I <sub>CC</sub>  | -                    | -    | 35                   | mA   | fo ≤ 212 MHz                                               |
|                                                                                                   |                  | -                    | -    | 40                   | mA   | fo > 212 MHz                                               |
| Disable current                                                                                   | I_dis            | -                    | -    | 25                   | mA   | OE = GND                                                   |
| Stand-by current                                                                                  | I_std            | -                    | -    | 30                   | μA   | ST = GND, T_use Max. = +85 °C                              |
|                                                                                                   |                  | -                    | -    | 60                   | μA   | ST = GND, T_use Max. = +105 °C                             |
| Rise time / Fall time                                                                             | tr / tf          | -                    | -    | 0.7                  | ns   | 20 % - 80 % (V <sub>OH</sub> - V <sub>OL</sub> )           |
| Differential output rise slew rate / fall slew rate                                               | Rr / Rf          | 2                    | -    | 10                   | V/ns | Between -0.15 V and 0.15 V of differential output          |
| Symmetry                                                                                          | SYM              | 45                   | 50   | 55                   | %    | At output crossing point                                   |
| Output voltage                                                                                    | V <sub>OH</sub>  | 0.5                  | -    | 0.7                  | V    | Output option: A, fo ≤ 212 MHz                             |
|                                                                                                   |                  | 0.4                  | -    | 0.65                 | V    | Output option: A, fo > 212 MHz                             |
|                                                                                                   |                  | 0.6                  | -    | 0.8                  | V    | Output option: B, fo ≤ 212 MHz                             |
|                                                                                                   |                  | 0.5                  | -    | 0.75                 | V    | Output option: B, fo > 212 MHz                             |
|                                                                                                   | V <sub>OL</sub>  | -0.15                | -    | 0.15                 | V    |                                                            |
| Differential swing                                                                                | V <sub>SW</sub>  | 0.7                  | -    | 1.4                  | V    | Output option: A                                           |
|                                                                                                   |                  | 0.8                  | -    | 1.6                  | V    | Output option: B                                           |
| Input voltage                                                                                     | V <sub>IH</sub>  | 70 % V <sub>CC</sub> | -    | -                    | V    | OE/ST terminal                                             |
|                                                                                                   | V <sub>IL</sub>  | -                    | -    | 30 % V <sub>CC</sub> | V    |                                                            |
| Output disable time (OE)                                                                          | tstp_oe          | -                    | -    | 100                  | ns   | Measured from the time OE pin crosses 30 % V <sub>CC</sub> |
| Output disable time (ST)                                                                          | tstp_st          | -                    | -    | 100                  | ns   | Measured from the time ST pin crosses 30 % V <sub>CC</sub> |
| Output enable time (OE)                                                                           | tsta_oe          | -                    | -    | 500                  | ns   | Measured from the time OE pin crosses 70 % V <sub>CC</sub> |
| Output enable time (ST)                                                                           | tsta_st          | -                    | -    | 10                   | ms   | Measured from the time ST pin crosses 70 % V <sub>CC</sub> |
| Phase jitter<br>Offset frequency<br>fo < 50 MHz: 12 kHz to 5 MHz<br>fo ≥ 50 MHz: 12 kHz to 20 MHz | t <sub>pj</sub>  | -                    | -    | 200                  | fs   | fo < 100 MHz                                               |
|                                                                                                   |                  | -                    | -    | 90                   | fs   | 100 MHz ≤ fo ≤ 156 MHz                                     |
|                                                                                                   |                  | -                    | -    | 70                   | fs   | 156 MHz < fo ≤ 212 MHz                                     |
|                                                                                                   |                  | -                    | -    | 60                   | fs   | 212 MHz < fo ≤ 391 MHz                                     |
|                                                                                                   |                  | -                    | -    | 50                   | fs   | fo > 391 MHz                                               |
| Jitter                                                                                            | t <sub>p-p</sub> | -                    | -    | 60                   | ps   | Peak to Peak Jitter                                        |
|                                                                                                   | t <sub>c-c</sub> | -                    | -    | 60                   | ps   | Cycle to cycle jitter (Peak to Peak)                       |
| PCIe jitter limits for CC architecture                                                            | -                | -                    | -    | 0.1                  | ps   | For PCIe Gen5                                              |
|                                                                                                   |                  | -                    | -    | 0.06                 | ps   | For PCIe Gen6                                              |

## [ 6 ] Thermal resistance (For reference only)

| Parameter            | Symbol          | Specification |      |      | Unit | Conditions |
|----------------------|-----------------|---------------|------|------|------|------------|
|                      |                 | Min.          | Typ. | Max. |      |            |
| Junction temperature | Tj              | -             | -    | +140 | °C   |            |
| Junction to case     | θ <sub>jc</sub> | -             | 114  | -    | °C/W | SG2016HGN  |
|                      |                 | -             | 122  | -    | °C/W | SG2520HGN  |
| Junction to ambient  | θ <sub>ja</sub> | -             | 243  | -    | °C/W | SG2016HGN  |
|                      |                 | -             | 155  | -    | °C/W | SG2520HGN  |

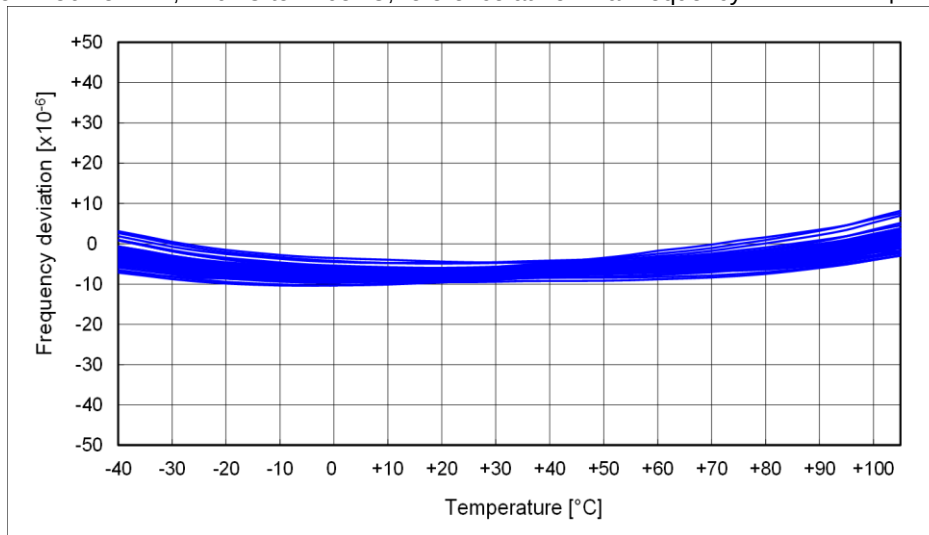
## [ 7 ] Typical Performance Characteristics (For reference only)

The following data shows typical performance characteristics

## (7-1) Frequency / Temperature Characteristics

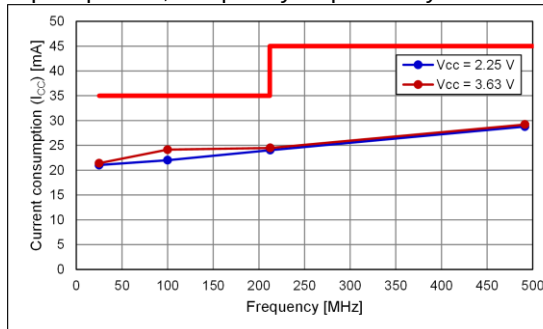
 $f_0 = 156.25$  MHz,  $-40$  °C to  $+105$  °C, reference at nominal frequency

n = 50 pcs

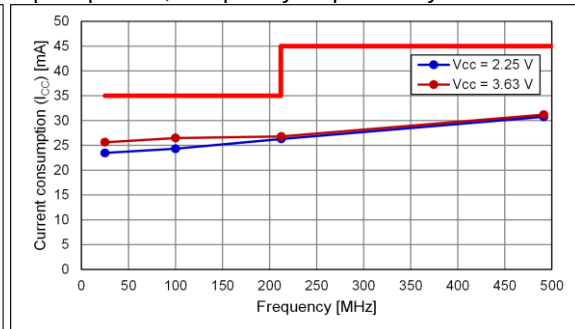
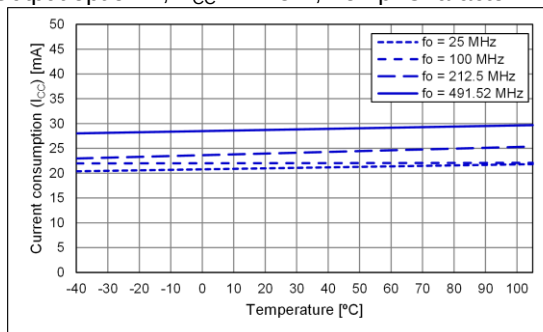
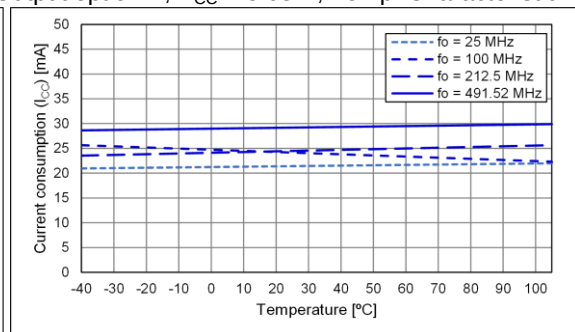
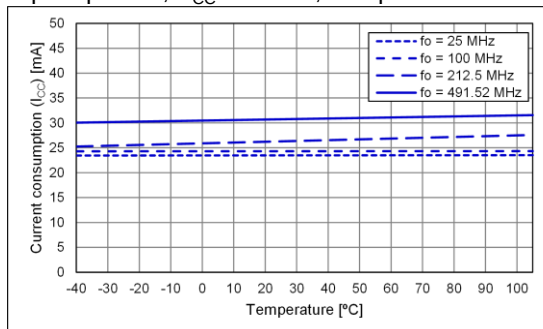
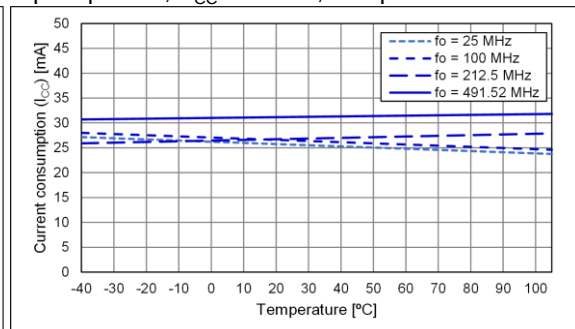


## (7-2) Current Consumption

Output option:A, Frequency Dependency



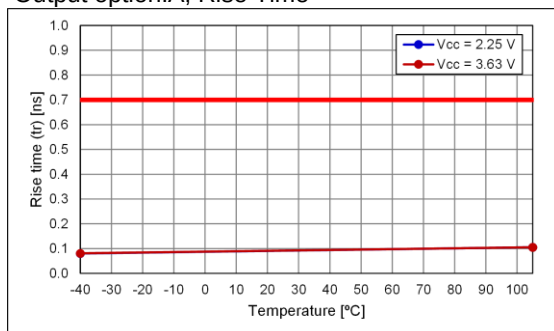
Output option:B, Frequency Dependency

Output option:A,  $V_{CC} = 2.25$  V, Temp. CharacteOutput option:A,  $V_{CC} = 3.63$  V, Temp. CharacteristicOutput option:B,  $V_{CC} = 2.25$  V, Temp. CharacteOutput option:B,  $V_{CC} = 3.63$  V, Temp. Characteristic

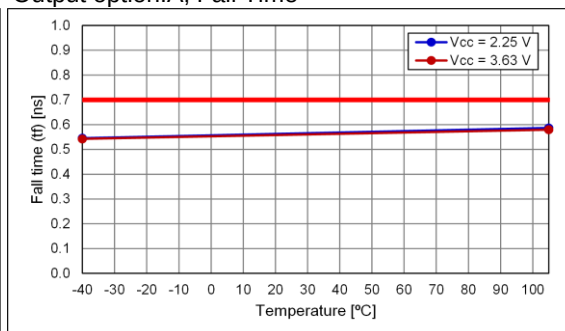
## (7-3) Rise Time / Fall Time Temperature Characteristic

 $f_o = 25 \text{ MHz}$ 

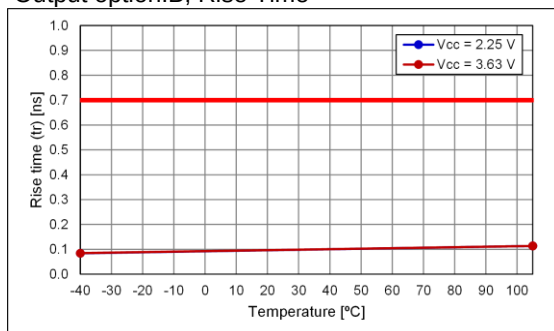
Output option:A, Rise Time



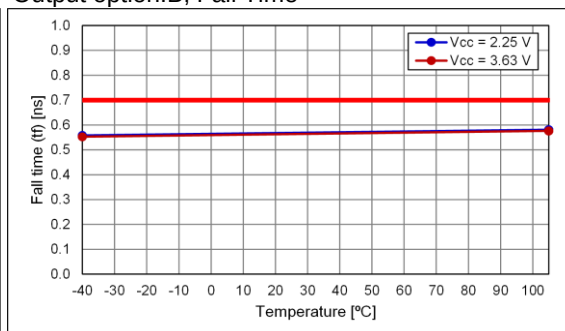
Output option:A, Fall Time



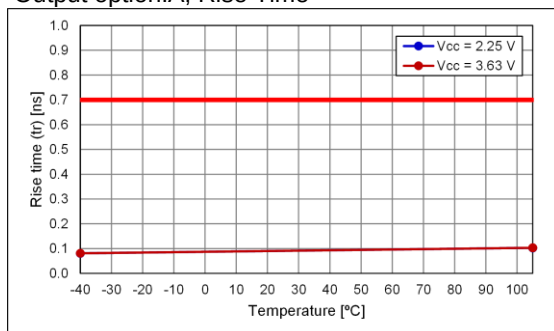
Output option:B, Rise Time



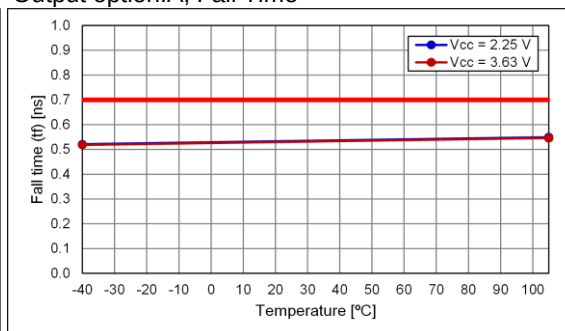
Output option:B, Fall Time

 $f_o = 100 \text{ MHz}$ 

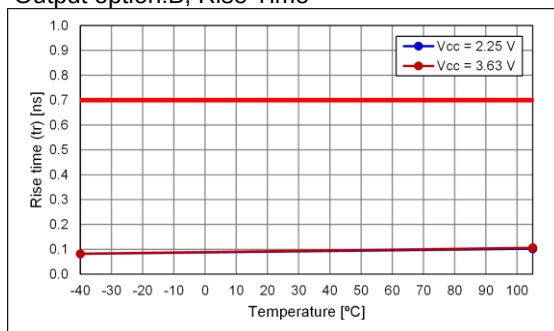
Output option:A, Rise Time



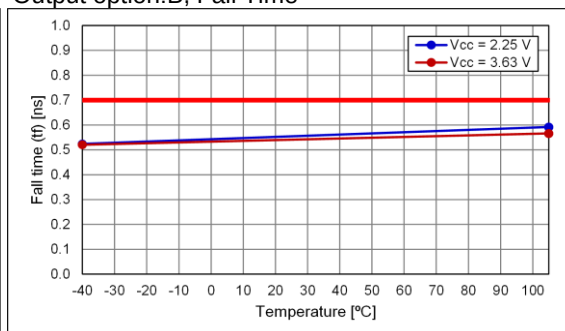
Output option:A, Fall Time



Output option:B, Rise Time

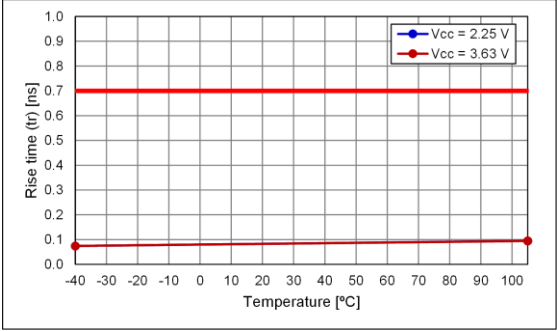


Output option:B, Fall Time

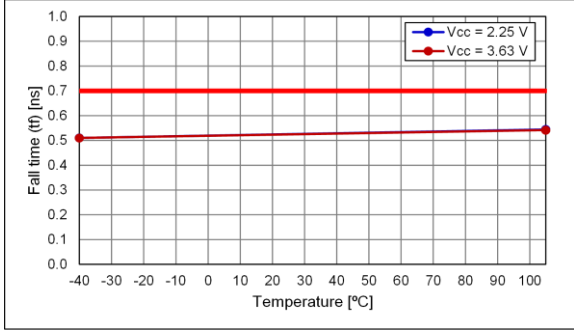


fo = 212.5 MHz

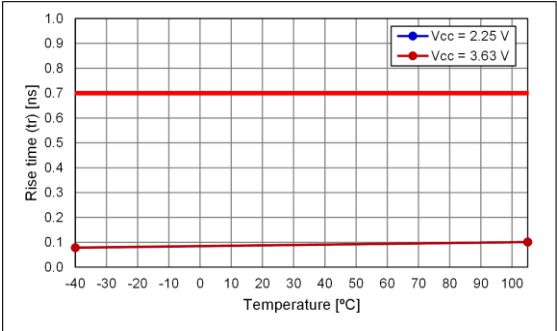
Output option:A, Rise Time



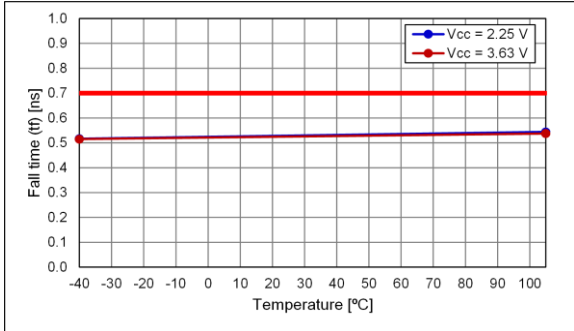
Output option:A, Fall Time



Output option:B, Rise Time

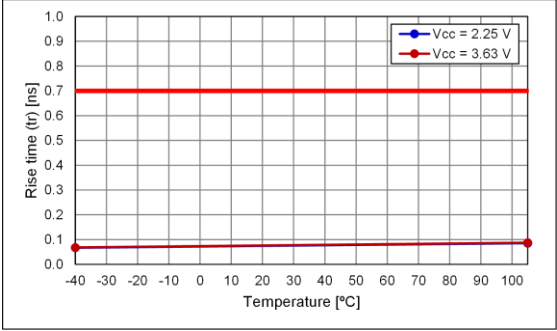


Output option:B, Fall Time

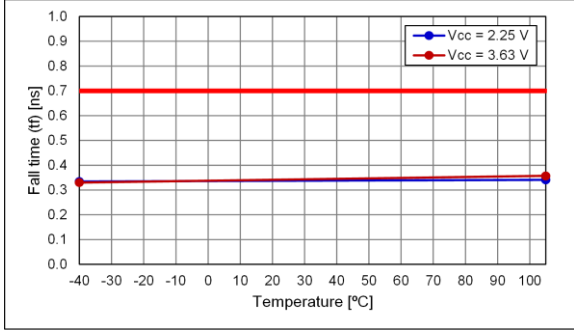


fo = 491.52 MHz

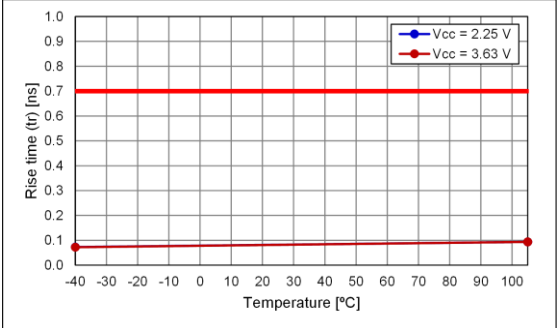
Output option:A, Rise Time



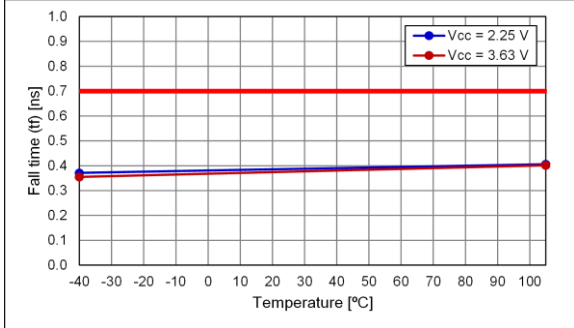
Output option:A, Fall Time



Output option:B, Rise Time



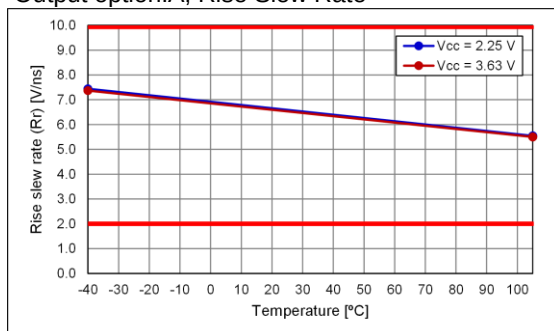
Output option:B, Fall Time



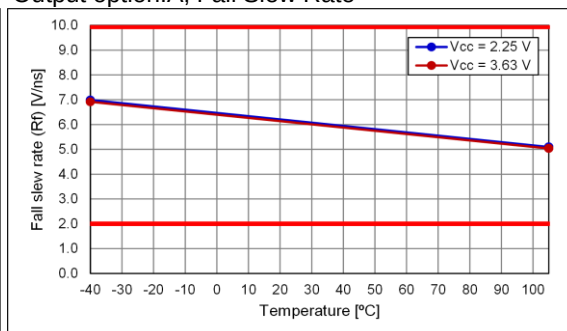
## (7-4) Differential Output Rise Slew Rate / Fall Slew Rate Temperature Characteristic

fo = 25 MHz

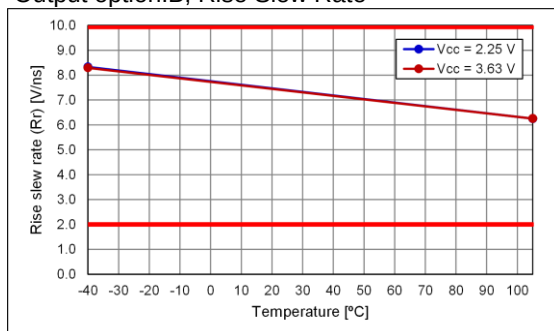
Output option:A, Rise Slew Rate



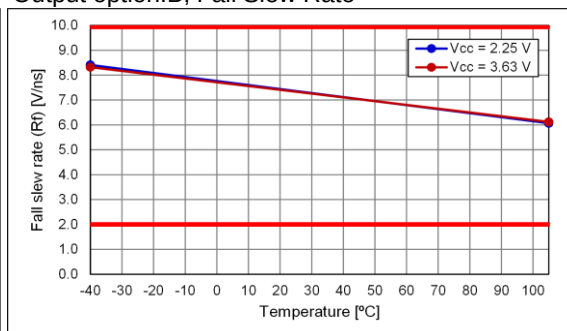
Output option:A, Fall Slew Rate



Output option:B, Rise Slew Rate

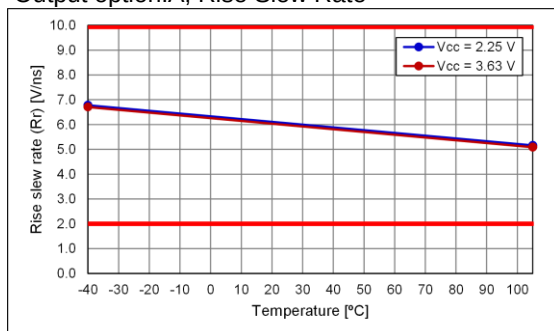


Output option:B, Fall Slew Rate

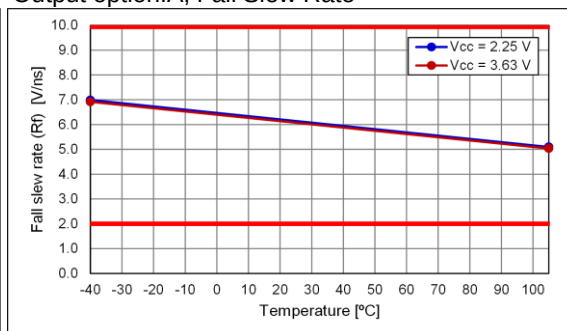


fo = 100 MHz

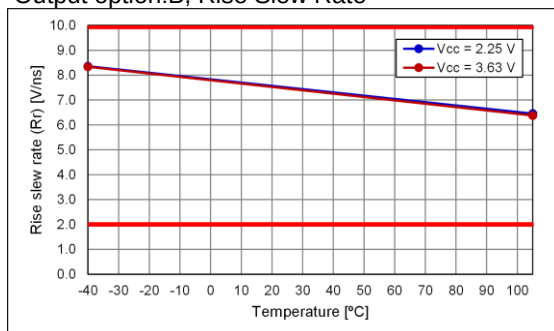
Output option:A, Rise Slew Rate



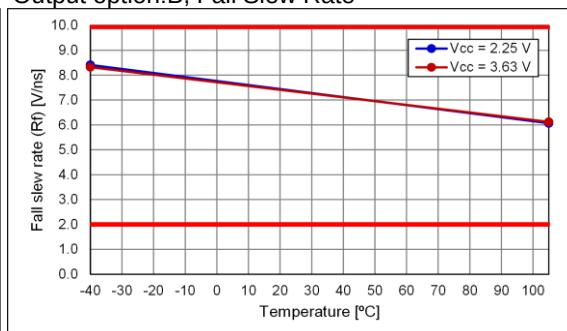
Output option:A, Fall Slew Rate



Output option:B, Rise Slew Rate



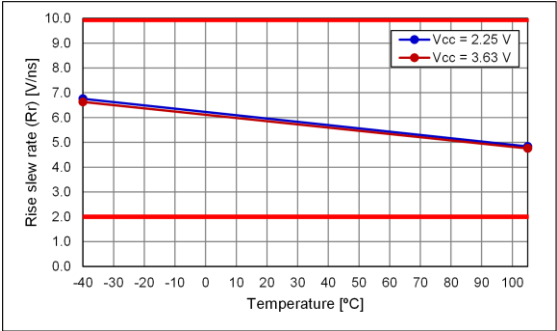
Output option:B, Fall Slew Rate



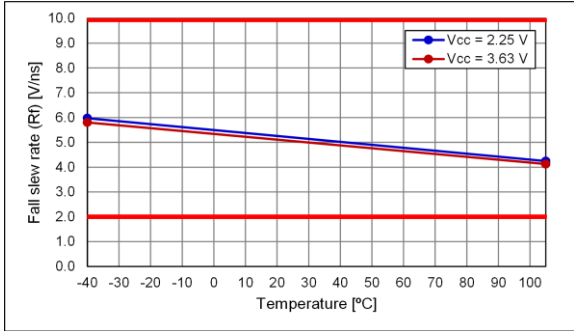


fo = 212.5 MHz

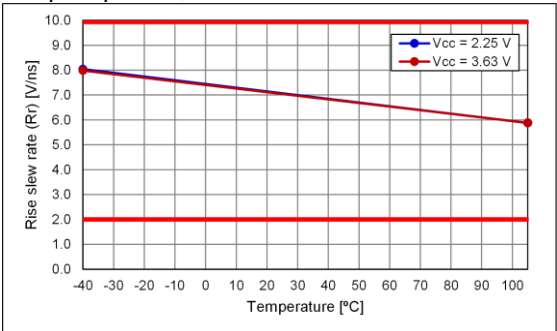
Output option:A, Rise Slew Rate



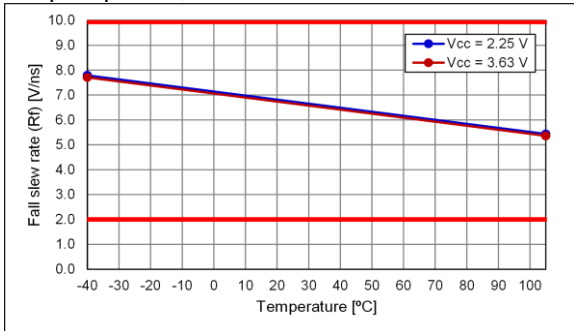
Output option:A, Fall Slew Rate



Output option:B, Rise Slew Rate

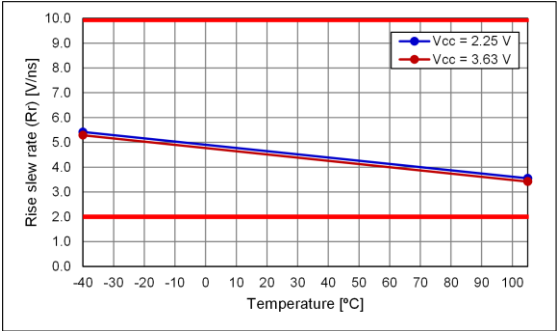


Output option:B, Fall Slew Rate

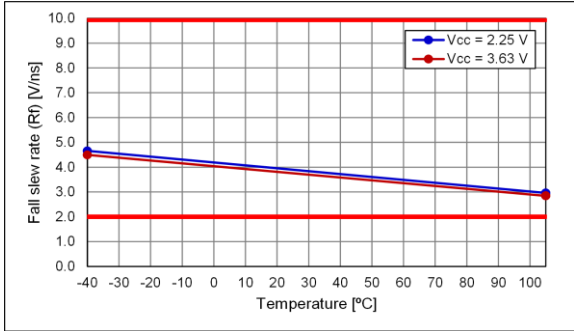


fo = 491.52 MHz

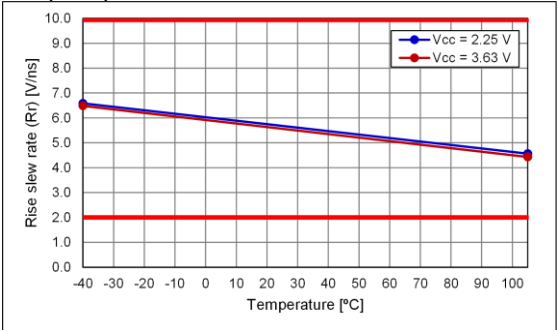
Output option:A, Rise Slew Rate



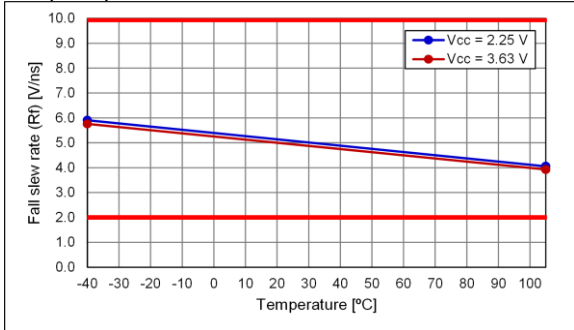
Output option:A, Fall Slew Rate



Output option:B, Rise Slew Rate



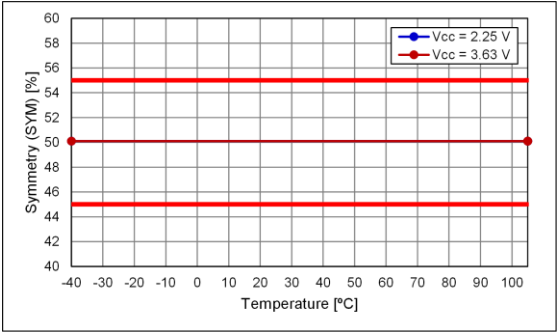
Output option:B, Fall Slew Rate



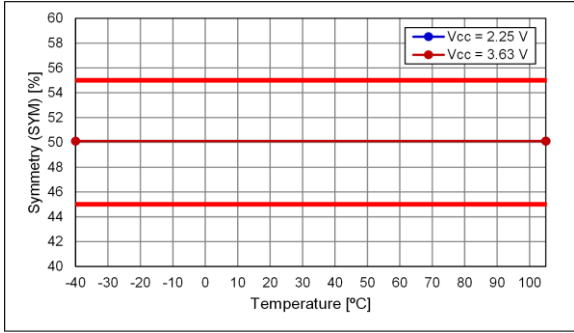
(7-5) Symmetry Temperature Characteristic

fo = 25 MHz

Output option:A

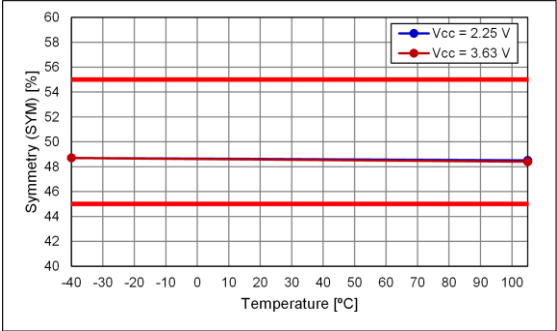


Output option:B

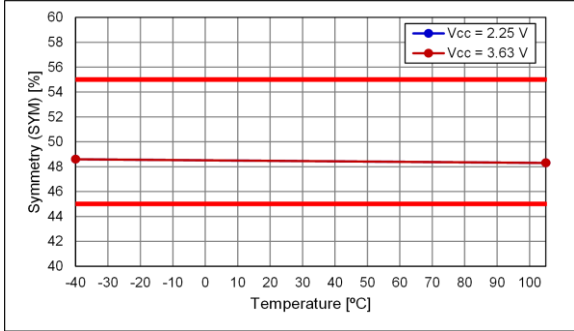


fo = 100 MHz

Output option:A

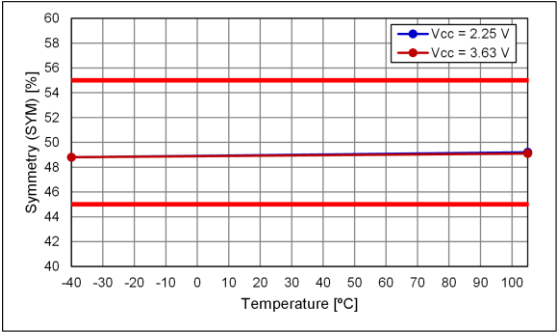


Output option:B

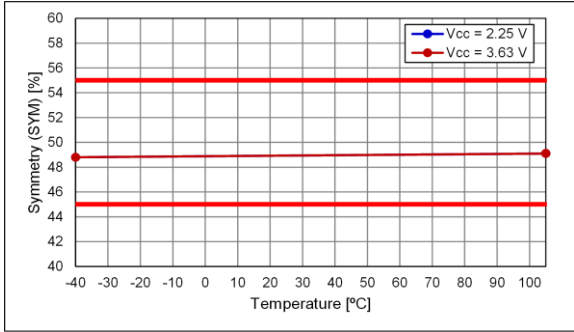


fo = 212.5 MHz

Output option:A

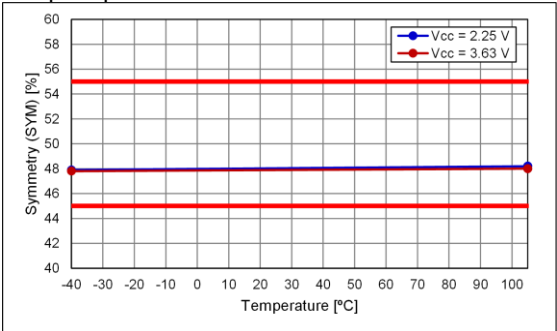


Output option:B

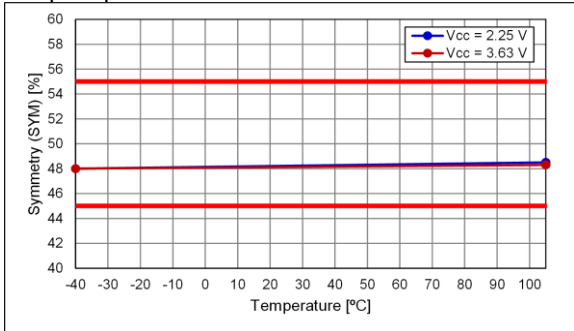


fo = 491.52 MHz

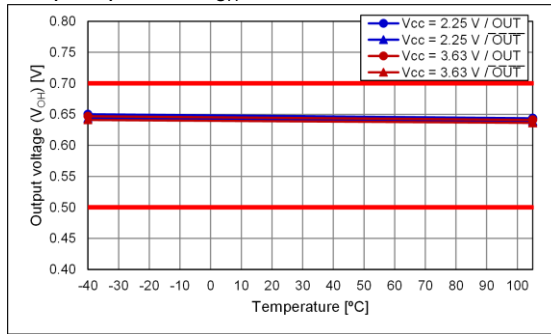
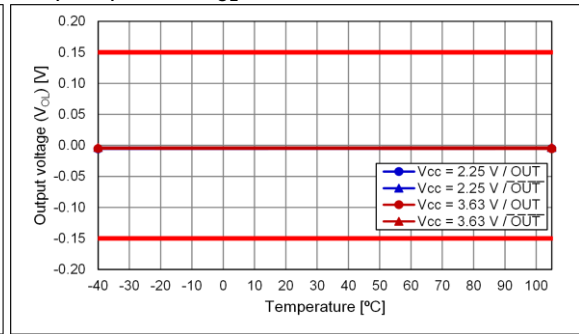
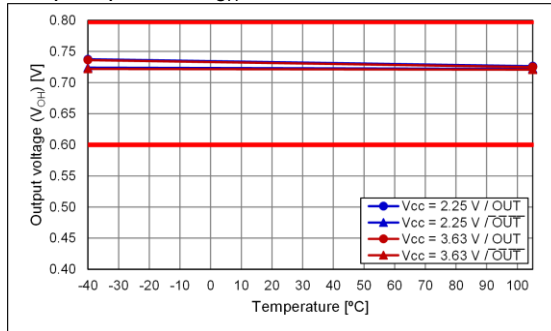
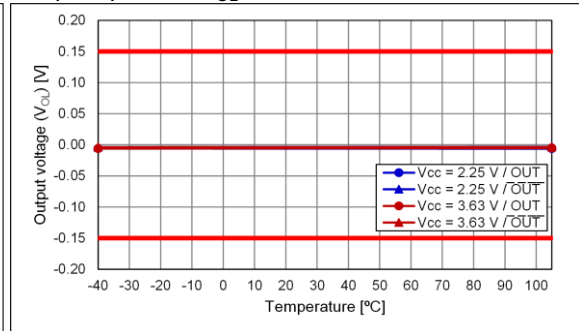
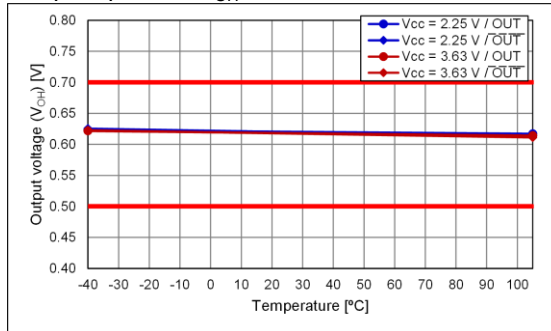
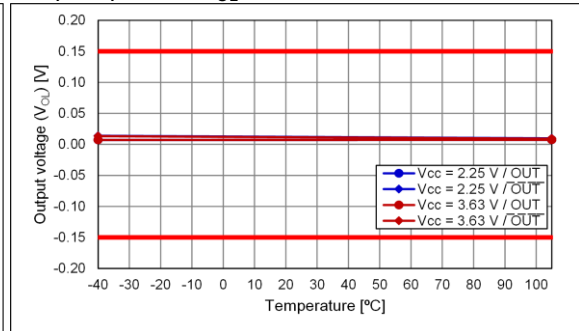
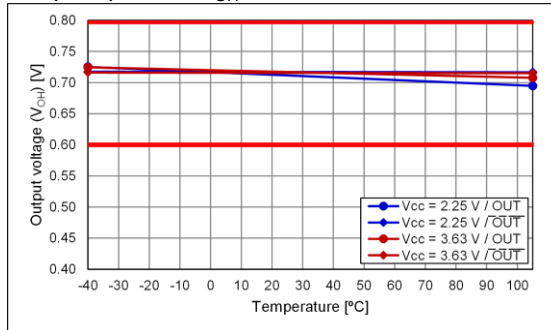
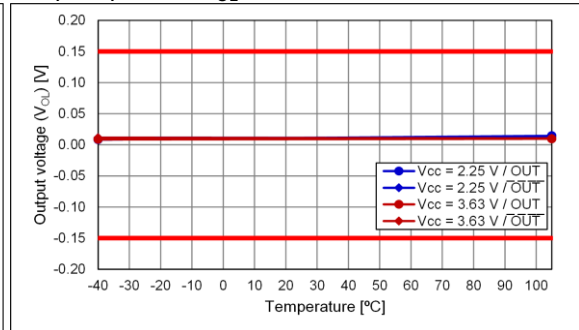
Output option:A



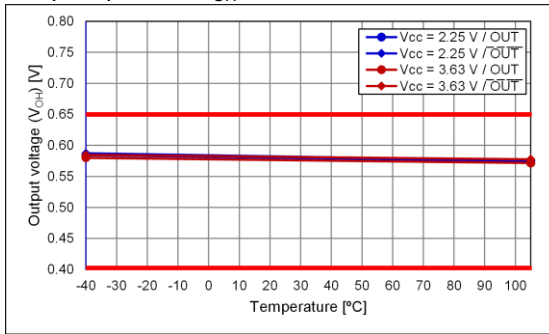
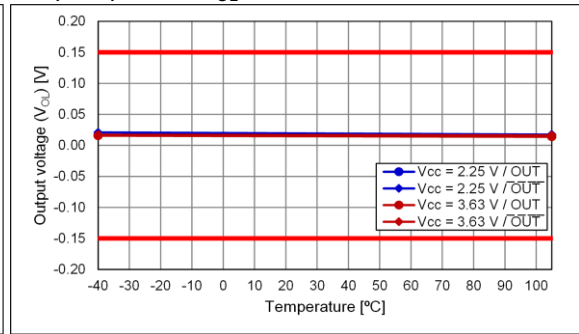
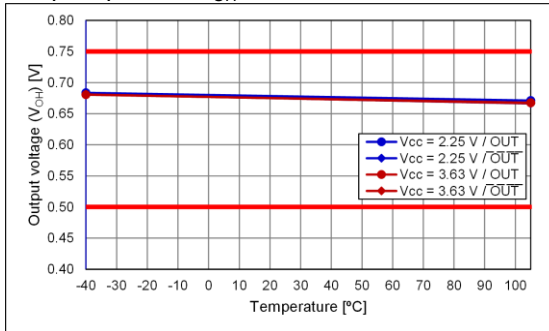
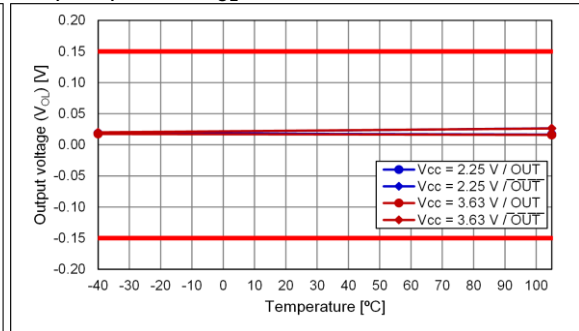
Output option:B



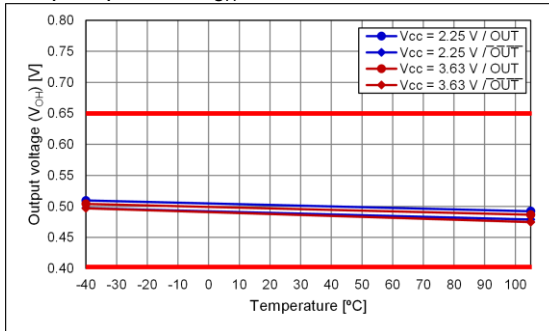
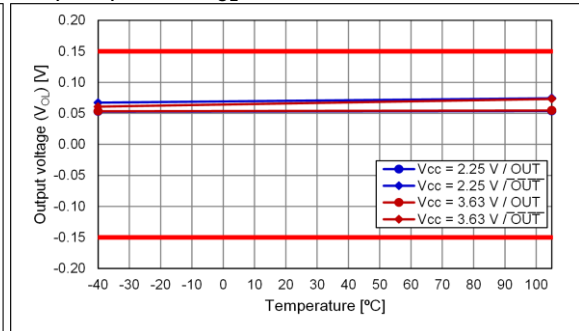
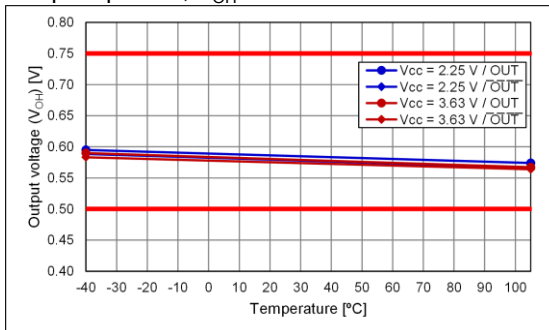
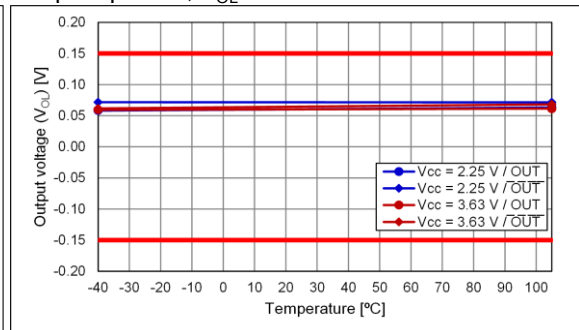
## (7-6) Output Voltage Temperature Characteristic

 $f_o = 25 \text{ MHz}$ Output option:A,  $V_{OH}$ Output option:A,  $V_{OL}$ Output option:B,  $V_{OH}$ Output option:B,  $V_{OL}$  $f_o = 100 \text{ MHz}$ Output option:A,  $V_{OH}$ Output option:A,  $V_{OL}$ Output option:B,  $V_{OH}$ Output option:B,  $V_{OL}$ 

fo = 212.5 MHz

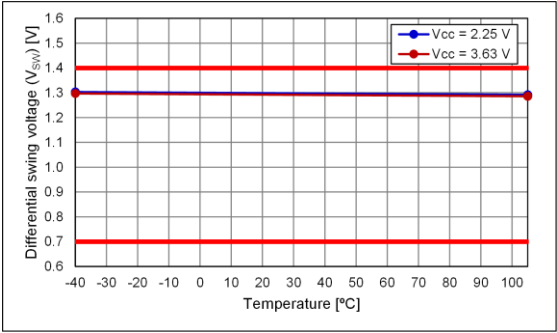
Output option:A,  $V_{OH}$ Output option:A,  $V_{OL}$ Output option:B,  $V_{OH}$ Output option:B,  $V_{OL}$ 

fo = 491.52 MHz

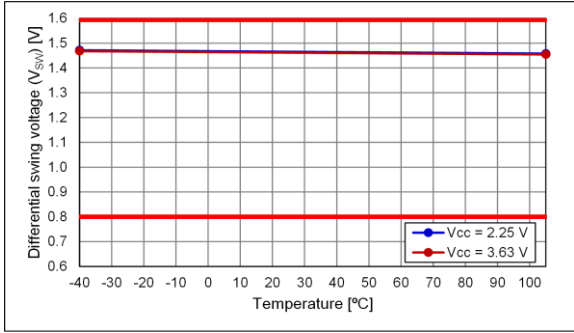
Output option:A,  $V_{OH}$ Output option:A,  $V_{OL}$ Output option:B,  $V_{OH}$ Output option:B,  $V_{OL}$ 

(7-7) Differential Swing Temperature Characteristic  
fo = 25 MHz

Output option:A

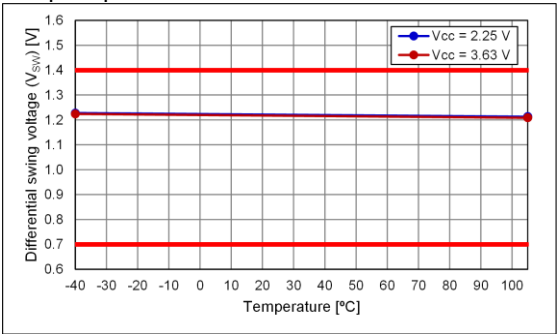


Output option:B

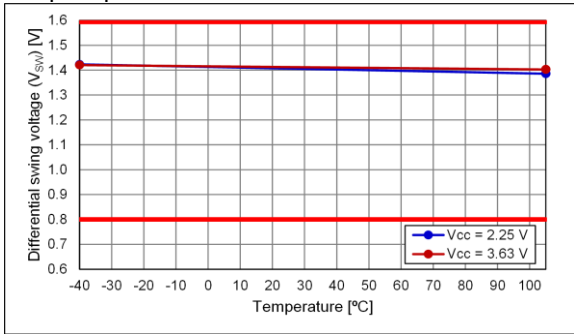


fo = 100 MHz

Output option:A

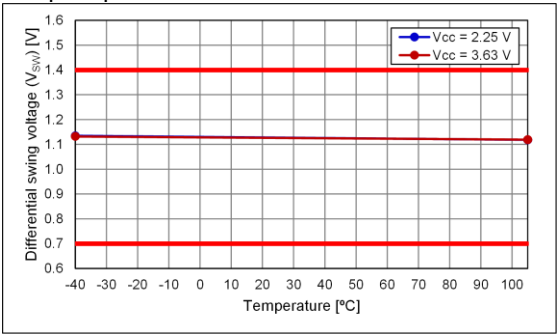


Output option:B

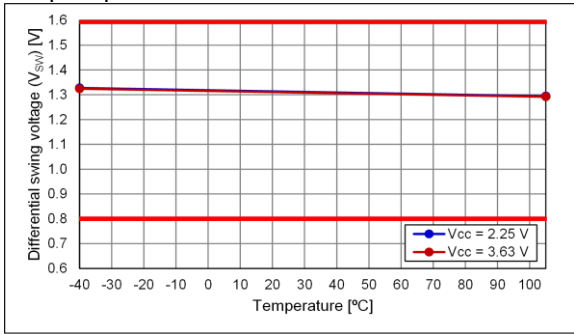


fo = 212.5 MHz

Output option:A

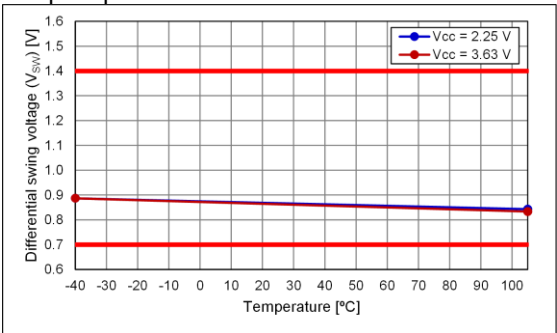


Output option:B

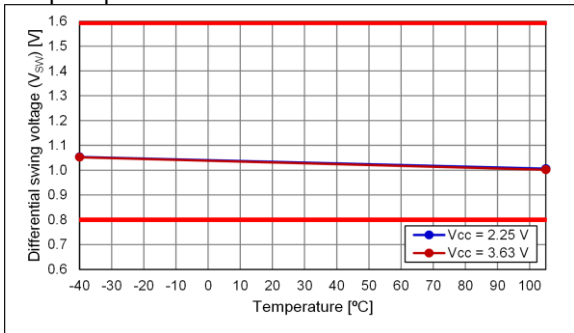


fo = 491.52 MHz

Output option:A

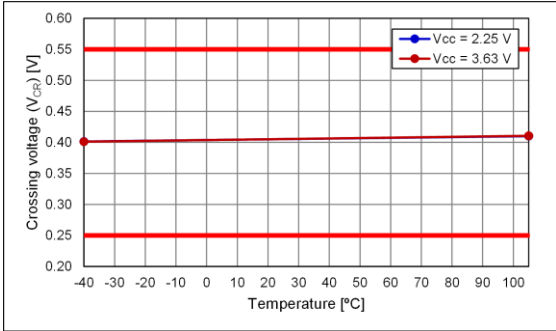


Output option:B

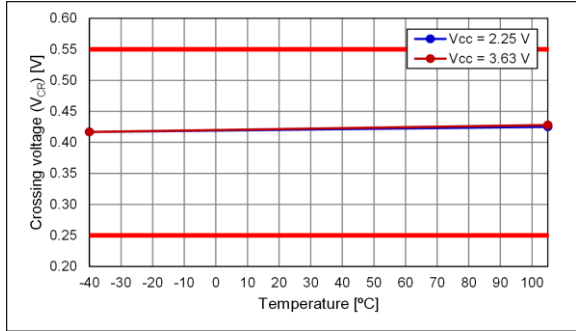


(7-8) Crossing Voltage Temperature Characteristic  
fo = 25 MHz

Output option:A

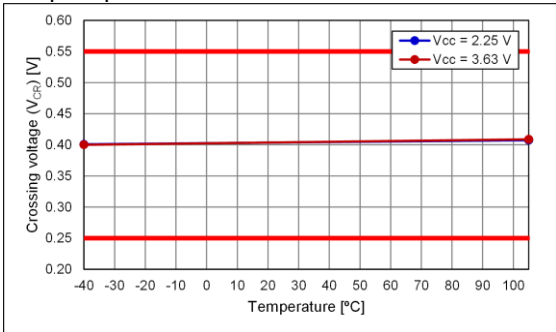


Output option:B

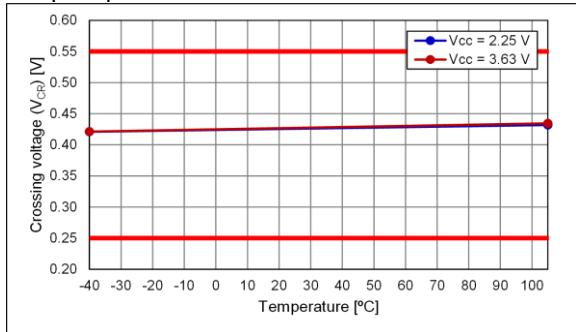


fo = 100 MHz

Output option:A

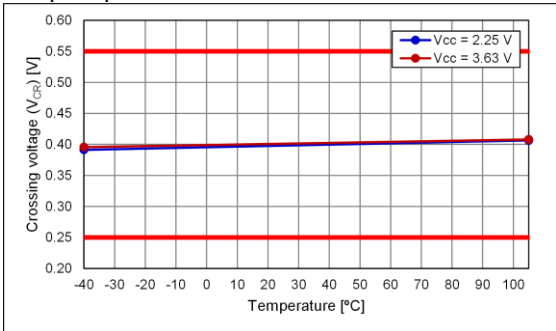


Output option:B

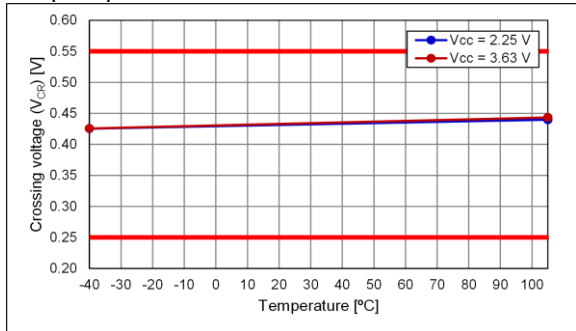


fo = 212.5 MHz

Output option:A

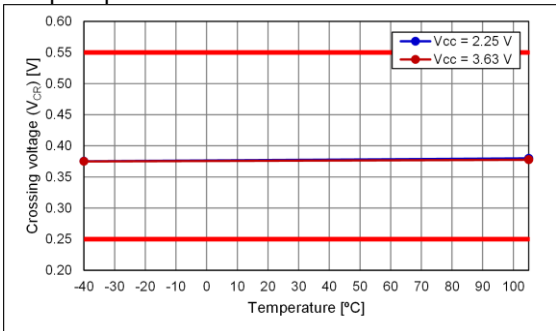


Output option:B

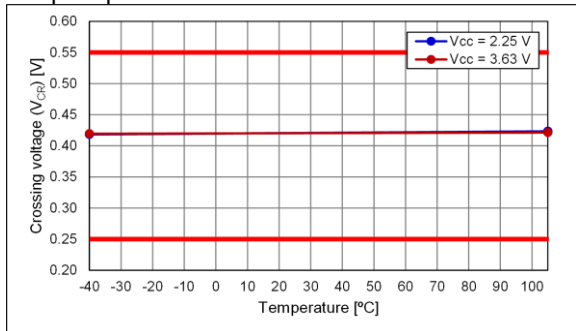


fo = 491.52 MHz

Output option:A



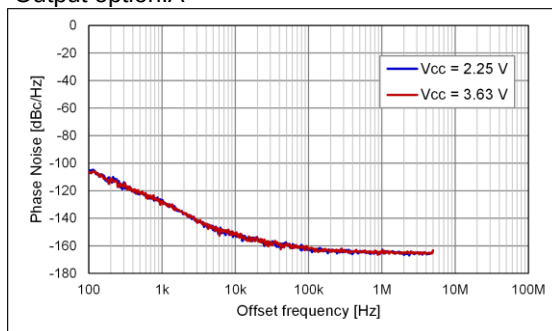
Output option:B



## (7-9) Phase Noise and Phase Jitter

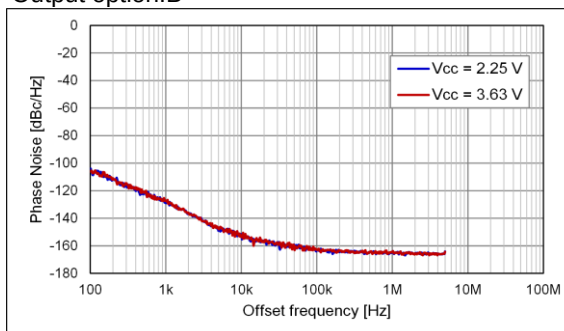
fo = 25 MHz

Output option:A



| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 116 fs        |
| 3.63 V          | 117 fs        |

Output option:B

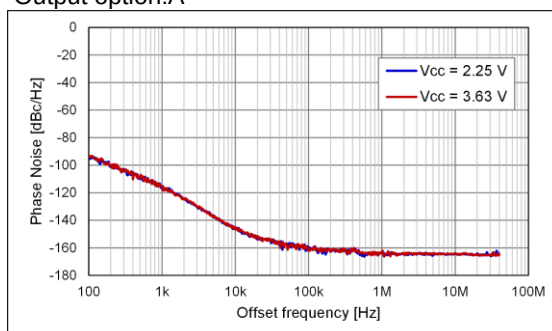


| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 110 fs        |
| 3.63 V          | 110 fs        |

\* Offset frequency: 12 kHz to 5 MHz

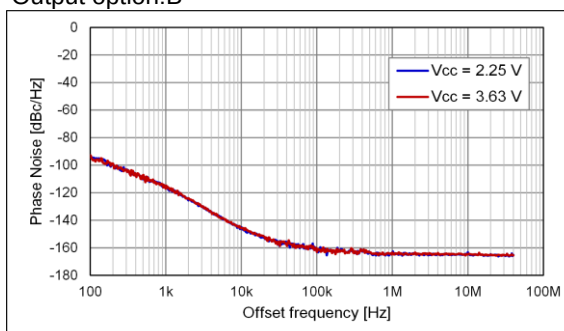
fo = 100 MHz

Output option:A



| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 60 fs         |
| 3.63 V          | 61 fs         |

Output option:B

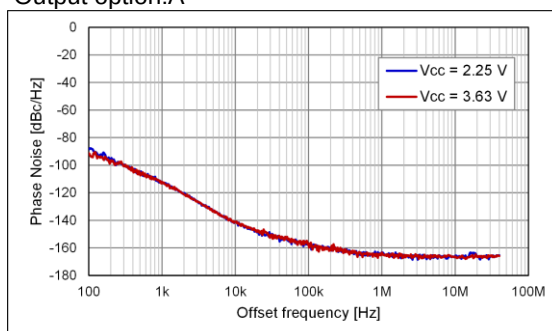


| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 58 fs         |
| 3.63 V          | 59 fs         |

\* Offset frequency: 12 kHz to 20 MHz

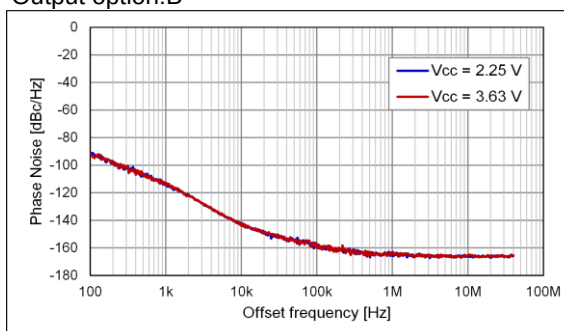
fo = 212.5 MHz

Output option:A



| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 26 fs         |
| 3.63 V          | 25 fs         |

Output option:B

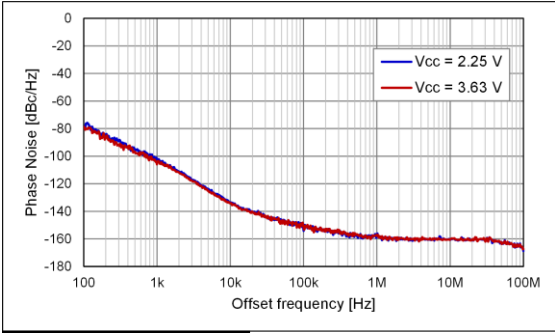


| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 25 fs         |
| 3.63 V          | 25 fs         |

\* Offset frequency: 12 kHz to 20 MHz

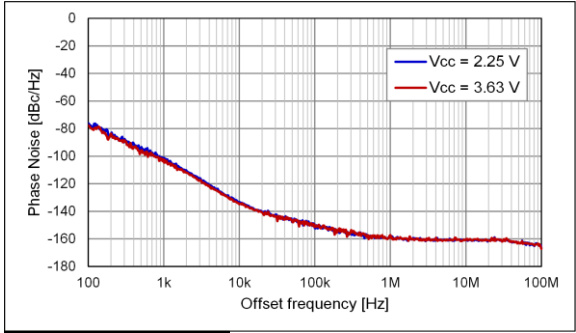
fo = 491.52 MHz

Output option:A



| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 22 fs         |
| 3.63 V          | 22 fs         |

Output option:B



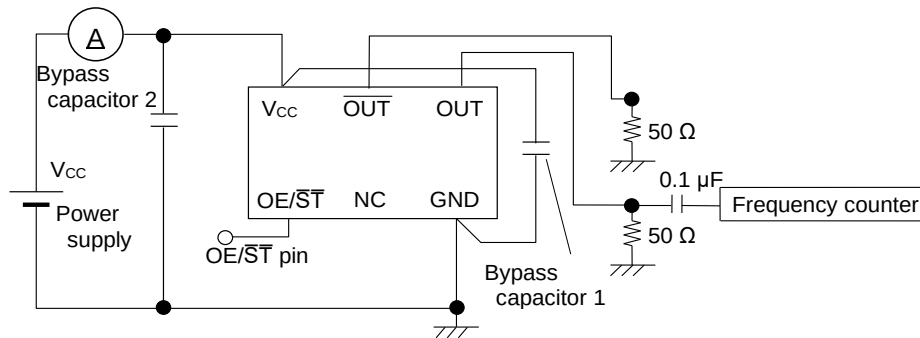
| V <sub>CC</sub> | Phase Jitter* |
|-----------------|---------------|
| 2.25 V          | 21 fs         |
| 3.63 V          | 21 fs         |

\* Offset frequency: 12 kHz to 20 MHz



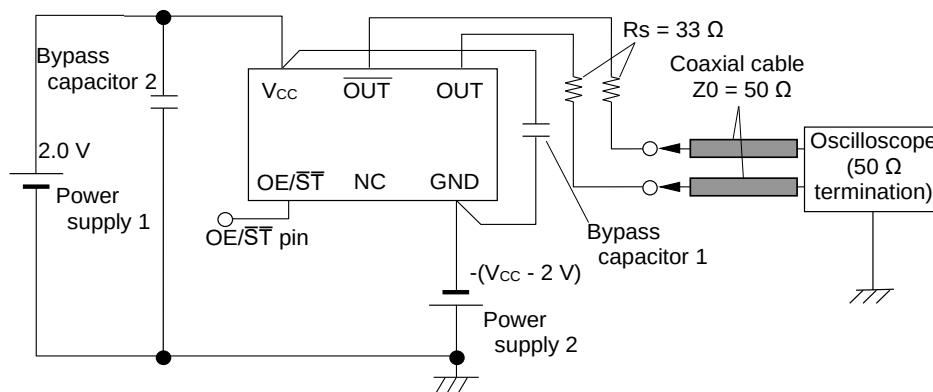
## [ 8 ] Test Circuit

## (8-1) Output Frequency and Current Consumption Test Setup



\* To measure Disable current or Stand-by current, OE/ST terminal is connected to GND

## (8-2) Waveform Observation Test Setup



\* Each output trace should be same length

## (8-3) Conditions

## (1) Oscilloscope

The bandwidth should be a minimum of 5 times the measurement frequency

(2) A 0.1 μF and a 10 μF bypass capacitor should be connected between V<sub>CC</sub> and GND pins located close to the device

## (3) Use a current meter with a low internal impedance

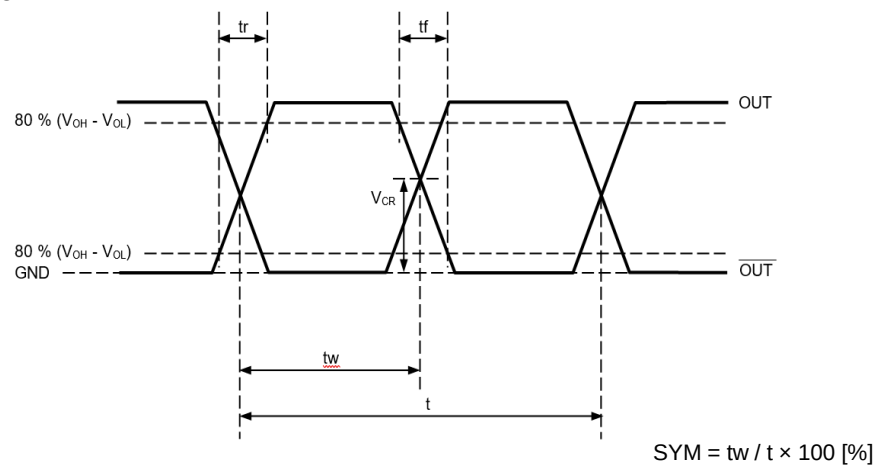
## (4) Power Supply

Power supply startup time (0 % V<sub>CC</sub> → 90 % V<sub>CC</sub>) should be more than 150 μs

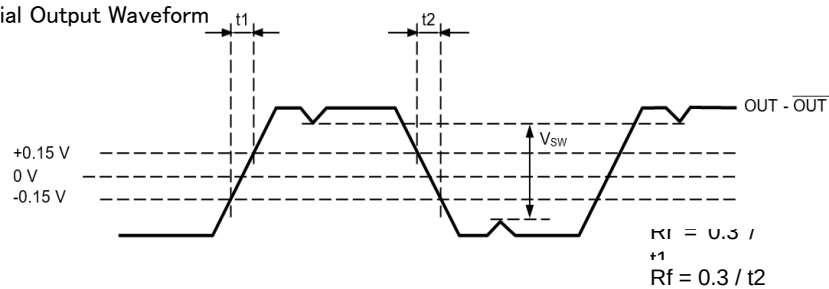
Power supply impedance should be as low as possible

## (8-4) Timing Chart

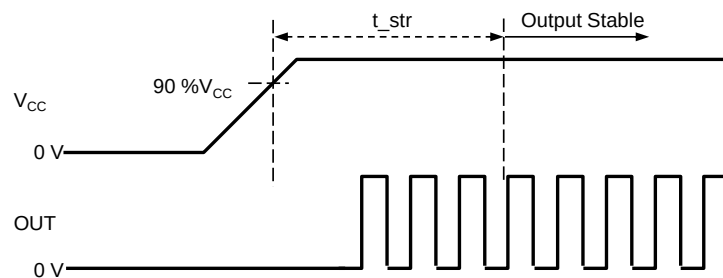
## (1) Output Waveform and Level

OUT,  $\overline{\text{OUT}}$  Waveform

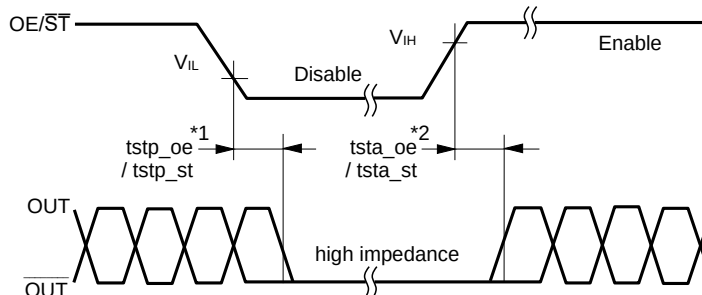
## Differential Output Waveform



## (2) Output Frequency Timing

(3) OE/ $\overline{\text{ST}}$  Function and Timing

| OE/ $\overline{\text{ST}}$ Terminal | Osc. Circuit                              | Output status                          |
|-------------------------------------|-------------------------------------------|----------------------------------------|
| "H" or OPEN                         | Oscillation                               | Specified frequency is output: Enable  |
| "L"                                 | OE: Oscillation                           | Output becomes high impedance: Disable |
|                                     | $\overline{\text{ST}}$ : Oscillation stop |                                        |



\*1 The period from OE/ $\overline{\text{ST}}$  =  $V_{IL}$  to OUT = High impedance (Disable)

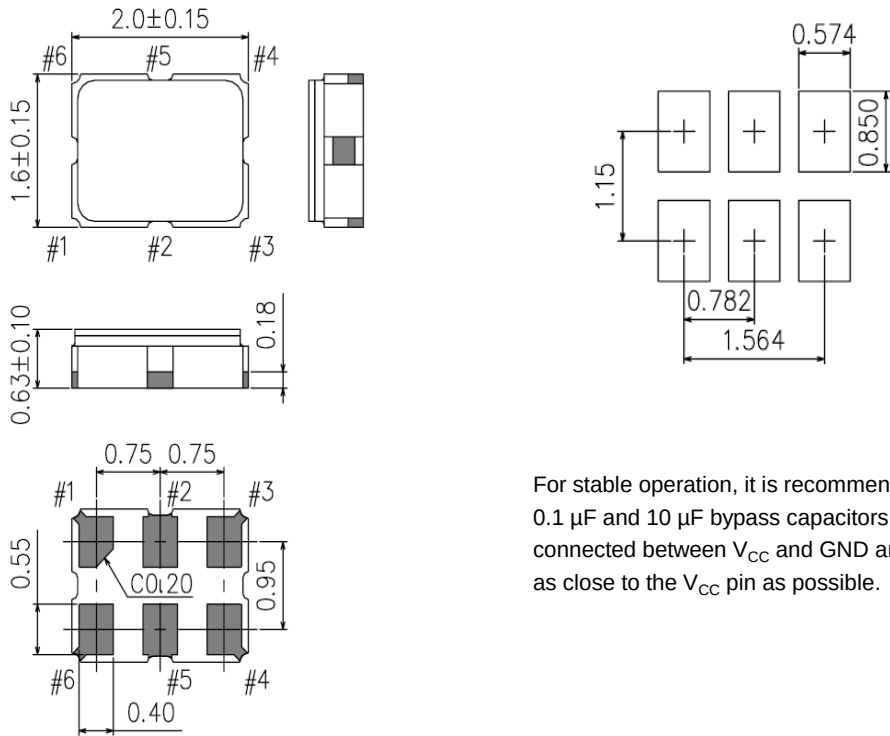
\*2 The period from OE/ $\overline{\text{ST}}$  =  $V_{IH}$  to OUT = Enable

\* OE/ $\overline{\text{ST}}$  terminal voltage level should not exceed supply voltage when using OE/ $\overline{\text{ST}}$  function.

Please note that OE/ $\overline{\text{ST}}$  rise time should not exceed supply voltage rise time at the start-up.

[ 9 ] Outline Drawing and Recommended Footprint  
(9-1) SG2016HGN

Units: mm



For stable operation, it is recommended that 0.1  $\mu$ F and 10  $\mu$ F bypass capacitors should be connected between  $V_{CC}$  and GND and placed as close to the  $V_{CC}$  pin as possible.

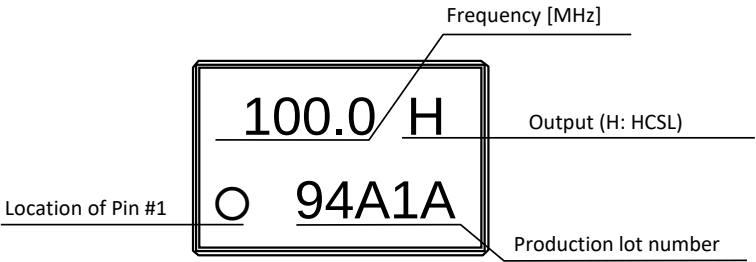
Terminal coating: Au plating

Reference Weight Typ.: 7.6 mg

Terminal Assignment

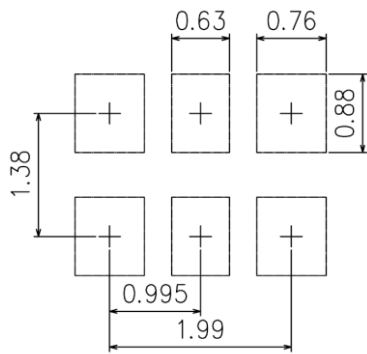
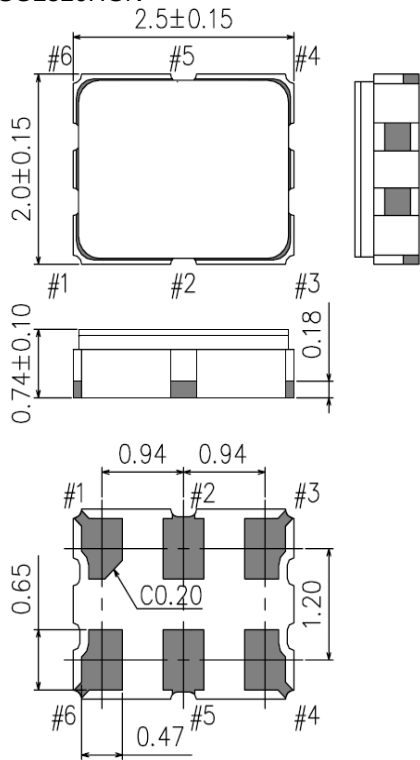
| Pin # | Connection          | Function                                   |                                                       |                                        |
|-------|---------------------|--------------------------------------------|-------------------------------------------------------|----------------------------------------|
| #1    | OE/ $\overline{ST}$ | OE/ $\overline{ST}$ terminal / active high |                                                       |                                        |
|       |                     | OE/ $\overline{ST}$ Terminal               | Osc. Circuit                                          | Output status                          |
|       |                     | "H" or OPEN                                | Oscillation                                           | Specified frequency is output: Enable  |
|       |                     | "L"                                        | OE: Oscillation<br>$\overline{ST}$ : Oscillation stop | Output becomes high impedance: Disable |
| #2    | NC                  | —                                          |                                                       |                                        |
| #3    | GND                 | GND terminal                               |                                                       |                                        |
| #4    | OUT                 | Output terminal (Positive)                 |                                                       |                                        |
| #5    | $\overline{OUT}$    | Output terminal (Negative)                 |                                                       |                                        |
| #6    | $V_{CC}$            | $V_{CC}$ terminal                          |                                                       |                                        |

Marking



(9-2) SG2520HGN

Units: mm



For stable operation, it is recommended that 0.1  $\mu$ F and 10  $\mu$ F bypass capacitors should be connected between  $V_{CC}$  and GND and placed as close to the  $V_{CC}$  pin as possible.

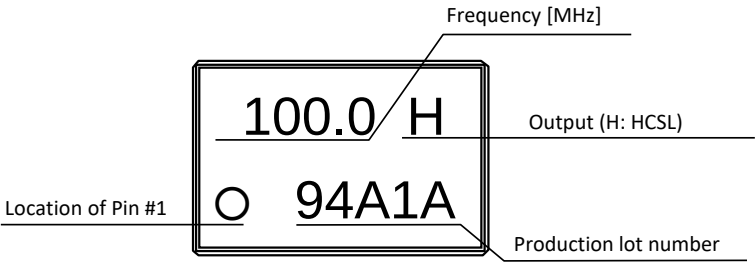
Terminal coating: Au plating

Reference Weight Typ.: 11.8 mg

Terminal Assignment

| Pin # | Connection          | Function                                   |                                                       |                                        |
|-------|---------------------|--------------------------------------------|-------------------------------------------------------|----------------------------------------|
| #1    | OE/ $\overline{ST}$ | OE/ $\overline{ST}$ terminal / active high |                                                       |                                        |
|       |                     | OE/ $\overline{ST}$ Terminal               | Osc. Circuit                                          | Output status                          |
|       |                     | "H" or OPEN                                | Oscillation                                           | Specified frequency is output: Enable  |
|       |                     | "L"                                        | OE: Oscillation<br>$\overline{ST}$ : Oscillation stop | Output becomes high impedance: Disable |
| #2    | NC                  | —                                          |                                                       |                                        |
| #3    | GND                 | GND terminal                               |                                                       |                                        |
| #4    | OUT                 | Output terminal (Positive)                 |                                                       |                                        |
| #5    | $\overline{OUT}$    | Output terminal (Negative)                 |                                                       |                                        |
| #6    | $V_{CC}$            | $V_{CC}$ terminal                          |                                                       |                                        |

Marking

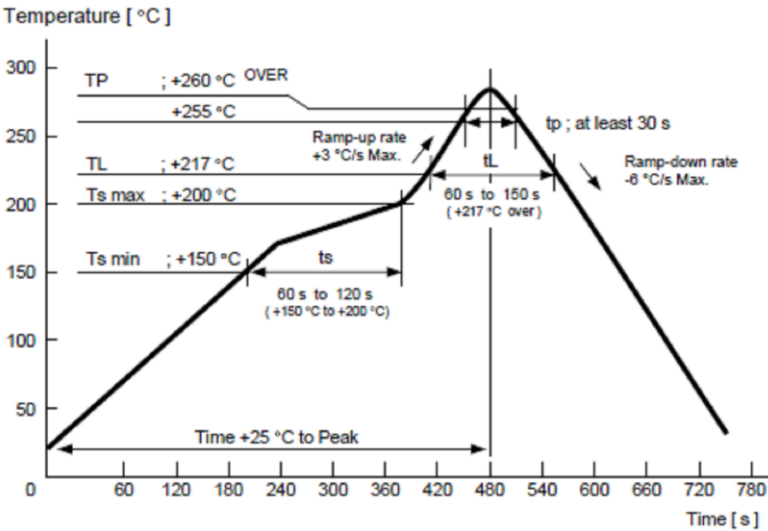


[ 10 ] Moisture Sensitivity Level

| Parameter | Specification | Conditions             |
|-----------|---------------|------------------------|
| MSL       | LEVEL 1       | IPC/JEDEC J-STD-020D.1 |

[ 11 ] Reflow Profile

IPC/JEDEC J-STD-020D.1



## [ 12 ] Packing Information

## (12-1) SG2016HGN

## (1) Packing Quantity

The last two digits of the Product Number (X1G006221~~xx~~) are a code that defines the packing quantity. The standard is "15" for a 2 000 pcs/Reel.

## (2) Taping Specification

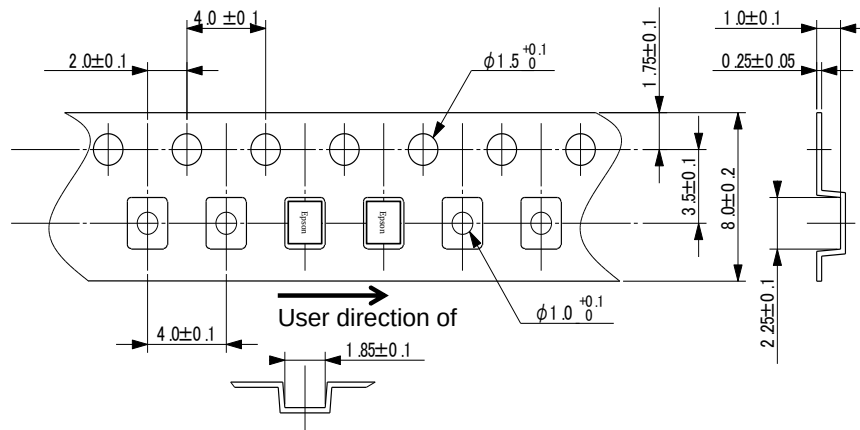
Subject to EIA-481, IEC-60286 and JIS C0806

## 1) Tape Dimensions

Carrier Tape Material: Black conductive PS (Polystyrene)

Top Tape Material: Antistatic PET (Polyethylene Terephthalate) + PE (Polyethylene)

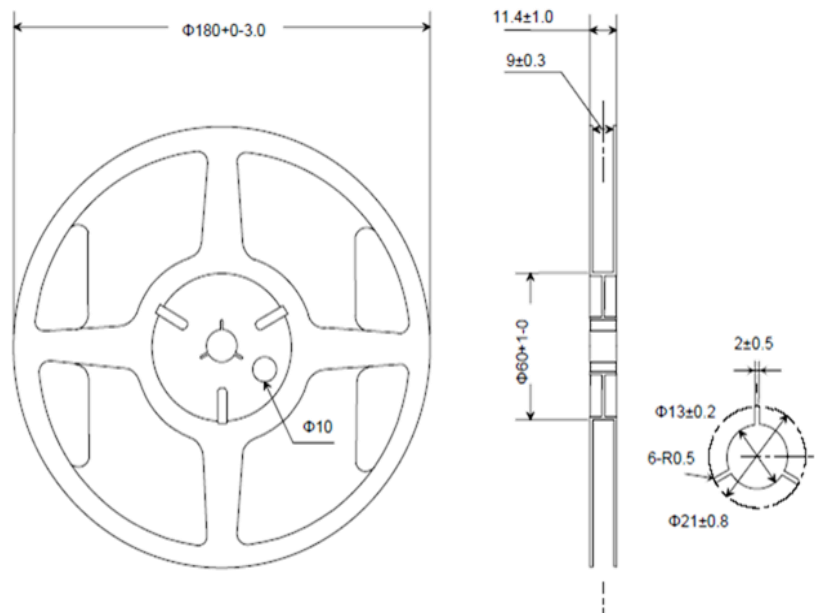
Units: mm



## 2) Reel Dimensions

Reel Material: Black conductive PS (Polystyrene)

Units: mm



\* The window shape of reel is a reference example

## 3) Storage Environment

We recommend to keep at normal temperature and normal humidity in a packed condition.

## (12-2) SG2520HGN

## (1) Packing Quantity

The last two digits of the Product Number (X1G005891xxxxxx) are a code that defines the packing quantity. The standard is "15" for a 2 000 pcs/Reel.

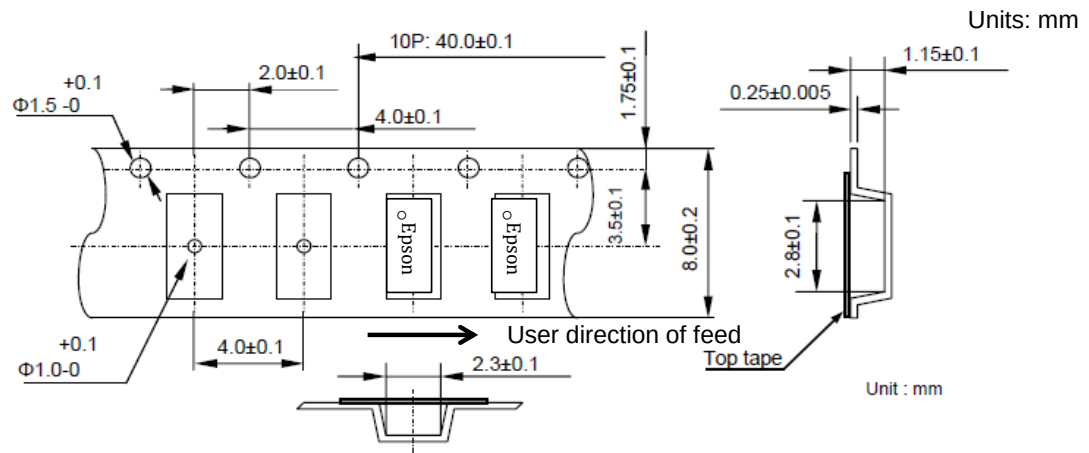
## (2) Taping Specification

Subject to EIA-481, IEC-60286 and JIS C0806

## 1) Tape Dimensions

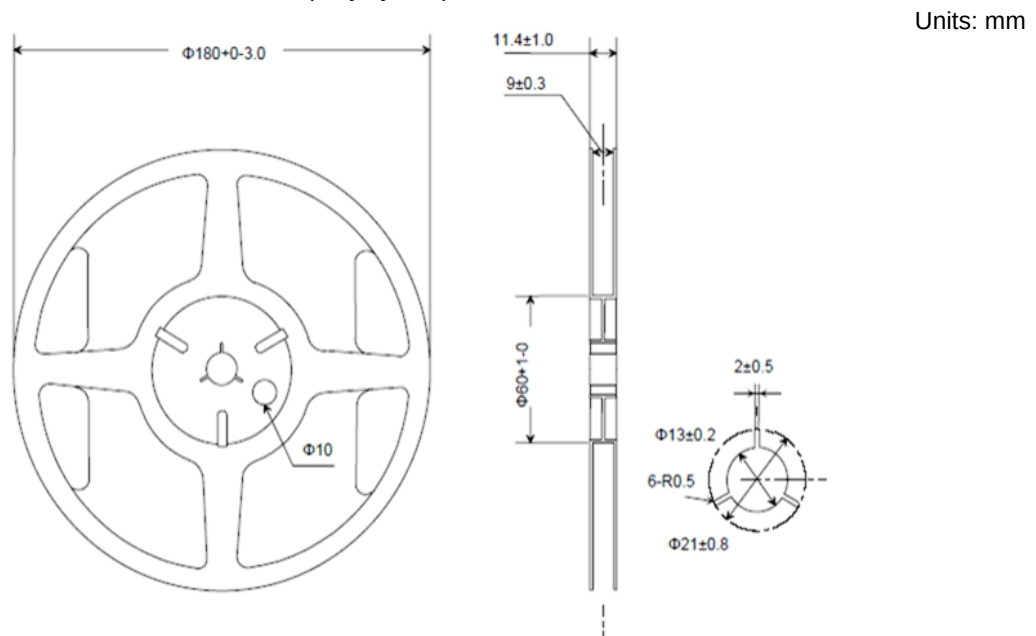
Carrier Tape Material: Black conductive PS (Polystyrene)

Top Tape Material: Antistatic PET (Polyethylene Terephthalate) + PE (Polyethylene)



## 2) Reel Dimensions

Reel Material: Black conductive PS (Polystyrene)



\* The window shape of reel is a reference example

## 3) Storage Environment

We recommend to keep at normal temperature and normal humidity in a packed condition.

## [ 13 ] Handling Precautions

Prior to using this product, please carefully read the section entitled "Precautions" on our Web site (<https://www5.epsondevice.com/en/information/#precaution>) for instructions on how to handle and use the product properly to ensure optimal performance of the product in your equipment.

Before using the product under any conditions other than those specified therein, please consult with us to verify and confirm that the performance of the product will not be negatively affected by use under such conditions.

In addition to the foregoing precautions, in order to avoid the deteriorating performance of the product, we strongly recommend that you DO NOT use the product under ANY of the following conditions:

- (1) Do not expose this product to excessive mechanical shock or vibration.
- (2) This product can be damaged by mechanical shock during the soldering process depending on the equipment used, process conditions, and any impact forces experienced. Always follow appropriate procedures, particularly when changing the assembly process in any way and be sure to follow applicable process qualification standards before starting production.
- (3) These devices are sensitive to ESD, use appropriate precautions during handling, assembly, test, shipment, and installation.
- (4) The use of ultrasonic technology for cleaning, bonding, etc. can damage the Xtal unit inside this product.  
Please carefully check for this consideration before using ultrasonic equipment for volume production with this product.
- (5) Noise and ripple on the power supply may have undesirable effects on operation and cause degradation of phase noise characteristics. Evaluate the operation of this device with appropriate power supplies carefully before use.
- (6) When applying power, ensure that the supply voltage increases monotonically for proper operation.  
On power down, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors are completely discharged since that may cause the unit to malfunction.
- (7) Aging specifications are estimated from environmental reliability tests and expected frequency variation over time.  
They do not provide a guarantee of aging over the product lifecycle.
- (8) The metal cap on top of the device is directly connected to the GND terminal (pin #2). Take necessary precautions to prevent any conductor not at ground potential from contacting the cap as that could cause a short circuit to GND.
- (9) To avoid any issues due to interference of other signal lines, please take care not to place signal lines near the product as this may have an adverse affect on the performance of the product.
- (10) A bypass capacitor of the recommended value(s) must be connected between the  $V_{CC}$  and GND terminals of the product.  
Whenever possible, mount the capacitor(s) on the same side of the PCB and as close to the product as possible to keep the routing traces short.
- (11) Power supply connections to  $V_{CC}$  and GND pins should be routed as thick as possible while keeping the high frequency impedance low in order to get the best performance.
- (12) The use of a filter or similar element in series with the power supply connections to protect from electromagnetic radiation noise may increase the high frequency impedance of the power supply line and may cause the oscillator to not operate properly.  
Please verify the design to ensure sufficient operational margin prior to use.
- (13) Keep PCB routing from the output terminal(s) to the load as short as possible for best performance.
- (14) The Enable (OE or  $\overline{ST}$ ) input terminal is high impedance and so susceptible to noise. Connect it to a low impedance source when used and when not used it is recommended to connect it to  $V_{CC}$  for active high inputs and GND for active low inputs.
- (15) Do not short the output to GND as that will damage the product. Always use with an appropriate load resistor connected.
- (16) This product should be reflowed no more than 3 times.  
If rework is needed after reflow, please correct it with a soldering iron with the tip set for a temperature of +350 °C or less and only contact each terminal once and for no more than 5 seconds.  
If this product is mounted on the bottom of the board during a reflow please check that it soldered down properly afterwards.
- (17) Product failures during the warranty period only apply when the product is used according to the recommended operating conditions described in the specifications. Products that have been opened for analysis or damaged will not be covered.  
It is recommended to store and use in normal temperature and humidity environments described in the specifications to ensure frequency accuracy and prevent moisture condensation. If the product is stored for more than one year, please confirm the pin solderability prior to use.
- (18) If the oscillation circuit is exposed to condensation, the frequency may change or oscillation may stop. Do not use in any conditions where condensation occurs.
- (19) Do not store or use the product in an environment where it can be exposed to chemical substances that are corrosive to metal or plastics such as salt water, organic solvents, chemical gasses, etc. Do not use the product when it is exposed to sunlight, dust, corrosive gasses, or other materials for long periods of time.
- (20) When using water-soluble solder flux make sure to completely remove the flux residue after soldering.  
Pay particular attention when the residues contain active halogens which will negatively affect the product and its performance.
- (21) Terminals on the side of the product are internally connected to the IC, be careful not to cause short-circuits or reduce the insulation resistance of them in any way.
- (22) Should any customer use the product in any manner contrary to the precautions and/or advice herein, such use shall be done at the customer's own risk.

[Availability of mounting conditions]

|                                                                  |                                                                          |
|------------------------------------------------------------------|--------------------------------------------------------------------------|
| Reflow on the board                                              | Available                                                                |
| Reflow under the board                                           | The parts may fall.<br>Please judge whether it is possible to implement. |
| Soldering pot/bath (Dip soldering system, Flow soldering system) | <b>Not Available</b>                                                     |
| Soldering iron                                                   | Available                                                                |



## PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

## WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major manufacturers as standard.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

### ■ Explanation of marks used in this datasheet

|                                                                                   |                                                                                                                                                                                                                              |
|-----------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|  | ●Pb free.                                                                                                                                                                                                                    |
|  | ●Complies with EU RoHS directive.<br>*About the products without the Pb-free mark.<br>Contains Pb in products exempted by EU RoHS directive<br>(Contains Pb in sealing glass, high melting temperature type solder or other) |

NOTICE: PLEASE READ CAREFULLY BELOW BEFORE THE USE OF THIS DOCUMENT ©Seiko Epson Corporation

- The content of this document is subject to change without notice. Before purchasing or using Epson products, please contact with sales representative of Seiko Epson Corporation ("Epson") for the latest information and be always sure to check the latest information published on Epson's official web sites and resources.
- This document may not be copied, reproduced, or used for any other purposes, in whole or in part, without Epson's prior consent.
- Information provided in this document including, but not limited to application circuits, programs and usage, is for reference purpose only. Epson makes no guarantees against any infringements or damages to any third parties' intellectual property rights or any other rights resulting from the information. This document does not grant you any licenses, any intellectual property rights or any other rights with respect to Epson products owned by Epson or any third parties.
- Using Epson products, you shall be responsible for safe design in your products; that is, your hardware, software, and/or systems shall be designed enough to prevent any critical harm or damages to life, health or property, even if any malfunction or failure might be caused by Epson products. In designing your products with Epson products, please be sure to check and comply with the latest information regarding Epson products (including, but not limited to this document, specifications, data sheets, manuals, and Epson's web site). Using technical contents such as product data, graphic and chart, and technical information, including programs, algorithms and application circuit examples under this document, you shall evaluate your products thoroughly both in stand-alone basis and within your overall systems. You shall be solely responsible for deciding whether to adopt/use Epson products with your products.
- Epson has prepared this document carefully to be accurate and dependable, but Epson does not guarantee that the information is always accurate and complete. Epson assumes no responsibility for any damages you incurred due to any misinformation in this document.
- No dismantling, analysis, reverse engineering, modification, alteration, adaptation, reproduction, etc., of Epson products is allowed.
- Epson products have been designed, developed and manufactured to be used in general electronic applications and specifically requires particular quality or extremely high reliability in order to refrain from causing any malfunction or failure leading to critical harm to life and health, serious property damage, or severe impact on society, including, but not limited to listed below ("Specific Purpose"). Therefore, you are strongly advised to use Epson products only for the Anticipated Purpose. Should you desire to purchase and use Epson products for Specific Purpose, Epson makes no warranty and disclaims with respect to Epson products, whether express or implied, including without limitation any implied warranty of merchantability or fitness for any Specific Purpose. Please be sure to contact our sales representative in advance, if you desire Epson products for Specific Purpose:  
Space equipment (artificial satellites, rockets, etc.)/ Transportation vehicles and their control equipment (automobiles, aircraft, trains, ships, etc.) / Medical equipment/ Relay equipment to be placed on sea floor/ Power station control equipment / Disaster or crime prevention equipment/Traffic control equipment/ Financial equipment  
Other applications requiring similar levels of reliability as the above
- Epson products listed in this document and our associated technologies shall not be used in any equipment or systems that laws and regulations in Japan or any other countries prohibit to manufacture, use or sell. Furthermore, Epson products and our associated technologies shall not be used for the purposes of military weapons development (e.g. mass destruction weapons), military use, or any other military applications. If exporting Epson products or our associated technologies, please be sure to comply with the Foreign Exchange and Foreign Trade Control Act in Japan, Export Administration Regulations in the U.S.A (EAR) and other export-related laws and regulations in Japan and any other countries and to follow their required procedures.
- Epson assumes no responsibility for any damages (whether direct or indirect) caused by or in relation with your non-compliance with the terms and conditions in this document or for any damages (whether direct or indirect) incurred by any third party that you give, transfer or assign Epson products.
- For more details or other concerns about this document, please contact our sales representative.
- Company names and product names listed in this document are trademarks or registered trademarks of their respective companies.