



NHDTA114/124/144EU-Q series

80 V, 100 mA PNP resistor-equipped transistors

Rev. 1 — 18 October 2024

Product data sheet

1. General description

PNP Resistor-Equipped Transistor (RET) family in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	R1	R2	Package		NPN complement:
	k Ω	k Ω	Nexperia	JEITA	
NHDTA114EU-Q	10	10	SOT323	SC-70	NHDTC114EU-Q
NHDTA124EU-Q	22	22			NHDTC124EU-Q
NHDTA144EU-Q	47	47			NHDTC144EU-Q

2. Features and benefits

- 100 mA output current capability
- High breakdown voltage
- Built-in resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Digital applications
- Cost saving alternative for BC856 series in digital applications
- Controlling IC inputs
- Switching loads

4. Quick reference data

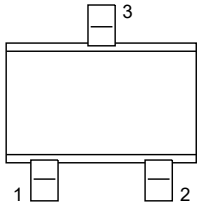
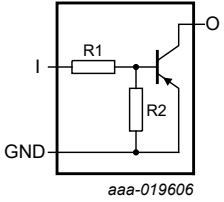
Table 2. Quick reference data

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-80	V
I_O	output current		-	-	-100	mA

5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		
2	GND	GND (emitter)		
3	O	output (collector)		

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
NHDTA114EU-Q	SC-70	plastic surface-mounted package; 3 leads	SOT323
NHDTA124EU-Q			
NHDTA144EU-Q			

7. Marking

Table 5. Marking

Type number	Marking code [1]
NHDTA114EU-Q	5F%
NHDTA124EU-Q	5J%
NHDTA144EU-Q	5L%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 6. Limiting values

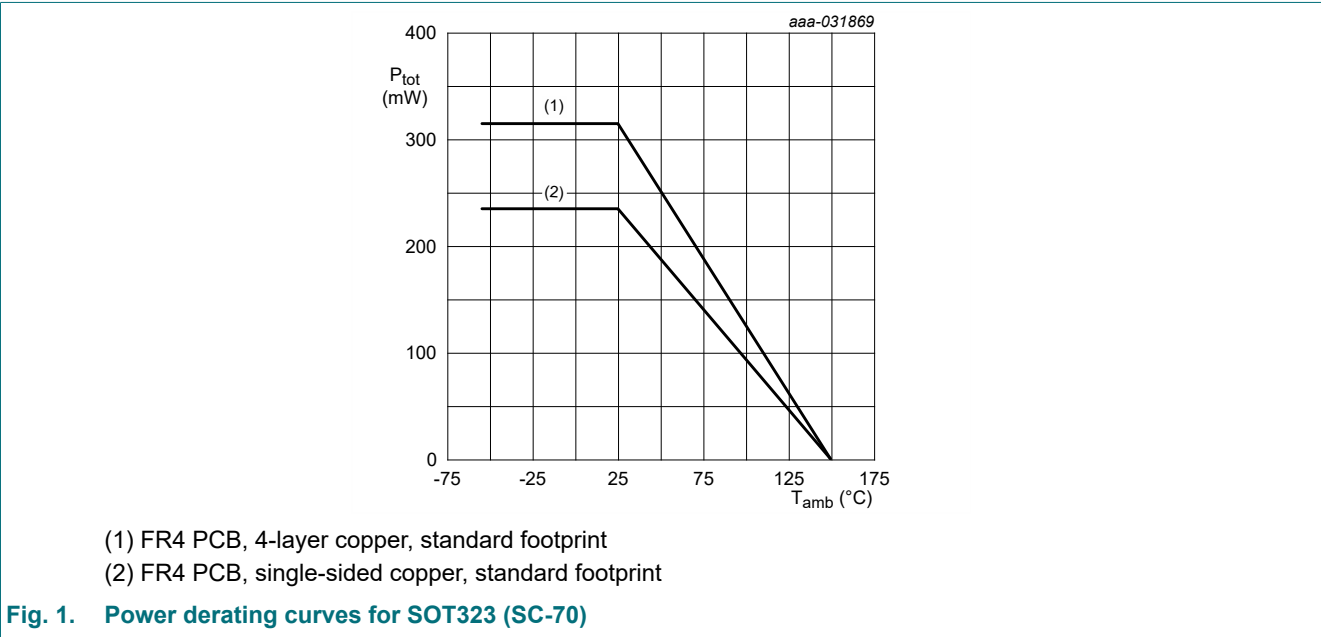
In accordance with the Absolute Maximum Rating System (IEC 60134).

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-80	V
V_{CEO}	collector-emitter voltage	open base	-	-80	V
V_{EBO}	emitter-base voltage	open collector	-	-10	V
V_I	input voltage				
	NHDTA114EU-Q		-40	+10	V
	NHDTA124EU-Q		-60	+10	V
	NHDTA144EU-Q		-80	+10	V
I_O	output current		-	-100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	235 mW
			[2]	-	315 mW
T_j	junction temperature		-	150	°C
T_{amb}	ambient temperature		-55	150	°C
T_{stg}	storage temperature		-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint.



9. Thermal characteristics

Table 7. Thermal characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	532	K/W
			[2]	-	-	397	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	150	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.

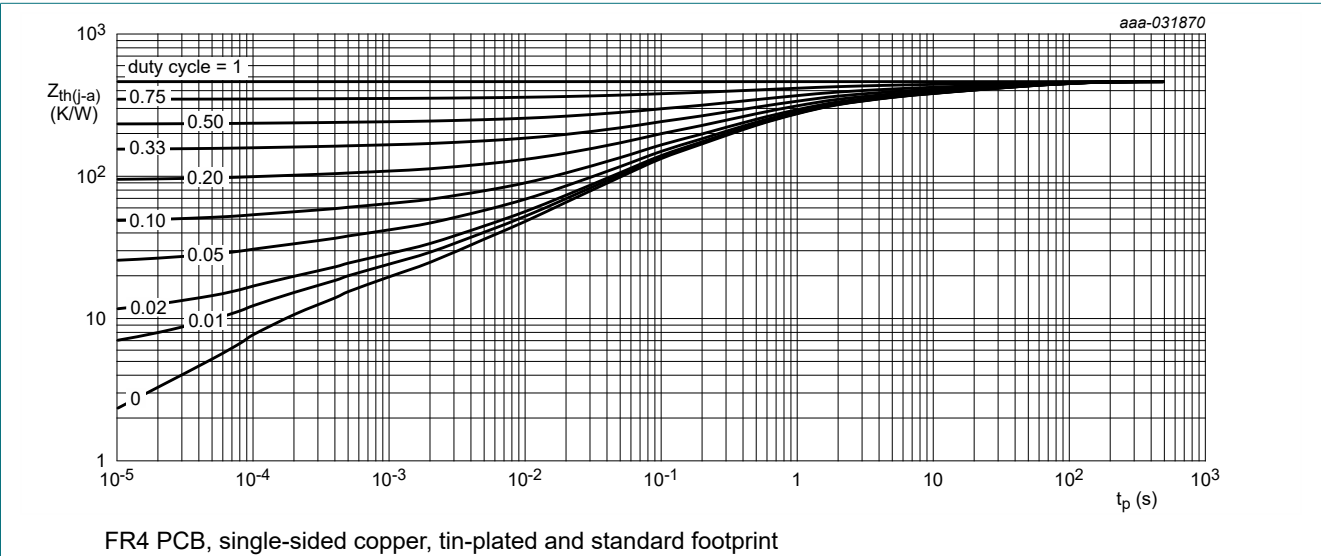


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

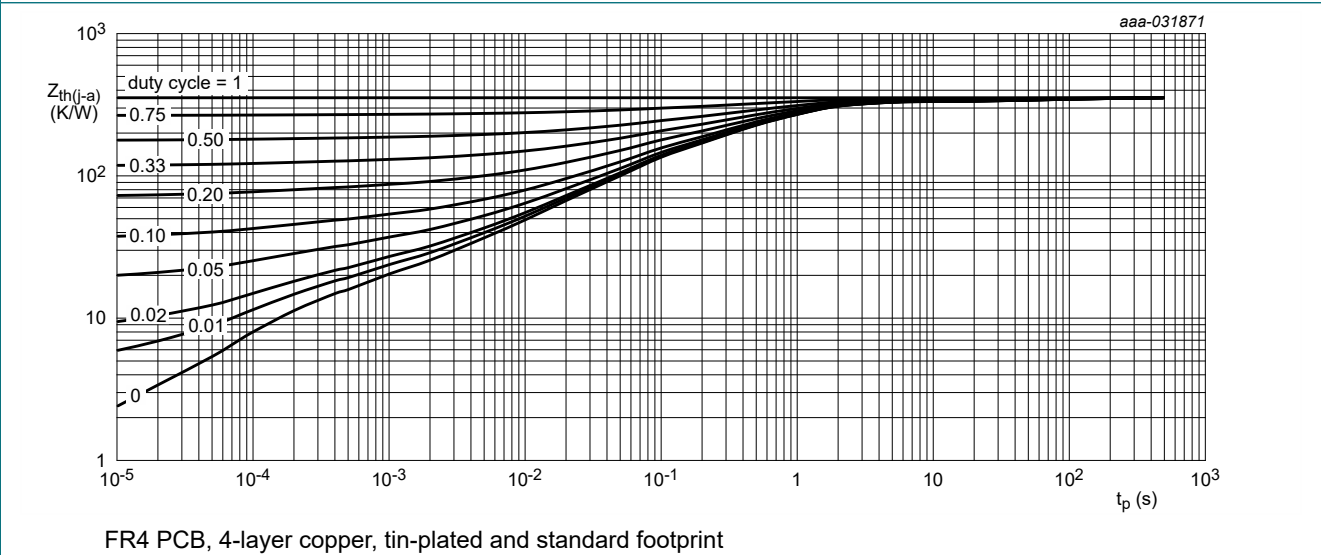


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 8. Characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A	-80	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -2 mA; I _B = 0 A	-80	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = -80 V; I _E = 0 A	-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -60 V; I _B = 0 A	-	-	-100	nA
		V _{CE} = -60 V; I _B = 0 A; T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current					
	NHDTA114EU-Q	V _{EB} = -7 V; I _C = 0 A	-	-	-600	μA
	NHDTA124EU-Q		-	-	-270	μA
	NHDTA144EU-Q		-	-	-130	μA
h _{FE}	DC current gain					
	NHDTA114EU-Q	V _{CE} = -5 V; I _C = -10 mA	50	-	-	
	NHDTA124EU-Q		70	-	-	
	NHDTA144EU-Q		100	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -10 mA; I _B = -0.5 mA	-	-	-100	mV
V _{I(off)}	off-state input voltage	V _{CE} = -5 V ; I _C = -100 μA	-	-1.15	-0.8	V
V _{I(on)}	on-state input voltage					
	NHDTA114EU-Q	V _{CE} = -0.3 V ; I _C = -10 mA	-2.5	-1.8	-	V
	NHDTA124EU-Q		-3	-2.3	-	V
	NHDTA144EU-Q		-5	-3.3	-	V
R1	bias resistor 1 (input)		[1]			
	NHDTA114EU-Q		7	10	13	kΩ
	NHDTA124EU-Q		15.4	22	28.6	kΩ
	NHDTA144EU-Q		33	47	61	kΩ
R2/R1	bias resistor ratio	[1]	0.8	1	1.2	
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz [2]	-	150	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz	-	-	3	pF

[1] See section "Test information" for resistor calculation and test conditions

[2] Characteristics of built-in transistor

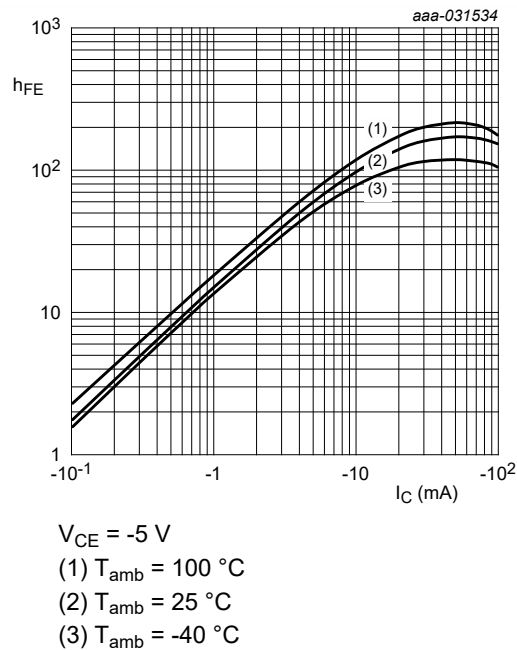


Fig. 4. NHDTA114EU-Q: DC current gain as a function of collector current; typical values

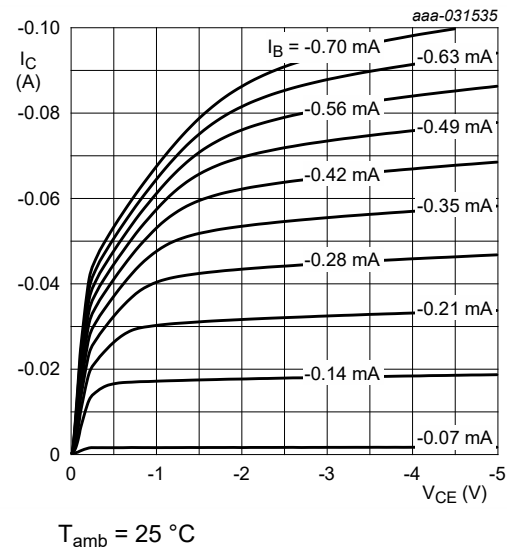


Fig. 5. NHDTA114EU-Q: Collector current as a function of collector-emitter voltage; typical values

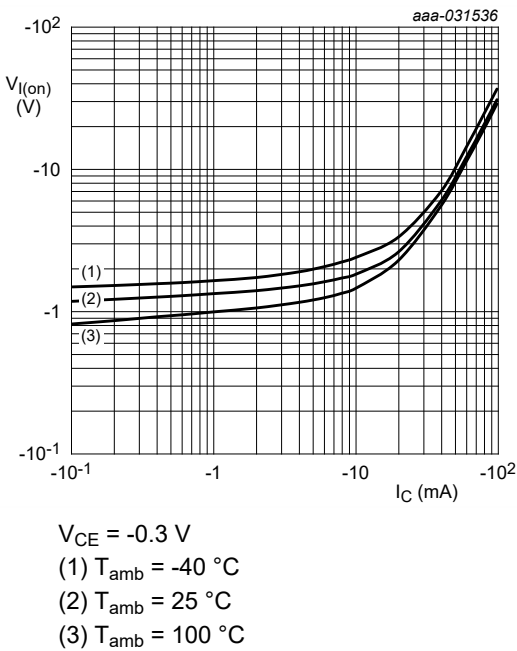


Fig. 6. NHDTA114EU-Q: On-state input voltage as a function of collector current; typical values

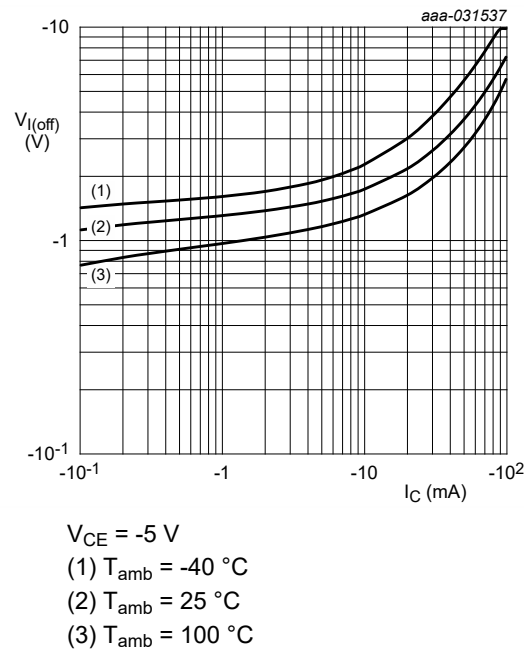


Fig. 7. NHDTA114EU-Q: Off-state input voltage as a function of collector current; typical values

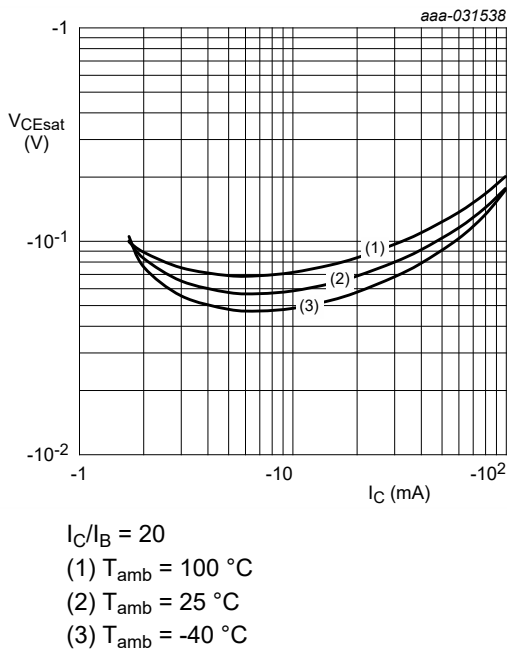


Fig. 8. NHDTA114EU-Q: Collector-emitter saturation voltage as a function of collector current; typical values

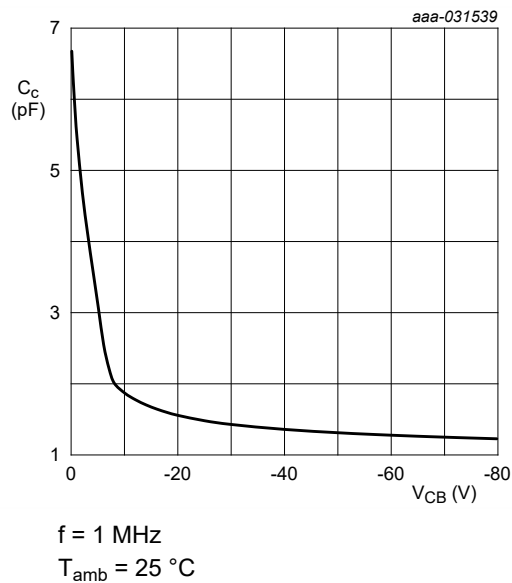


Fig. 9. NHDTA114EU-Q: Collector capacitance as a function of collector-base voltage; typical values

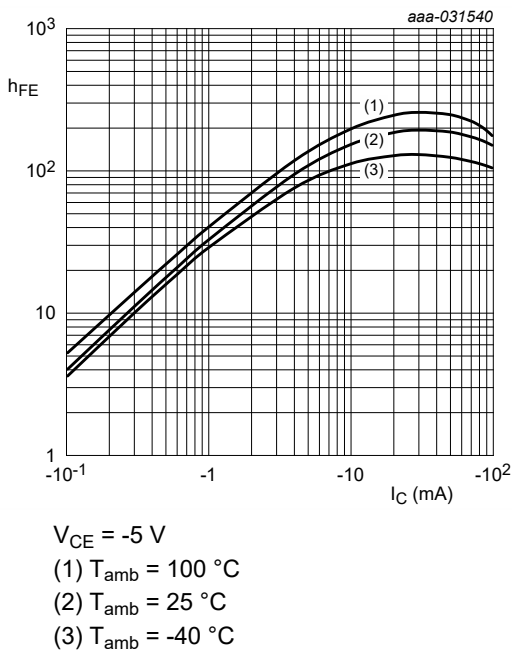


Fig. 10. NHDTA124EU-Q: DC current gain as a function of collector current; typical values

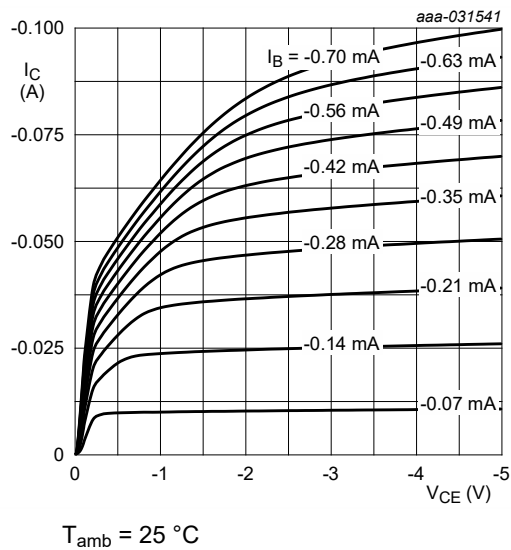


Fig. 11. NHDTA124EU-Q: Collector current as a function of collector-emitter voltage; typical values

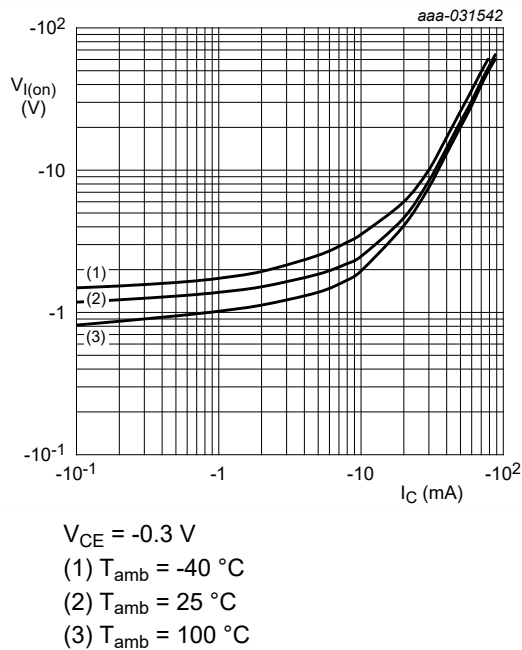


Fig. 12. NHDTA124EU-Q: On-state input voltage as a function of collector current; typical values

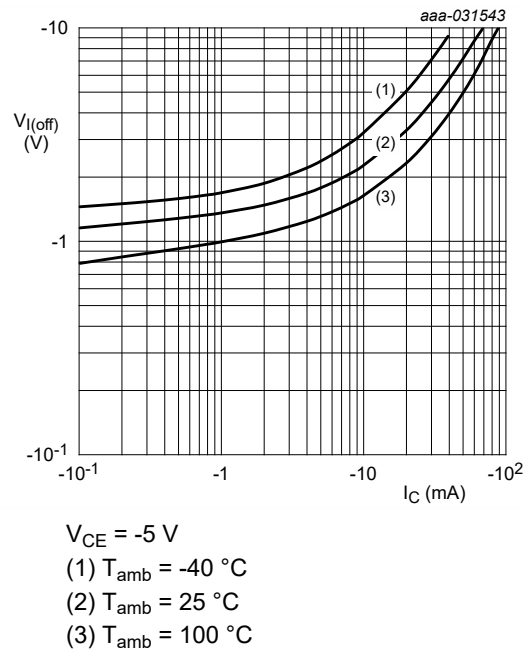


Fig. 13. NHDTA124EU-Q: Off-state input voltage as a function of collector current; typical values

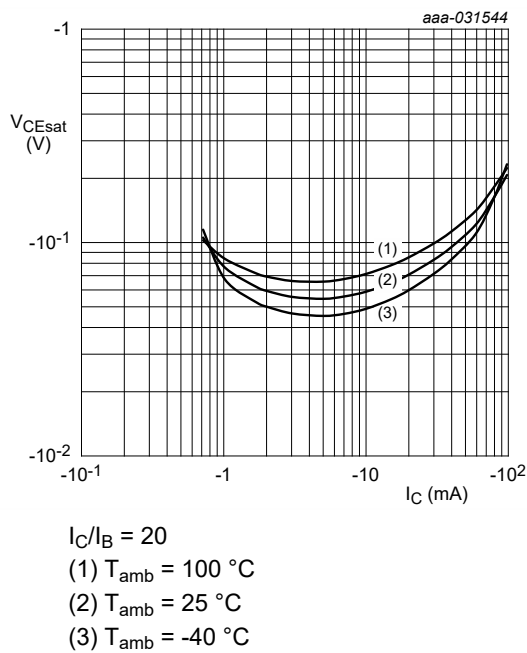


Fig. 14. NHDTA124EU-Q: Collector-emitter saturation voltage as a function of collector current; typical values

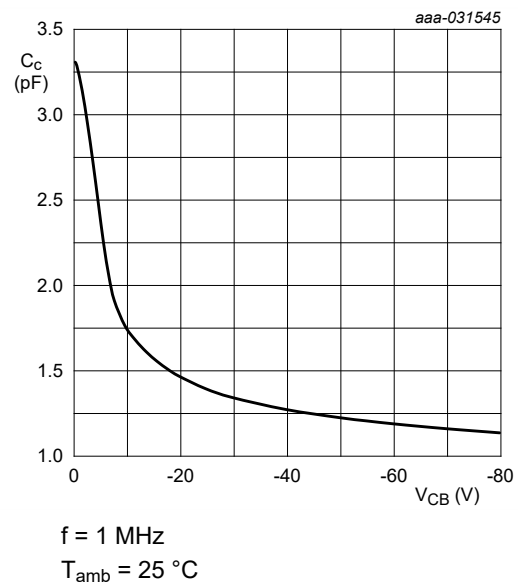


Fig. 15. NHDTA124EU-Q: Collector capacitance as a function of collector-base voltage; typical values

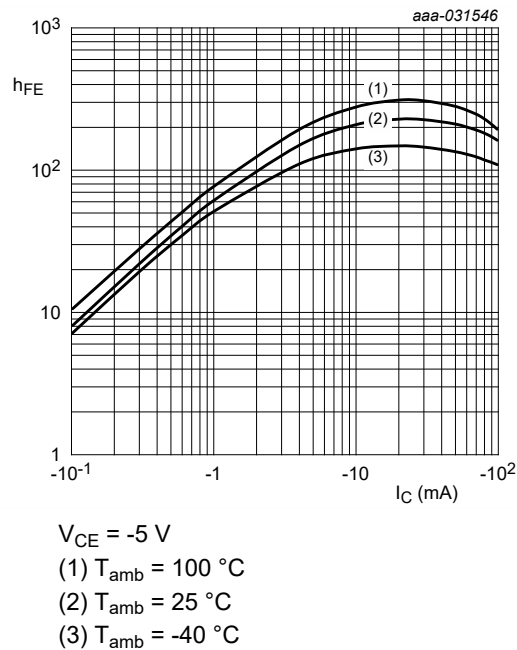


Fig. 16. NHDTA144EU-Q: DC current gain as a function of collector current; typical values

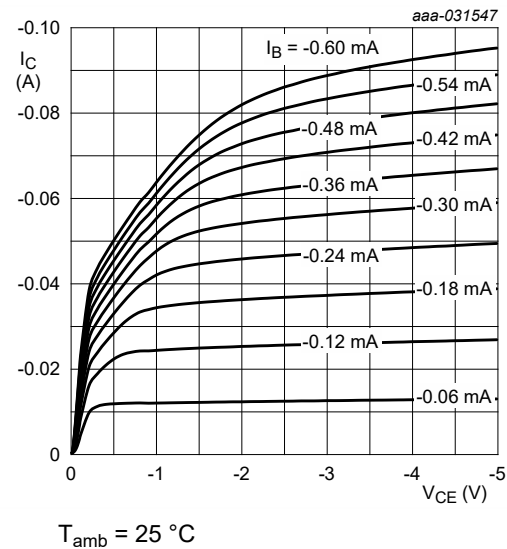


Fig. 17. NHDTA144EU-Q: Collector current as a function of collector-emitter voltage; typical values

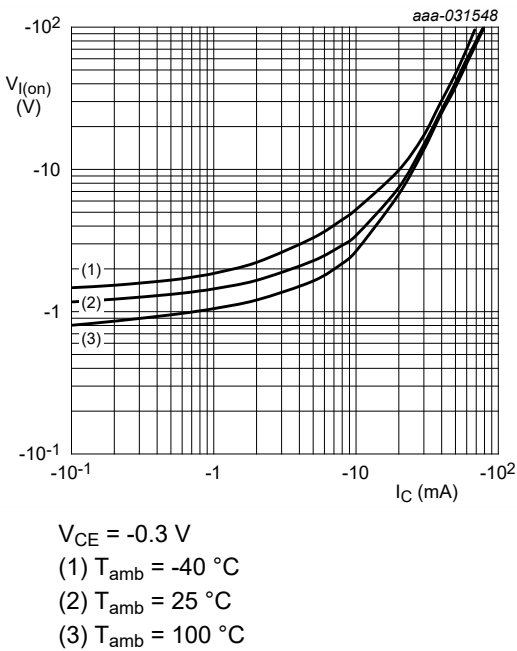


Fig. 18. NHDTA144EU-Q: On-state input voltage as a function of collector current; typical values

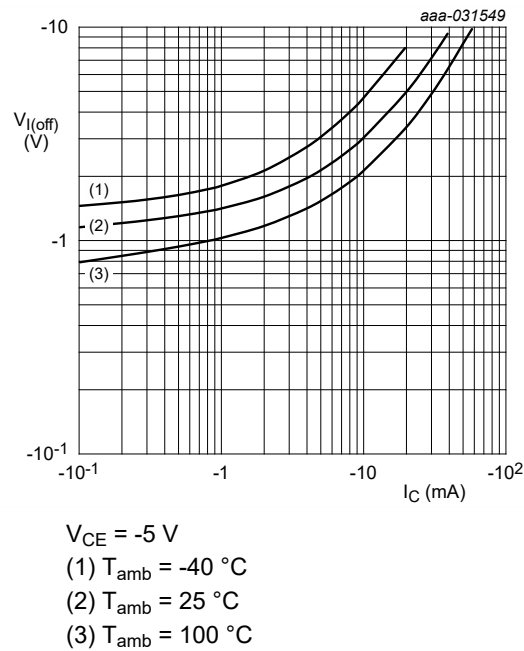


Fig. 19. NHDTA144EU-Q: Off-state input voltage as a function of collector current; typical values

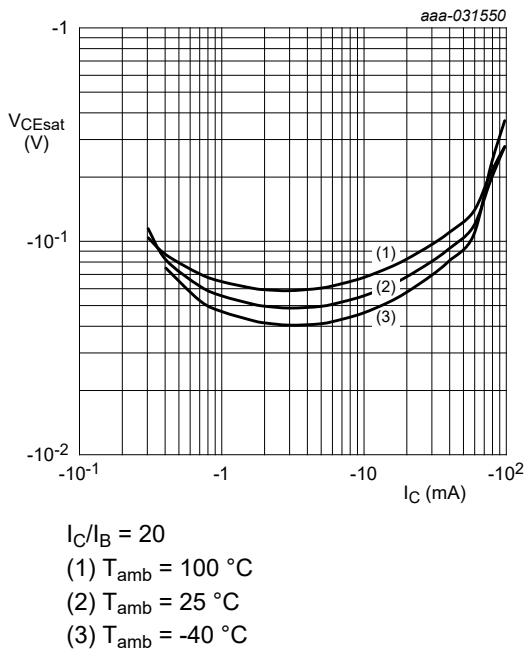


Fig. 20. NHDTA144EU-Q: Collector-emitter saturation voltage as a function of collector current; typical values

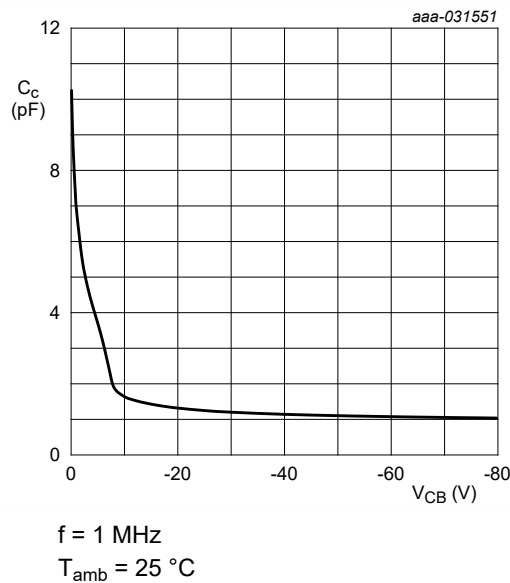


Fig. 21. NHDTA144EU-Q: Collector capacitance as a function of collector-base voltage; typical values of built-in transistor

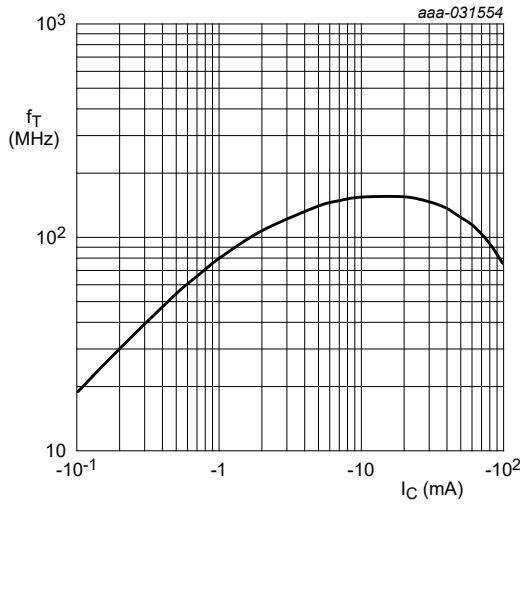


Fig. 22. Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Resistor calculation

- Calculation of bias resistor 1 (R1)
$$R_I = \frac{V(I_2) - V(I_I)}{I_2 - I_I}$$
- Calculation of bias resistor ratio (R2/R1)
$$\frac{R2}{R1} = \frac{V(I_4) - V(I_3)}{R1 \cdot (I_4 - I_3)} - 1$$

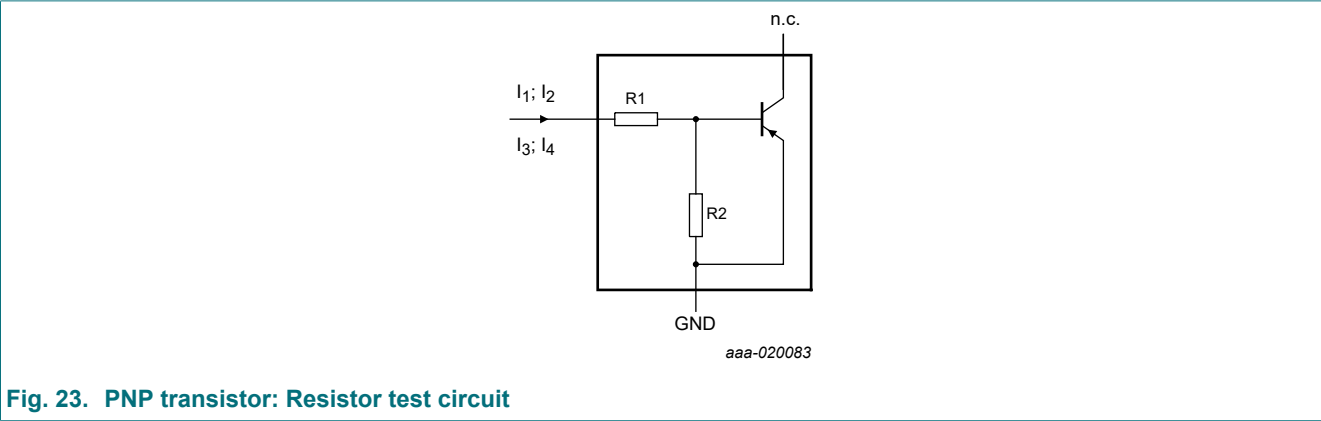


Fig. 23. PNP transistor: Resistor test circuit

Resistor test conditions

Table 9. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I ₁	I ₂	I ₃	I ₄
NHDTA114EU-Q	10	10	-800 μA	-1.1 mA	350 μA	450 μA
NHDTA124EU-Q	22	22	-550 μA	-750 μA	150 μA	230 μA
NHDTA144EU-Q	47	47	-250 μA	-350 μA	55 μA	105 μA

12. Package outline

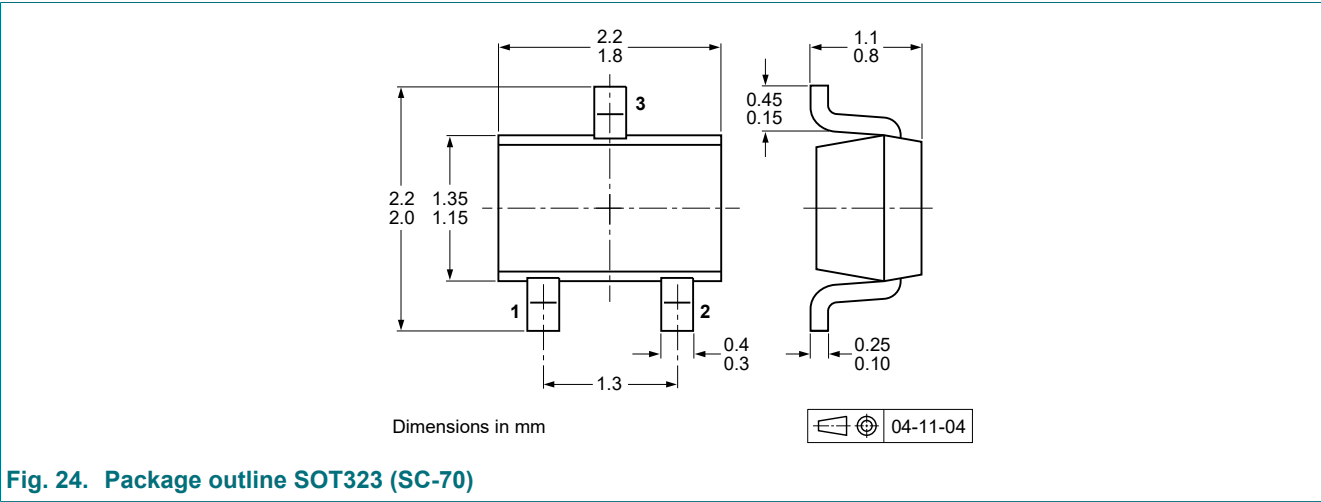


Fig. 24. Package outline SOT323 (SC-70)

13. Soldering

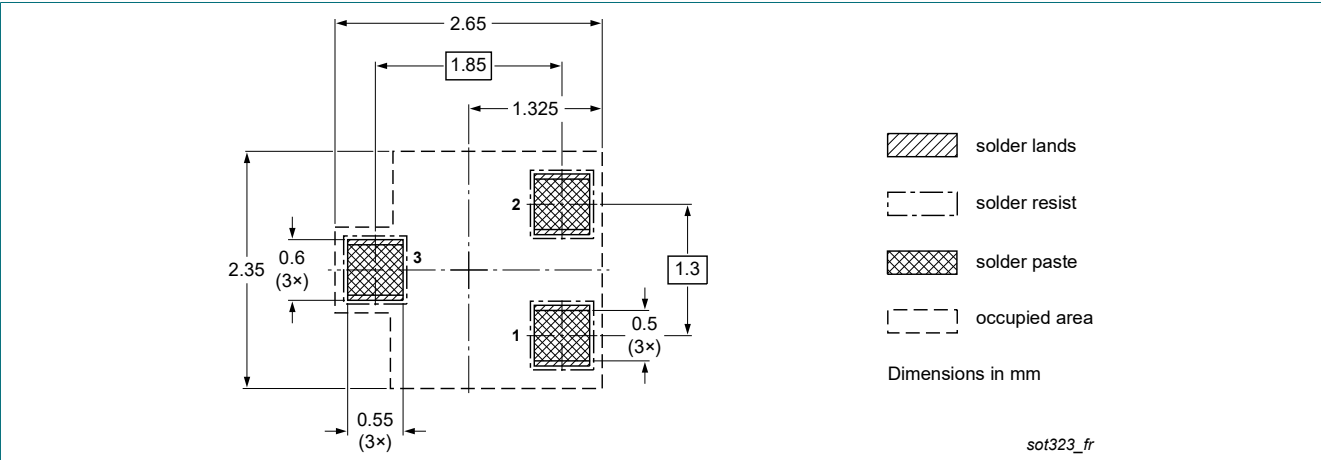


Fig. 25. Reflow soldering footprint for SOT323 (SC-70)

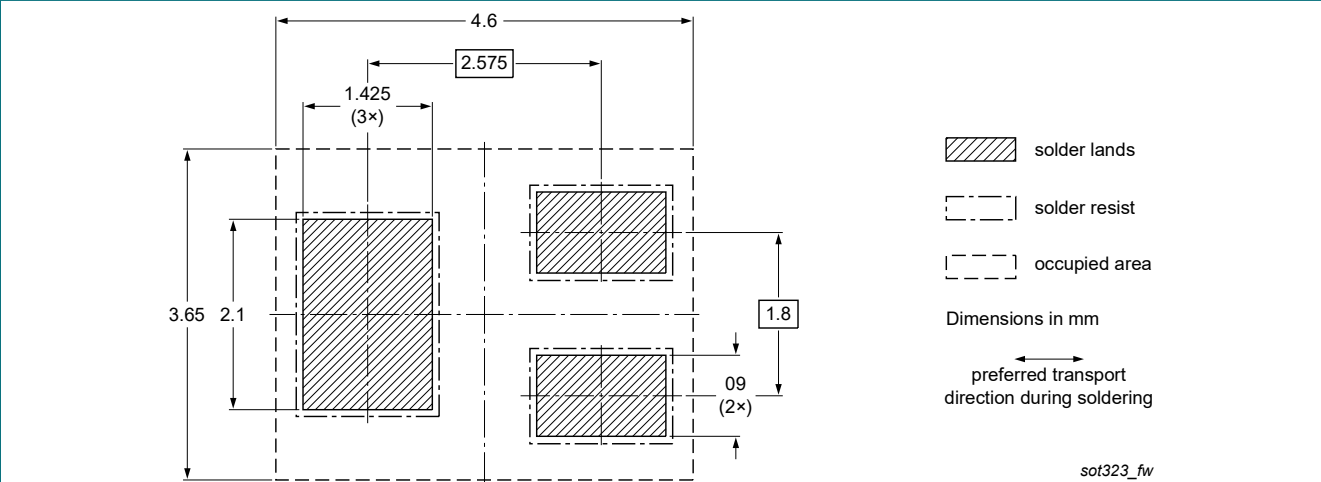


Fig. 26. Wave soldering footprint for SOT323 (SC-70)

14. Revision history

Table 10. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NHDTA114_124_144EU-Q_SER v.1	20241018	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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