



PHPT60410NY-Q

40 V, 10 A NPN high power bipolar transistor

27 September 2024

Product data sheet

1. General description

NPN high power bipolar transistor in a SOT669 (LFAK56) Surface-Mounted Device (SMD) power plastic package.

PNP complement: PHPT60410PY-Q

2. Features and benefits

- High thermal power dissipation capability
- High temperature applications up to 175 °C
- Reduced Printed Circuit Board (PCB) requirements comparing to transistors in DPAK
- High energy efficiency due to less heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Power management
- Load switch
- Linear mode voltage regulator
- Backlighting applications
- Motor drive
- Relay replacement

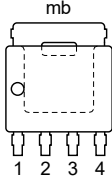
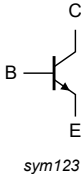
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CE0}	collector-emitter voltage	open base	-	-	40	V
I_C	collector current		-	-	10	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	20	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = 10$ A; $I_B = 1$ A; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	28	40	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 LFPAK56; Power-SO8 (SOT669)	 <i>sym123</i>
2	E	emitter		
3	E	emitter		
4	B	base		
mb	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PHPT60410NY-Q	LFPAK56; Power-SO8	plastic, single-ended surface-mounted package; 4 terminals	SOT669

7. Marking

Table 4. Marking codes

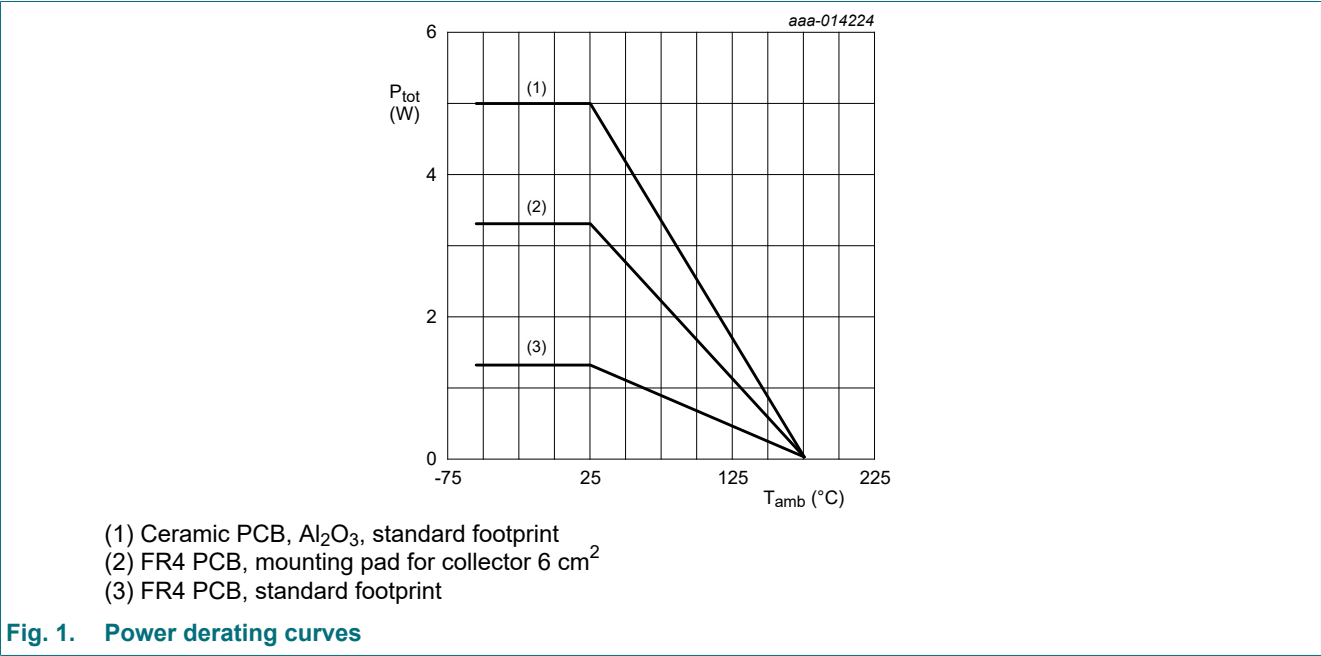
Type number	Marking code
PHPT60410NY-Q	0410NAB

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	10	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	20	A
I _B	base current			-	1	A
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	2	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.3	W
			[2]	-	3.3	W
			[3]	-	5	W
			[4]	-	25	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated and mounting pad for collector 6 cm².
- [3] Device mounted on an ceramic PCB; Al₂O₃, standard footprint.
- [4] Power dissipation from junction to mounting base.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	115	K/W
			[2]	-	-	45	K/W
			[3]	-	-	30	K/W
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	-	6	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
[3] Device mounted on an ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

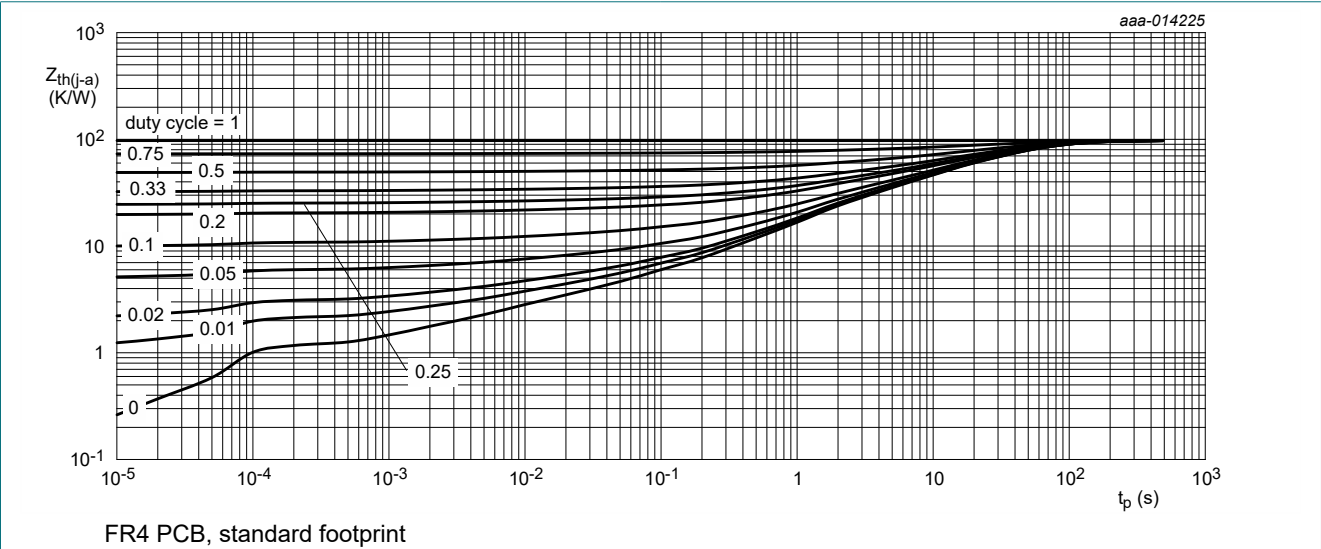


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

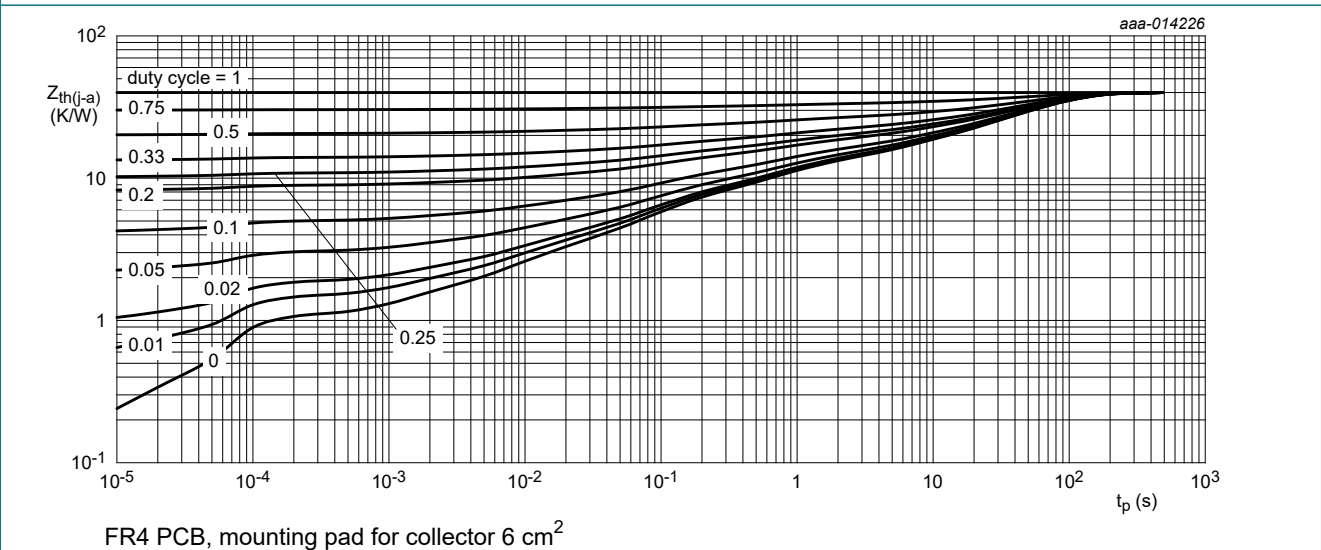


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 32 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
		V _{CB} = 32 V; I _E = 0 A; T _j = 150 °C		-	-	50	µA
I _{CES}	collector-emitter cut-off current	V _{CE} = 32 V; V _{BE} = 0 V; T _{amb} = 25 °C		-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 7 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C		230	370	-	
		V _{CE} = 2 V; I _C = 1 A; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		220	360	-	
		V _{CE} = 2 V; I _C = 5 A; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		140	230	-	
		V _{CE} = 2 V; I _C = 10 A; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C; pulsed		55	90	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 1 A; I _B = 50 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C; pulsed		-	35	50	mV
		I _C = 5 A; I _B = 500 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	140	200	mV
		I _C = 10 A; I _B = 500 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	330	460	mV
R _{CEsat}	collector-emitter saturation resistance	I _C = 10 A; I _B = 1 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	28	40	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 1 A; I _B = 50 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	-	0.95	V
		I _C = 5 A; I _B = 500 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	-	1.2	V
		I _C = 10 A; I _B = 500 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	-	1.3	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C		-	-	0.8	V
t _d	delay time	V _{CC} = 12.5 V; I _C = 5 A; I _{Bon} = 250 mA; I _{Boff} = -250 mA; T _{amb} = 25 °C		-	15	-	ns
t _r	rise time			-	130	-	ns
t _{on}	turn-on time			-	145	-	ns
t _s	storage time			-	310	-	ns
t _f	fall time			-	95	-	ns
t _{off}	turn-off time			-	405	-	ns
f _T	transition frequency			-	128	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	57	-	pF

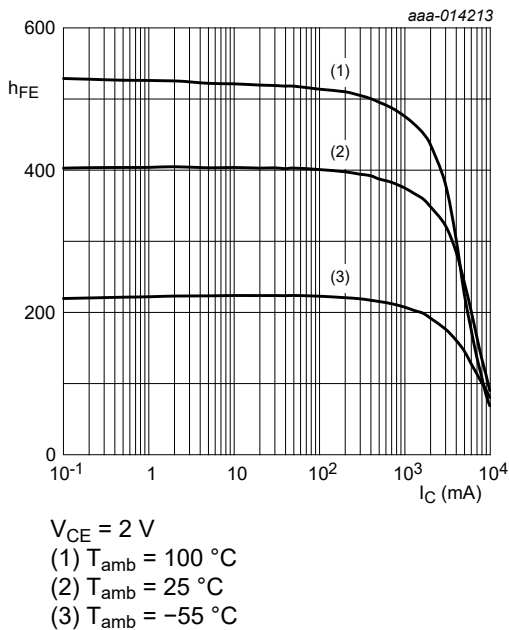


Fig. 4. DC current gain as a function of collector current; typical values

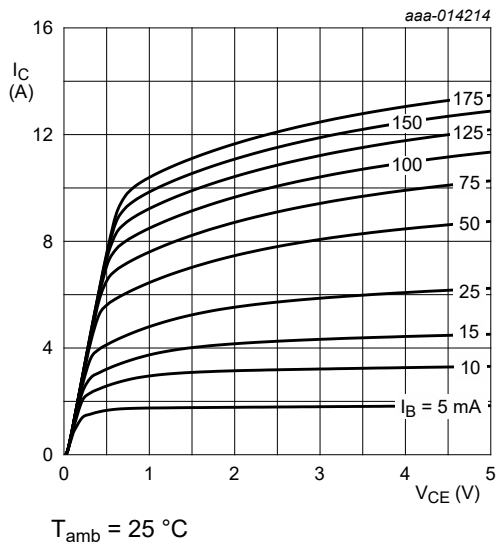


Fig. 5. Collector current as a function of collector-emitter voltage; typical values

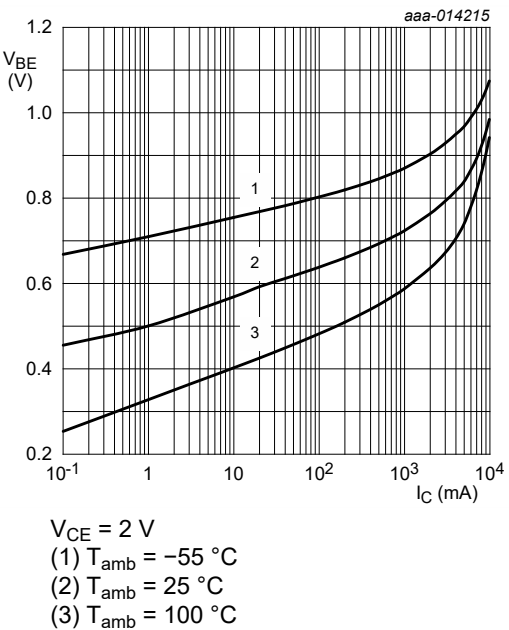


Fig. 6. Base-emitter voltage as a function of collector current; typical values

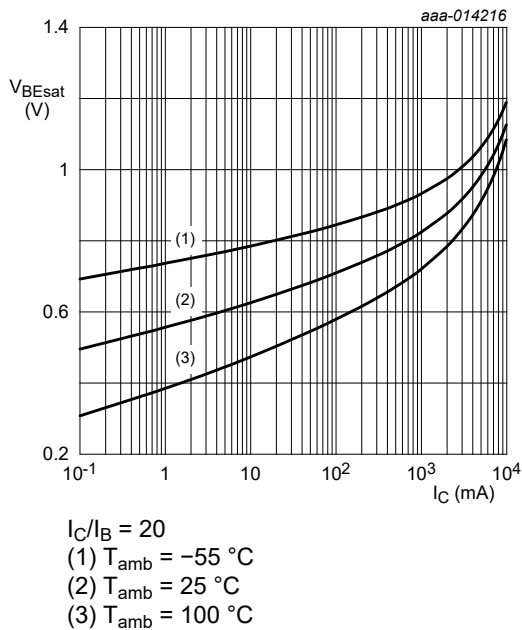


Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

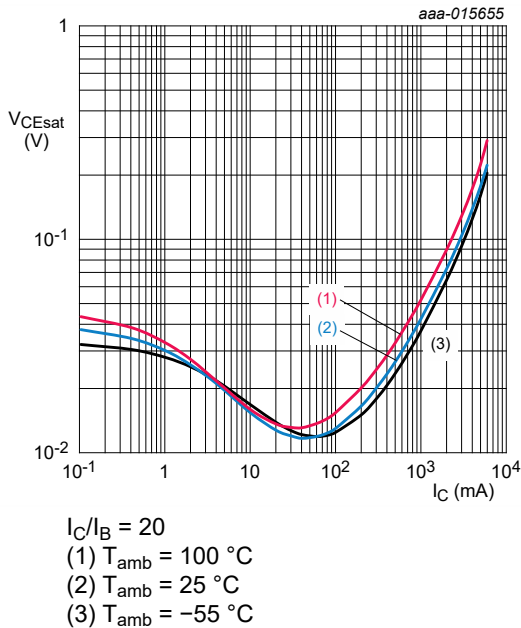


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

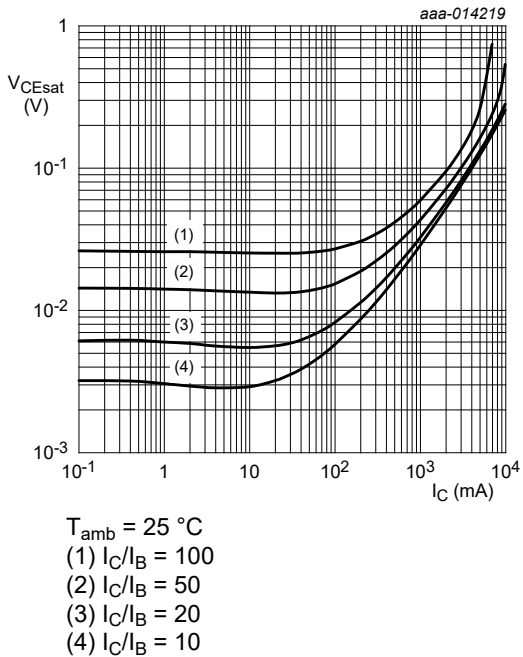


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

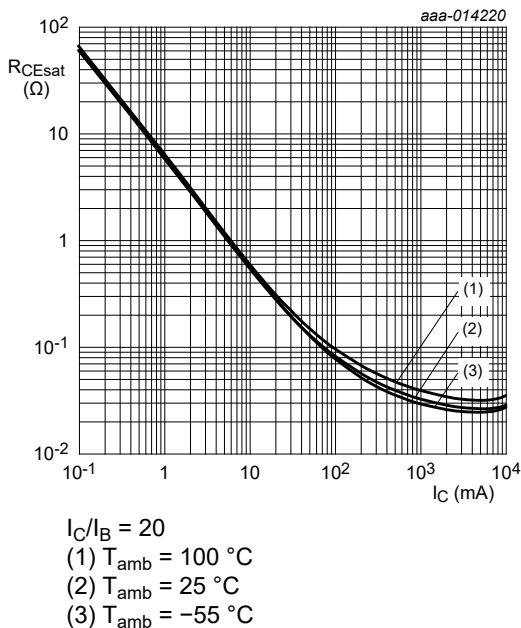


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

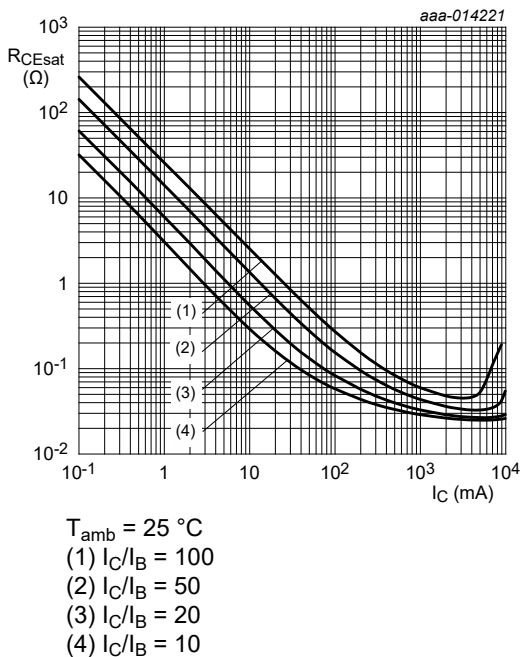


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

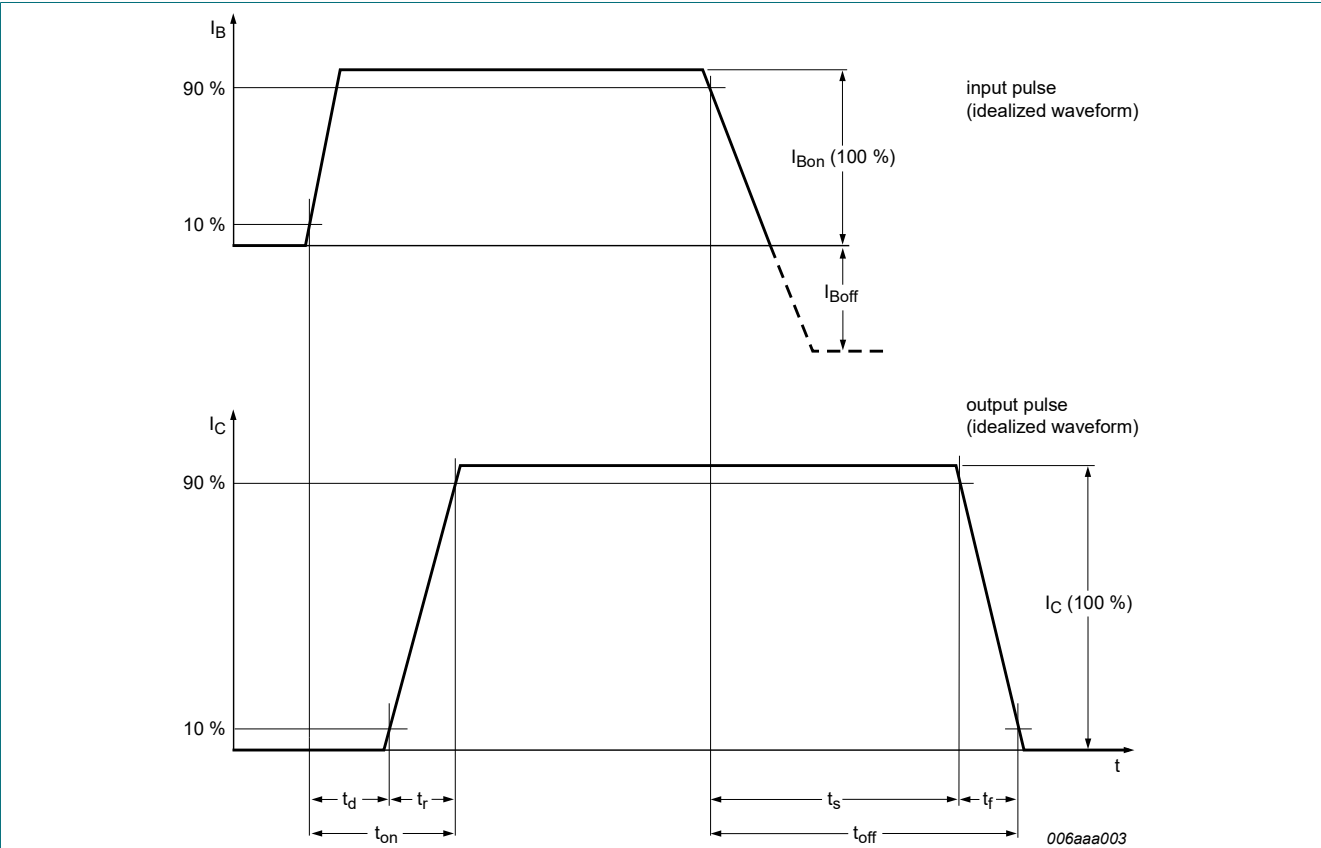


Fig. 12. Switching time definition

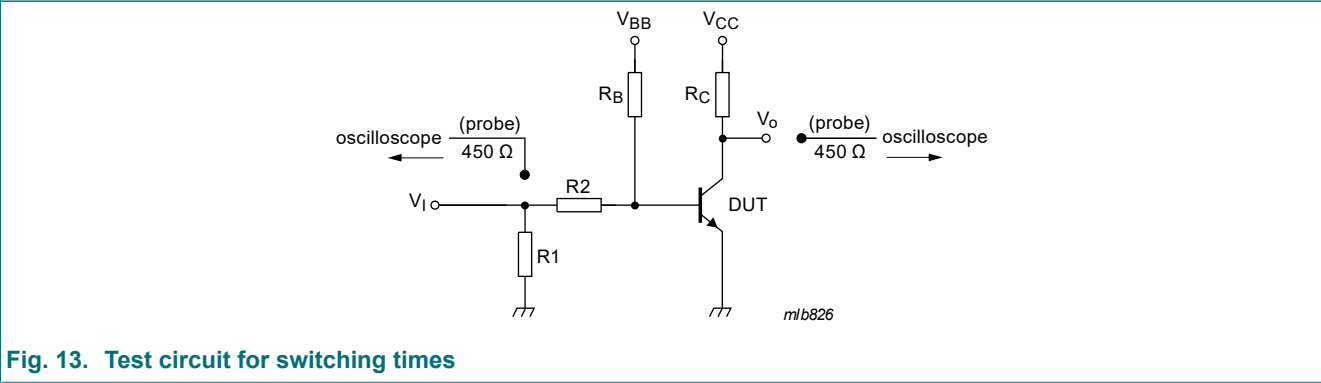


Fig. 13. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

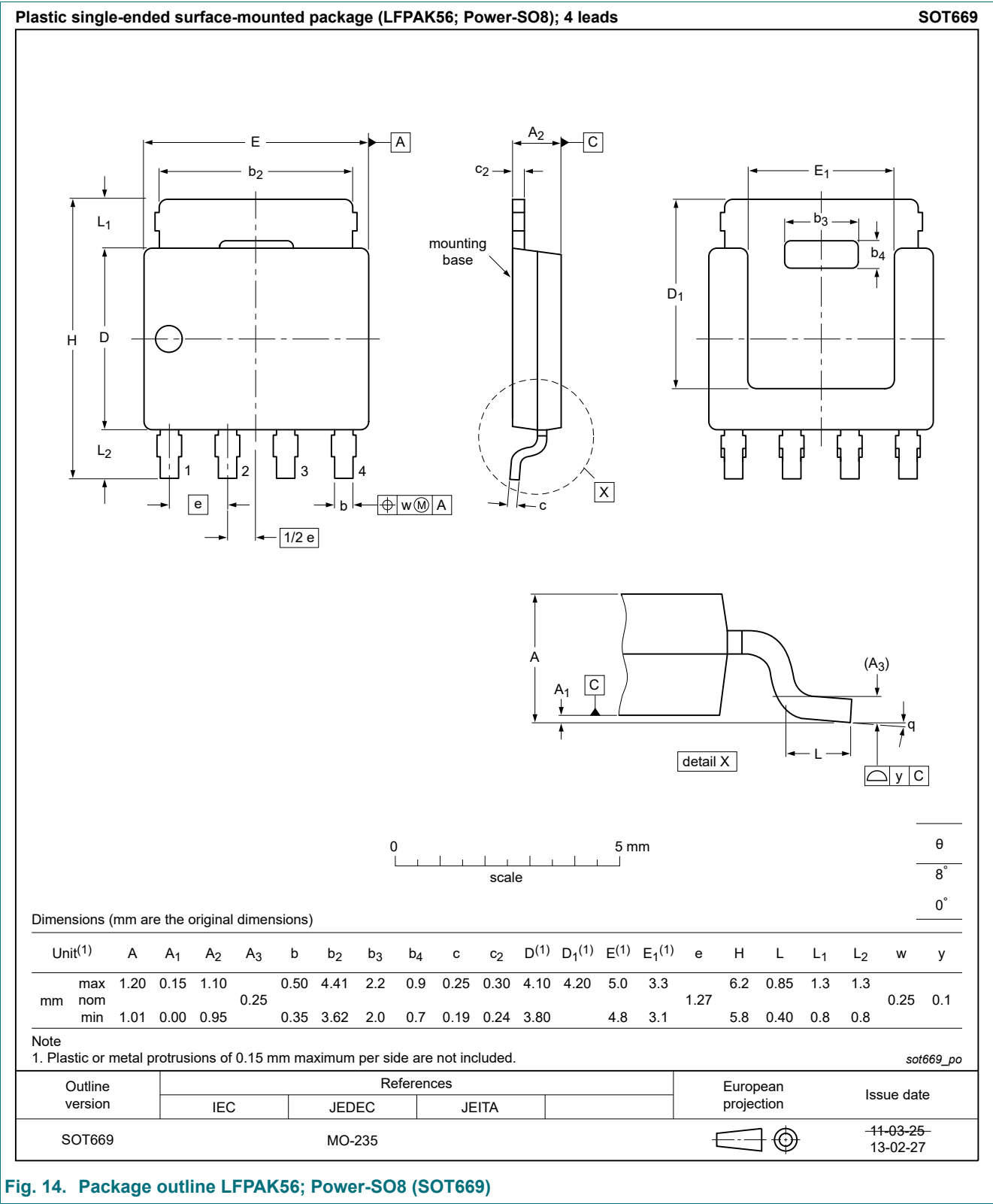


Fig. 14. Package outline LPAK56; Power-SO8 (SOT669)

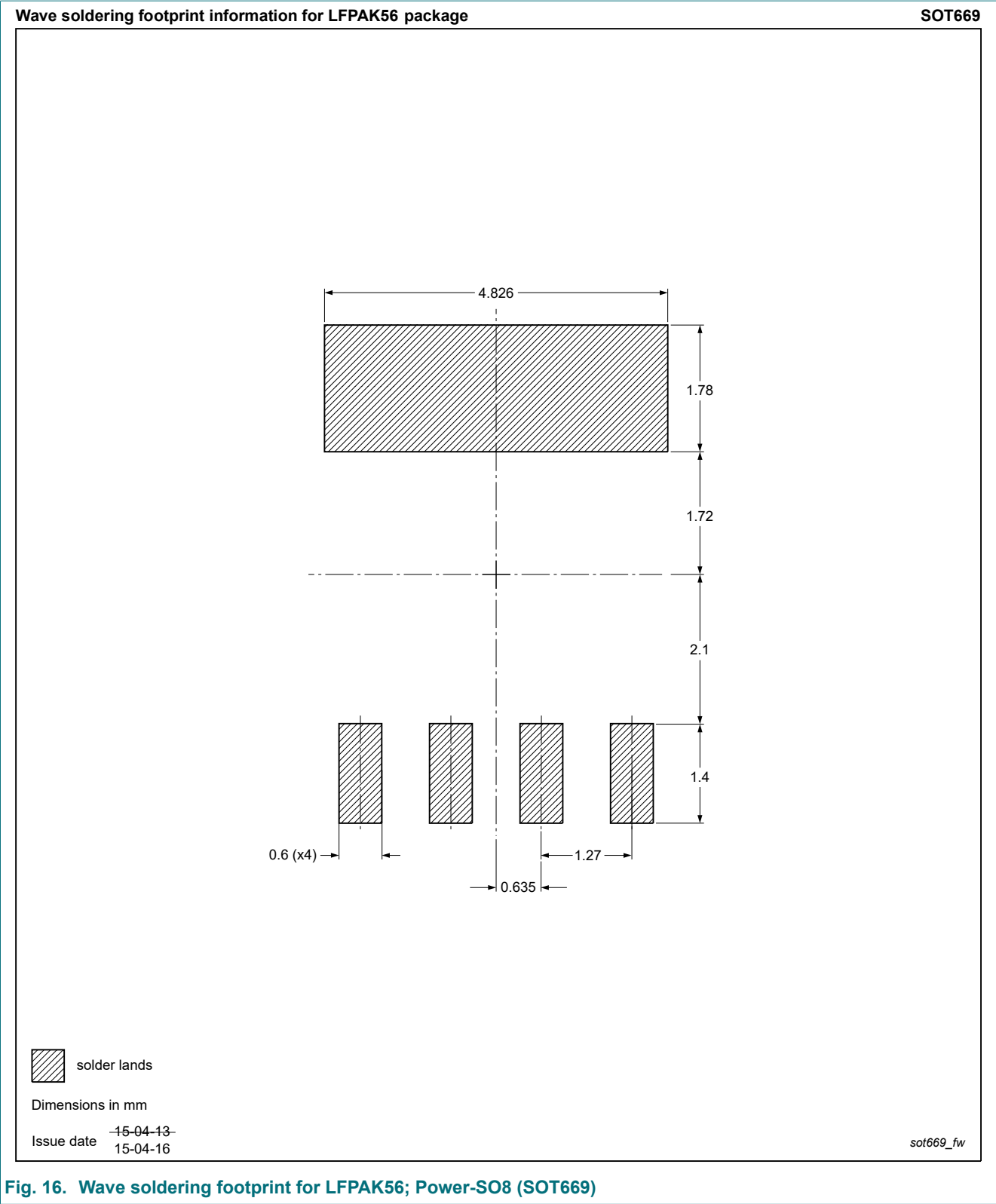


Fig. 16. Wave soldering footprint for LFPAK56; Power-SO8 (SOT669)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PHPT60410NY-Q v.1	20240927	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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