



PNU650100EJ

650 V, 10 A ultrafast recovery rectifier

3 May 2024

Product data sheet

1. General description

High power density, ultrafast switching time recovery rectifier with high-efficiency planar technology, encapsulated in D2PAK Real-2-Pin (SOT8018).

2. Features and benefits

- Reverse voltage $V_R \leq 650$ V
- Forward current $I_F \leq 10$ A
- Typical switching time t_{tr} of 27 ns
- Pt doped life time control
- Low inductance

3. Applications

- AC/DC converter
- DC/DC converter
- SMPS/UPS
- Battery charger
- Inverter
- Freewheeling applications

4. Quick reference data

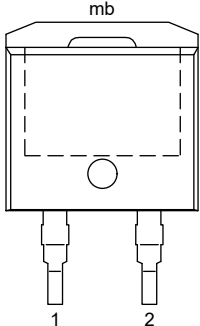
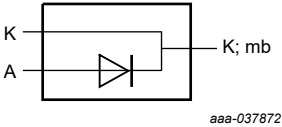
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20$ kHz; square wave; $T_c \leq 142$ °C		-	-	10	A
V_{RRM}	repetitive peak reverse voltage	$T_j = 25$ °C		-	-	650	V
V_R	reverse voltage			-	-	650	V
V_F	forward voltage	$I_F = 10$ A; pulsed; $T_j = 25$ °C	[1]	-	1.2	1.55	V
		$I_F = 10$ A; pulsed; $T_j = 125$ °C	[1]	-	1.1	1.4	V
		$I_F = 10$ A; pulsed; $T_j = 175$ °C	[1]	-	1	-	V
I_R	reverse current	$V_R = 650$ V; pulsed; $T_j = 25$ °C	[1]	-	-	5	μ A
		$V_R = 650$ V; pulsed; $T_j = 125$ °C	[1]	-	1.4	50	μ A
		$V_R = 650$ V; pulsed; $T_j = 175$ °C	[1]	-	31	-	μ A

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 D2PAK R2P (SOT8018)	 aaa-037872
2	A	anode		
mb	K	mounting base; connected to cathode, also referred to as the case		

6. Marking

Table 3. Marking codes

Type number	Marking code
PNU650100EJ	U65010

7. Limiting values

Table 4. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 601134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	650	V
V _R	reverse voltage			-	650	V
V _{RMS}	RMS voltage			-	460	V
I _F	forward current	δ = 1; T _c ≤ 133 °C		-	14	A
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _c ≤ 142 °C		-	10	A
I _{FSM}	non-repetitive peak forward current	t _p = 8.3 ms; single half sine wave (applied at rated load condition); T _{j(init)} = 25 °C		-	120	A
		t _p = 10 ms; square wave; T _{j(init)} = 25 °C		-	99	A
P _{tot}	total power dissipation	T _c ≤ 25 °C	[1]	-	2.4	W
			[2]	-	4	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 6 cm².

8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	62	K/W
			[2]	-	-	37	K/W
$R_{th(j-c)}$	thermal resistance from junction to case		[3]	-	-	2.3	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 6 cm².
[3] Soldering point of cathode tab.

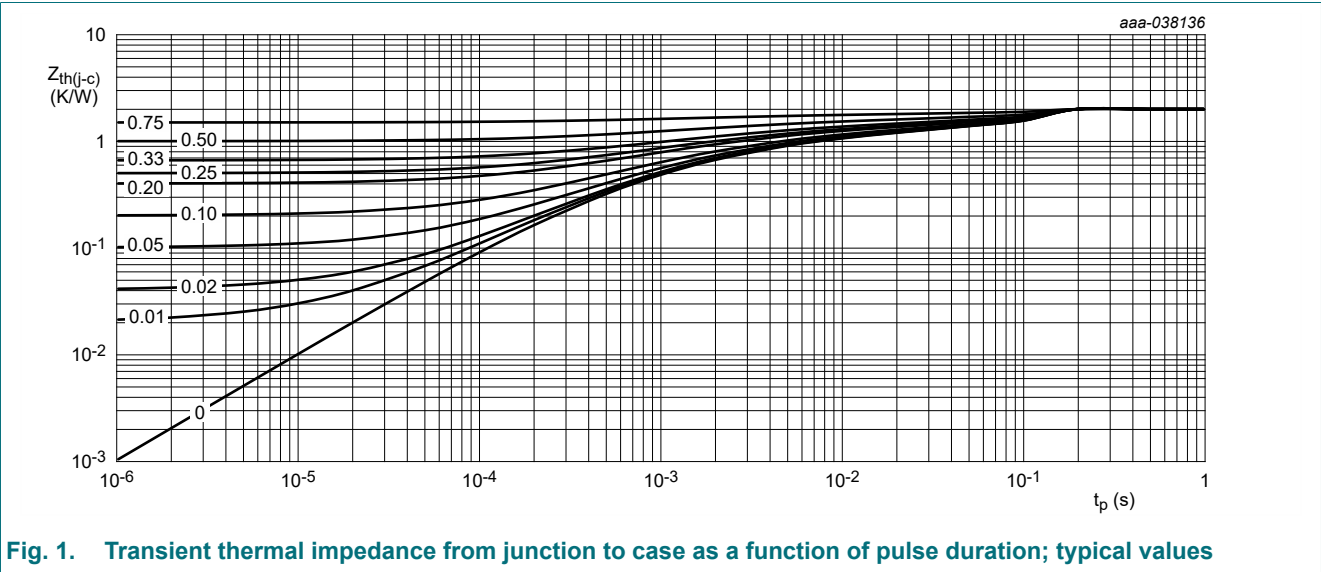


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; typical values

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 100\text{ }\mu\text{A}$; pulsed; $T_j = 25\text{ }^{\circ}\text{C}$	[1]	650	-	-	V
V_F	forward voltage	$I_F = 10\text{ A}$; pulsed; $T_j = 25\text{ }^{\circ}\text{C}$	[1]	-	1.2	1.55	V
		$I_F = 10\text{ A}$; pulsed; $T_j = 125\text{ }^{\circ}\text{C}$	[1]	-	1.1	1.4	V
		$I_F = 10\text{ A}$; pulsed; $T_j = 175\text{ }^{\circ}\text{C}$	[1]	-	1	-	V
I_R	reverse current	$V_R = 650\text{ V}$; pulsed; $T_j = 25\text{ }^{\circ}\text{C}$	[1]	-	-	5	μA
		$V_R = 650\text{ V}$; pulsed; $T_j = 125\text{ }^{\circ}\text{C}$	[1]	-	1.4	50	μA
		$V_R = 650\text{ V}$; pulsed; $T_j = 175\text{ }^{\circ}\text{C}$	[1]	-	31	-	μA
C_d	diode capacitance	$V_R = 400\text{ V}$; $f = 1\text{ MHz}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	7	-	pF
t_{rr}	reverse recovery time ; step recovery	$I_F = 0.5\text{ A}$; $I_R = 1\text{ A}$; $I_{R(meas)} = 0.25\text{ A}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	27	60	ns
	reverse recovery time ; ramp recovery	$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	105	-	ns
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	58	-	ns
		$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	159	-	ns
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	72	-	ns
I_{RM}	peak reverse recovery current	$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	5.1	-	A
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	16.8	-	A
		$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	8.9	-	A
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	27.6	-	A
Q_{rr}	reverse recovery charge	$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	306	-	nC
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	560	-	nC
		$I_F = 10\text{ A}$; $dI_F/dt = -200\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	796	-	nC
		$I_F = 10\text{ A}$; $dI_F/dt = -1000\text{ A}/\mu\text{s}$; $V_R = 400\text{ V}$; $T_j = 125\text{ }^{\circ}\text{C}$		-	1481	-	nC

[1] Very short pulse, in order to maintain a stable junction temperature.

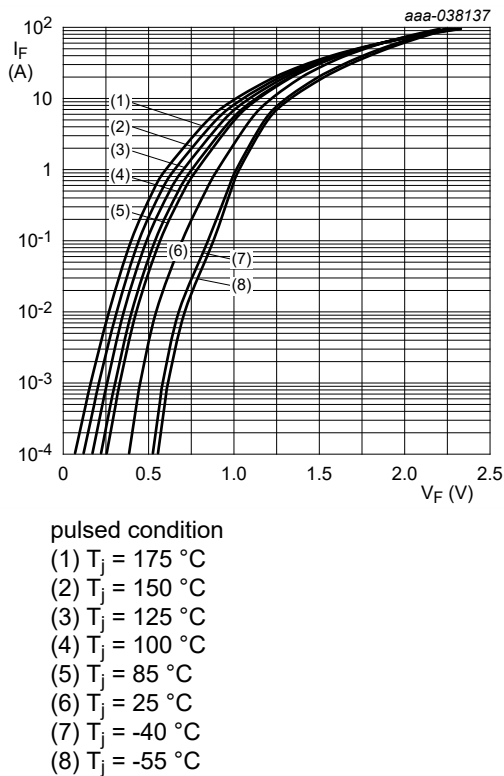


Fig. 2. Forward current as a function of forward voltage; typical values

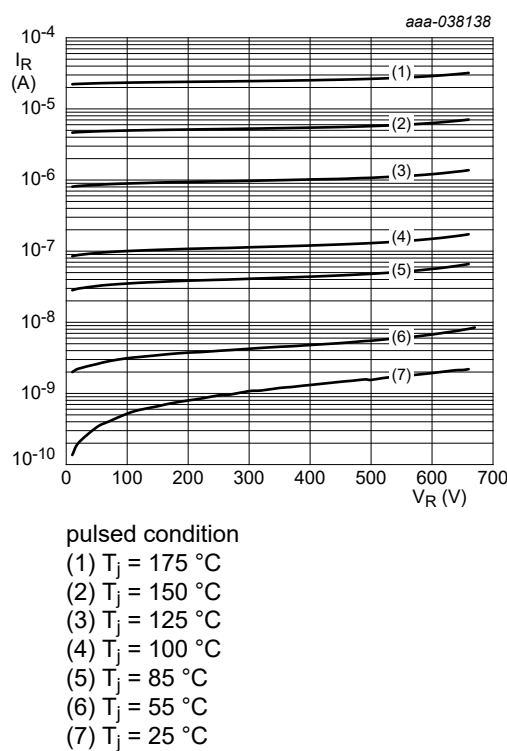


Fig. 3. Reverse current as a function of reverse voltage; typical values

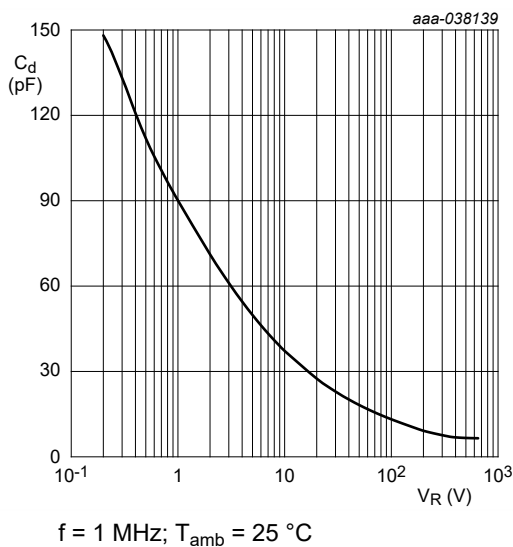


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

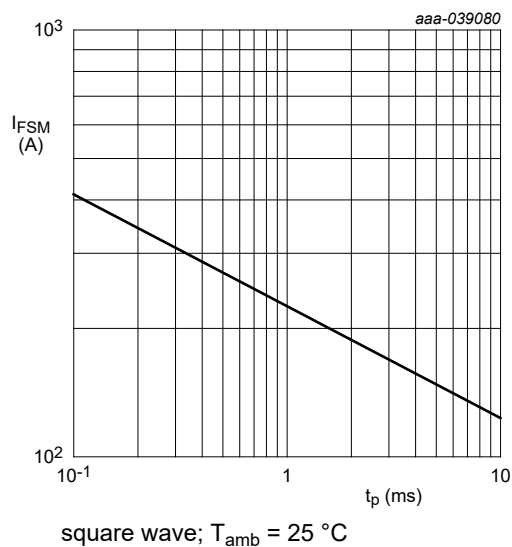


Fig. 5. Non-repetitive peak forward current as a function of pulse duration; typical values

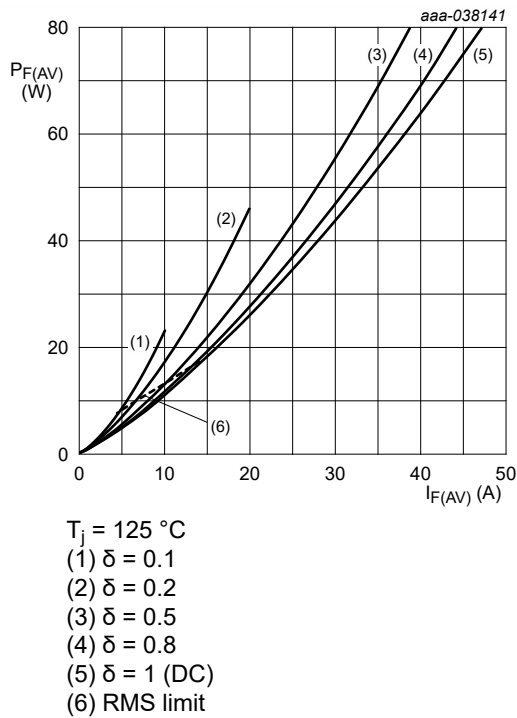


Fig. 6. Average forward power dissipation as a function of average forward current; typical values

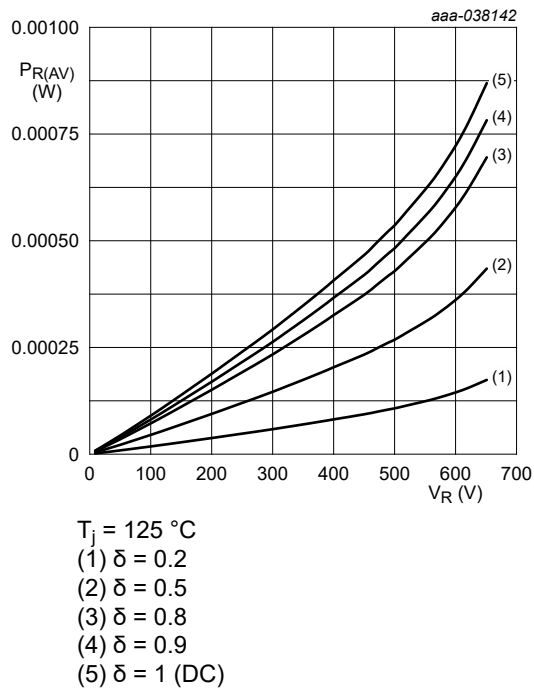


Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values

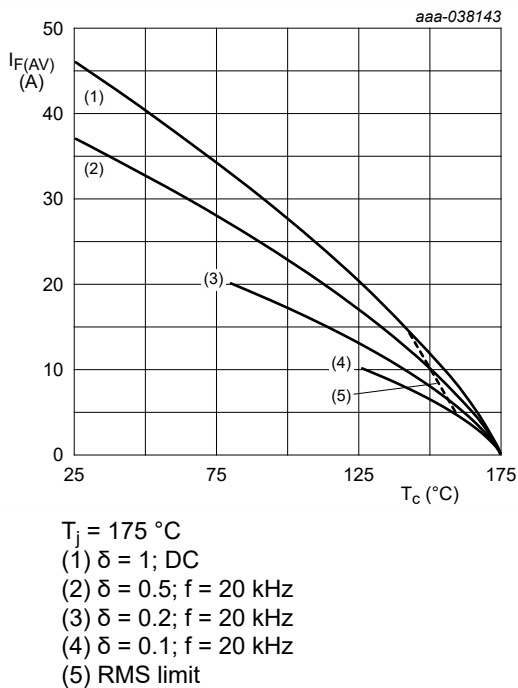


Fig. 8. Average forward current as a function of case temperature; typical values

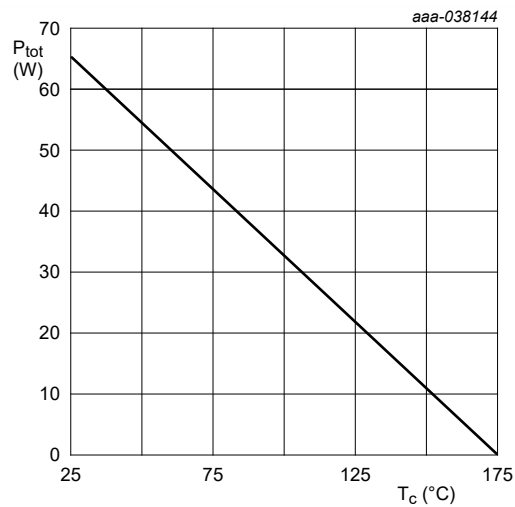
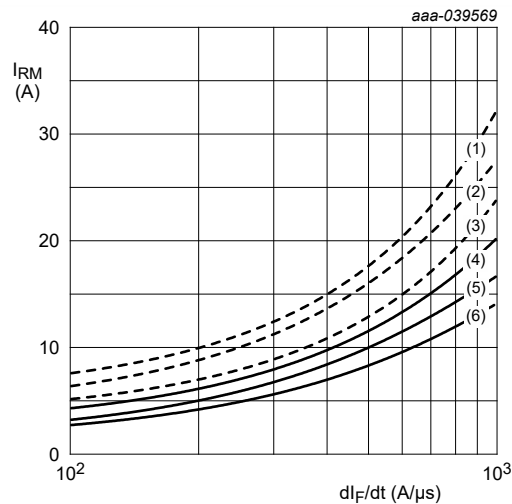
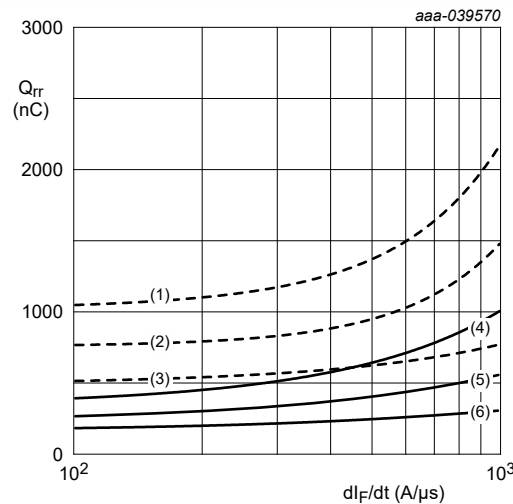


Fig. 9. Power dissipation as a function of case temperature; maximum values



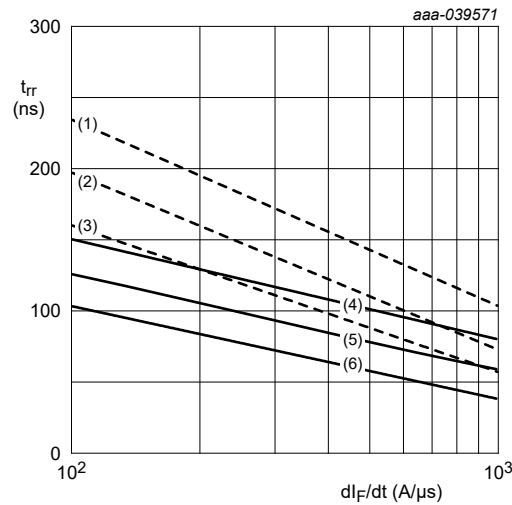
$V_R = 400$ V
(1) $I_F = 20$ A; $T_j = 125$ °C
(2) $I_F = 10$ A; $T_j = 125$ °C
(3) $I_F = 5$ A; $T_j = 125$ °C
(4) $I_F = 20$ A; $T_j = 25$ °C
(5) $I_F = 10$ A; $T_j = 25$ °C
(6) $I_F = 5$ A; $T_j = 25$ °C

Fig. 10. Peak reverse recovery current as a function of ramp rate; typical values



$V_R = 400$ V
(1) $I_F = 20$ A; $T_j = 125$ °C
(2) $I_F = 10$ A; $T_j = 125$ °C
(3) $I_F = 5$ A; $T_j = 125$ °C
(4) $I_F = 20$ A; $T_j = 25$ °C
(5) $I_F = 10$ A; $T_j = 25$ °C
(6) $I_F = 5$ A; $T_j = 25$ °C

Fig. 11. Reverse recovery charge as a function of ramp rate; typical values



$V_R = 400$ V
(1) $I_F = 20$ A; $T_j = 125$ °C
(2) $I_F = 10$ A; $T_j = 125$ °C
(3) $I_F = 5$ A; $T_j = 125$ °C
(4) $I_F = 20$ A; $T_j = 25$ °C
(5) $I_F = 10$ A; $T_j = 25$ °C
(6) $I_F = 5$ A; $T_j = 25$ °C

Fig. 12. Reverse recovery time as a function of ramp rate; typical values

10. Test information



Fig. 13. Reverse recovery definition; step recovery



Fig. 14. Reverse recovery definition; ramp recovery



Fig. 15. Forward recovery definition

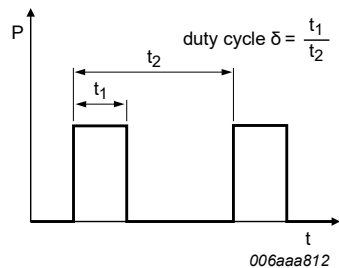


Fig. 16. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

$I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$

with I_{RMS} defined as RMS current.

11. Package outline

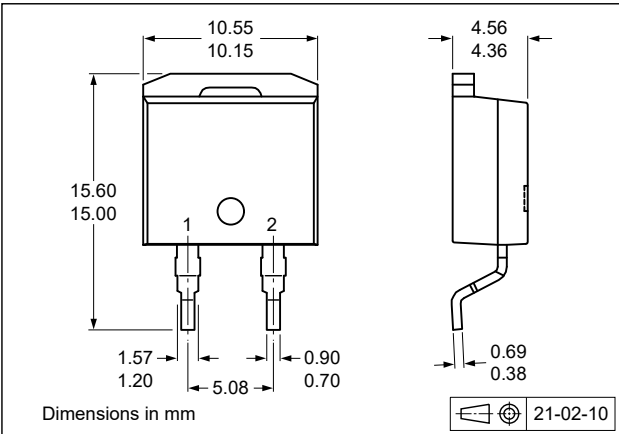


Fig. 17. Package outline D2PAK R2P (SOT8018)

12. Soldering

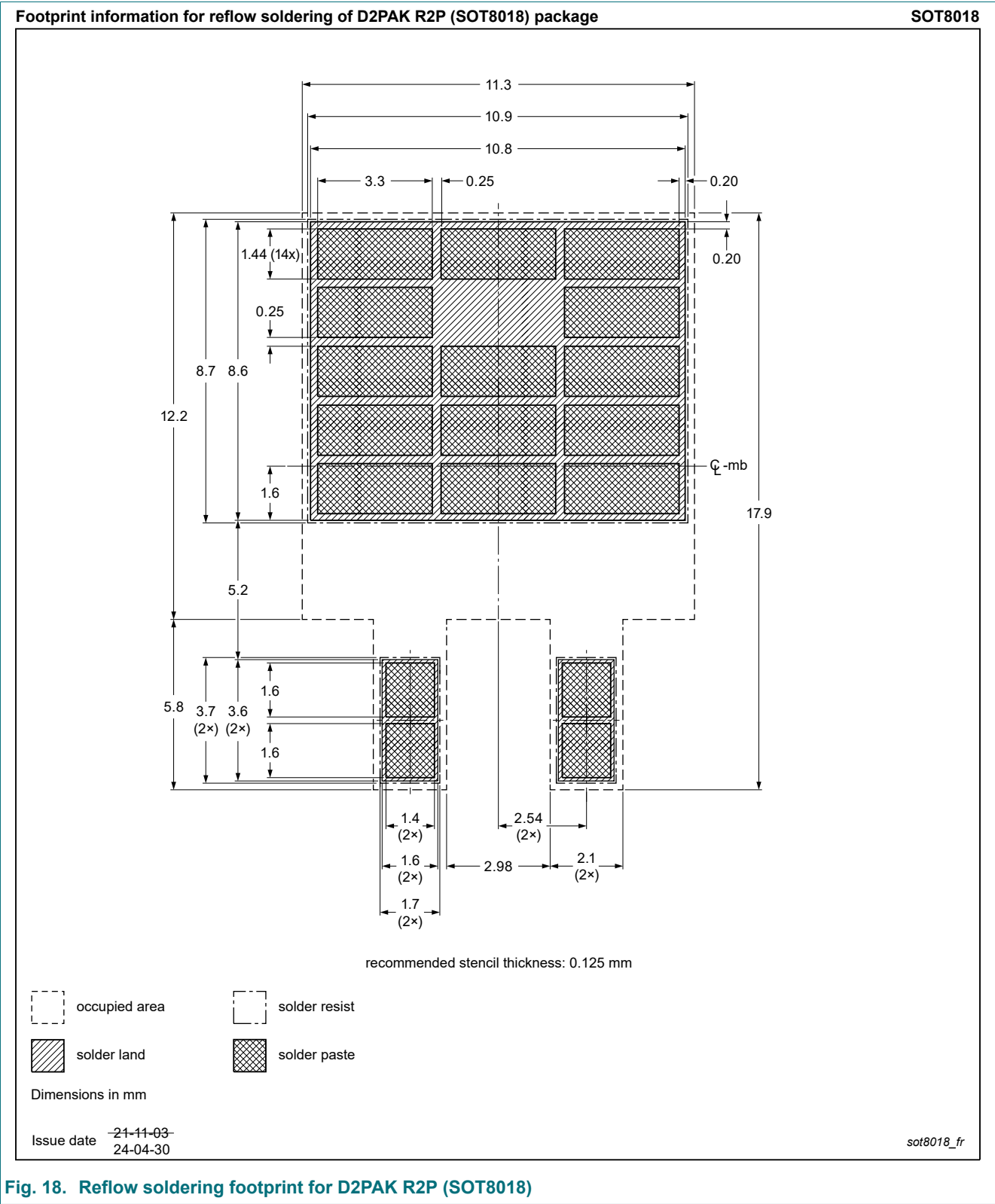


Fig. 18. Reflow soldering footprint for D2PAK R2P (SOT8018)

13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PNU650100EJ v.1	20240503	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Marking..... 2

7. Limiting values..... 3

8. Thermal characteristics..... 4

9. Characteristics.....5

10. Test information.....9

11. Package outline..... 10

12. Soldering..... 11

13. Revision history.....12

14. Legal information.....13

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