



PMCPB5530XA

20 V, complementary Trench MOSFET

18 March 2024

Product data sheet

1. General description

Complementary N/P-channel enhancement mode Field-Effect Transistor (FET) in a small and leadless DFN2020-6 (SOT1118) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Exposed drain pad for excellent thermal conduction
- AEC-Q101 qualified

3. Applications

- DC to DC conversion
- High-speed line driver
- High/Low-side load switch
- Switching circuits

4. Quick reference data

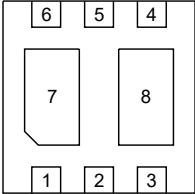
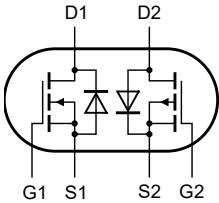
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
TR1 (N-channel)							
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$		-	-	20	V
V_{GS}	gate-source voltage			-10	-	10	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	-	4.5	A
TR2 (P-channel)							
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$		-	-	-20	V
V_{GS}	gate-source voltage			-10	-	10	V
I_D	drain current	$V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	-	-3.6	A
TR1 (N-channel), Static characteristics							
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 4.5\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$		-	26	34	m Ω
TR2 (P-channel), Static characteristics							
R_{DSon}	drain-source on-state resistance	$V_{GS} = -4.5\text{ V}; I_D = -3.6\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$		-	50	66	m Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 Transparent top view DFN2020-6 (SOT1118)	 017aaa261
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		
7	D1	drain TR1		
8	D2	drain TR2		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMCPB5530XA	DFN2020-6	plastic, leadless thermal enhanced ultra thin small outline package; no leads; 6 terminals; 0.65 mm pitch; 2 mm x 2 mm x 0.65 mm body	SOT1118

7. Marking

Table 4. Marking codes

Type number	Marking code
PMCPB5530XA	8L

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
TR1 (N-channel)						
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-10	10	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	4.5	A
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	2.8	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	48	A
TR2 (P-channel)						
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-10	10	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-3.6	A
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-2.3	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	-37	A
TR1 and TR2 (per transistor)						
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	490	mW
			[1]	-	1.2	W
		T _{sp} = 25 °C		-	8.3	W
Per device						
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	640	mW
			[1]	-	1.6	W
		T _{sp} = 25 °C		-	11	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
TR1 (N-channel), Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	1.2	A
TR1 (N-channel), Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	T _{j(init)} = 25 °C; I _D = 0.5 A; DUT in avalanche (unclamped)		-	4.5	mJ
TR2 (P-channel), Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	-1.2	A
TR2 (P-channel), Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	T _{j(init)} = 25 °C; I _D = -0.65 A; DUT in avalanche (unclamped)		-	4.4	mJ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.

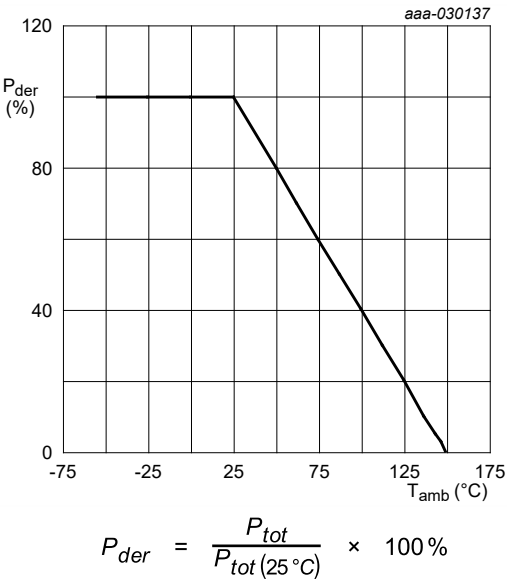


Fig. 1. Normalized total power dissipation as a function of ambient temperature

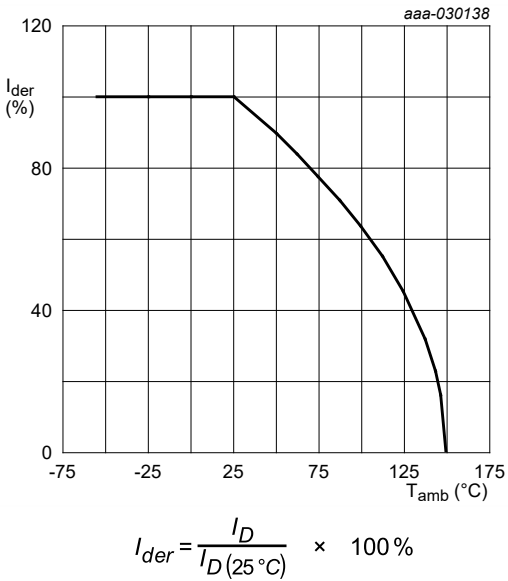


Fig. 2. Normalized continuous drain current as a function of ambient temperature

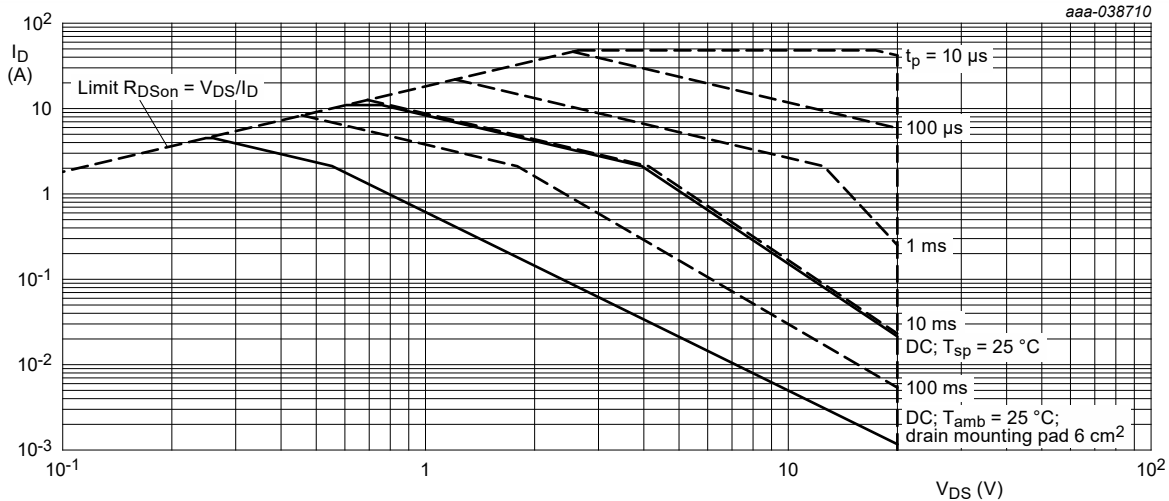
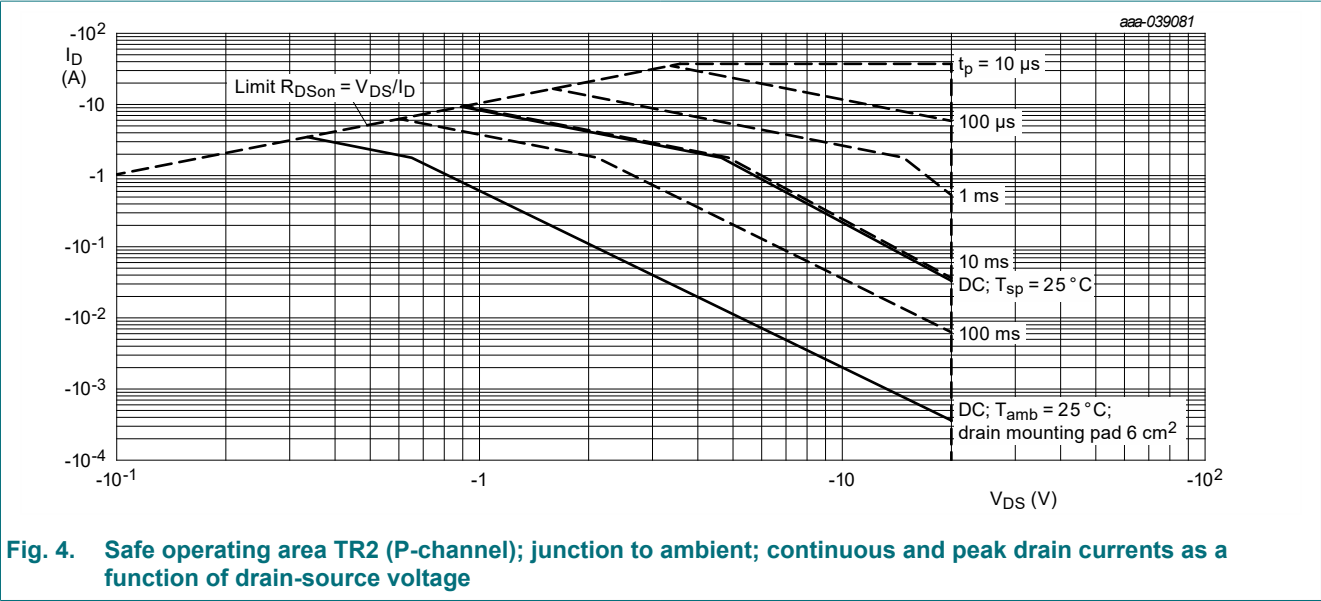


Fig. 3. Safe operating area TR1 (N-channel); junction to ambient; continuous and peak drain currents as a function of drain-source voltage

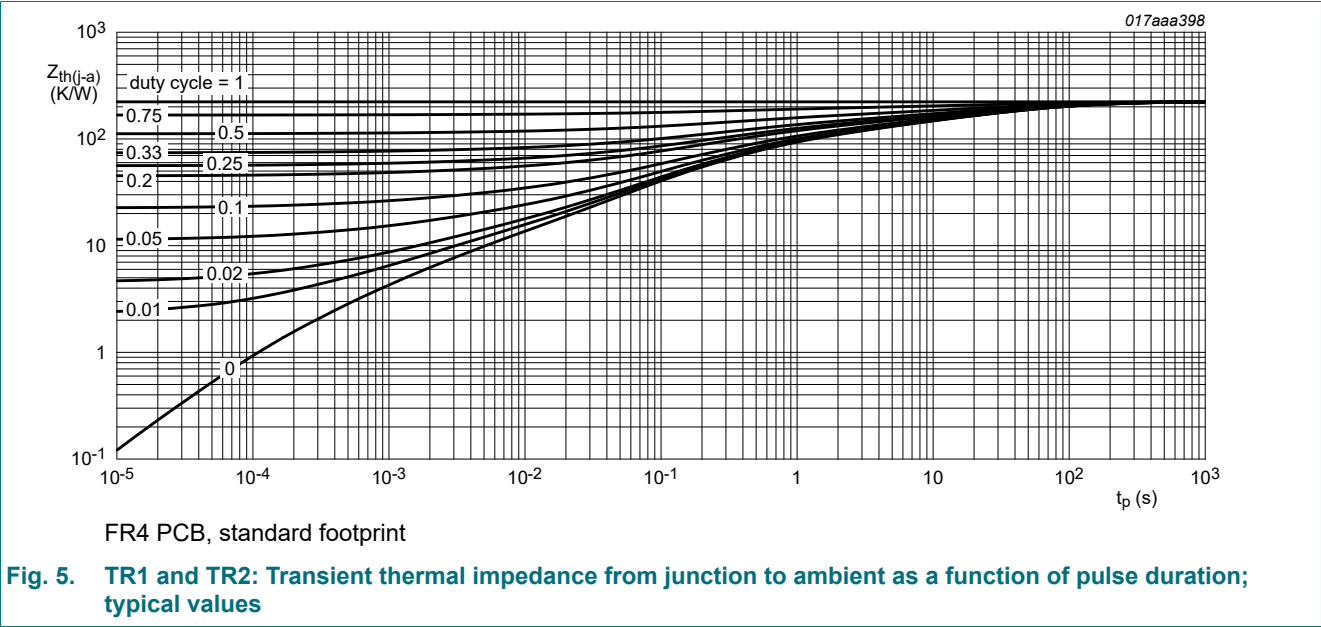


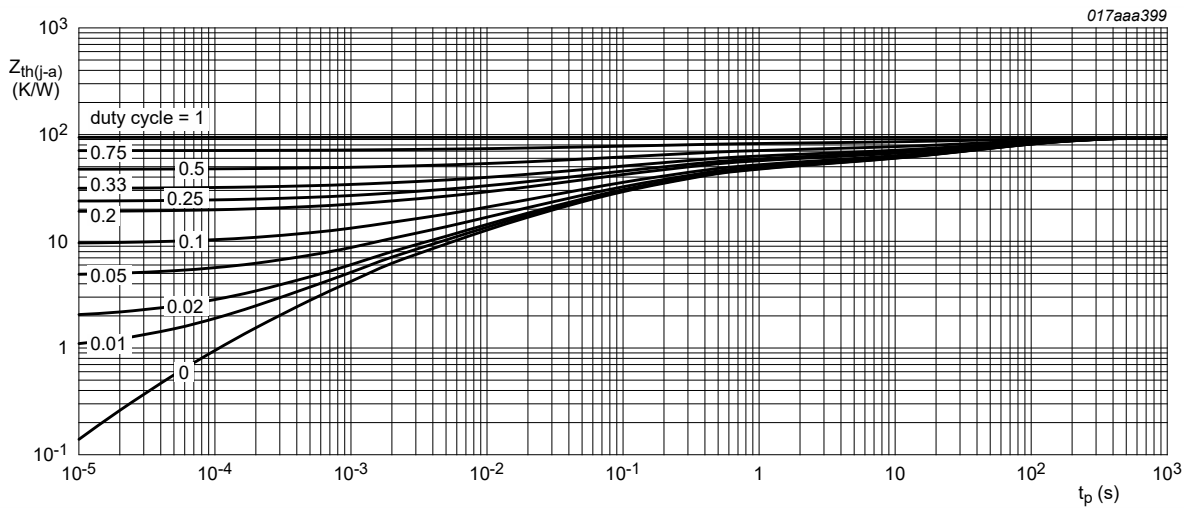
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
TR1 and TR2, per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	223	256	K/W
			[2]	-	93	107	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	10	15	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	190	K/W
			[2]	-	-	80	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	11	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².





FR4 PCB, mounting pad for drain 6 cm²

Fig. 6. TR1 and TR2: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
TR1 (N-channel), Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C		20	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS} ; T _j = 25 °C		0.4	0.65	0.9	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 4.5 A; T _j = 25 °C		-	26	34	mΩ
		V _{GS} = 4.5 V; I _D = 4.5 A; T _j = 150 °C		-	42	55	mΩ
		V _{GS} = 2.5 V; I _D = 4 A; T _j = 25 °C		-	33	46	mΩ
		V _{GS} = 1.8 V; I _D = 0.5 A; T _j = 25 °C		-	50	80	mΩ
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 4.3 A; T _j = 25 °C		-	8	-	S
TR1 (N-channel), Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = 10 V; I _D = 4.3 A; V _{GS} = 4.5 V; T _j = 25 °C		-	6.6	10	nC
Q _{GS}	gate-source charge			-	0.9	-	nC
Q _{GD}	gate-drain charge			-	1.5	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	619	-	pF
C _{oss}	output capacitance			-	60	-	pF
C _{rss}	reverse transfer capacitance			-	53	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 10 V; I _D = 4.3 A; V _{GS} = 4.5 V; R _{G(ext)} = 6 Ω; T _j = 25 °C		-	6.5	-	ns
t _r	rise time			-	2.3	-	ns
t _{d(off)}	turn-off delay time			-	10	-	ns
t _f	fall time			-	25	-	ns
TR1 (N-channel), Source-drain diode							
V _{SD}	source-drain voltage	I _S = 1.2 A; V _{GS} = 0 V; T _j = 25 °C		-	0.7	1.2	V
t _{rr}	reverse recovery time	I _S = 2 A; dI _S /dt = -75 A/μs; V _{GS} = 4.5 V; V _{DS} = 20 V; T _j = 25 °C		-	6.4	-	ns
Q _r	recovered charge			-	0.8	-	nC
TR2 (P-channel), Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C		-20	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = -250 μA; V _{DS} = V _{GS} ; T _j = 25 °C		-0.47	-0.65	-1	V
I _{DSS}	drain leakage current	V _{DS} = -20 V; V _{GS} = 0 V; T _j = 25 °C		-	-	-1	μA
I _{GSS}	gate leakage current	V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-100	nA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = -4.5 V; I _D = -3.6 A; T _j = 25 °C		-	50	66	mΩ
		V _{GS} = -4.5 V; I _D = -3.6 A; T _j = 150 °C		-	73	96	mΩ
		V _{GS} = -2.5 V; I _D = -2.6 A; T _j = 25 °C		-	62	87	mΩ
		V _{GS} = -1.8 V; I _D = -0.5 A; T _j = 25 °C		-	83	135	mΩ

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
g_{fs}	forward transconductance	$V_{DS} = -10\text{ V}$; $I_D = -3.6\text{ A}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	12	-	S
TR2 (P-channel), Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$V_{DS} = -10\text{ V}$; $I_D = -3.6\text{ A}$; $V_{GS} = -4.5\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	8.8	13	nC
Q_{GS}	gate-source charge			-	1.2	-	nC
Q_{GD}	gate-drain charge			-	2.4	-	nC
C_{iss}	input capacitance	$V_{DS} = -10\text{ V}$; $f = 1\text{ MHz}$; $V_{GS} = 0\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	785	-	pF
C_{oss}	output capacitance			-	70	-	pF
C_{rss}	reverse transfer capacitance			-	62	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = -10\text{ V}$; $I_D = -3.6\text{ A}$; $V_{GS} = -4.5\text{ V}$; $R_{G(ext)} = 6\text{ }\Omega$; $T_j = 25\text{ }^{\circ}\text{C}$		-	11	-	ns
t_r	rise time			-	2	-	ns
$t_{d(off)}$	turn-off delay time			-	55	-	ns
t_f	fall time			-	135	-	ns
TR2 (P-channel), Source-drain diode							
V_{SD}	source-drain voltage	$I_S = -1.2\text{ A}$; $V_{GS} = 0\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	-0.8	-1.2	V
t_{rr}	reverse recovery time	$I_S = -2\text{ A}$; $dI_S/dt = 60\text{ A}/\mu\text{s}$; $V_{GS} = -4.5\text{ V}$; $V_{DS} = -20\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$		-	14	-	ns
Q_r	recovered charge			-	4.8	-	nC

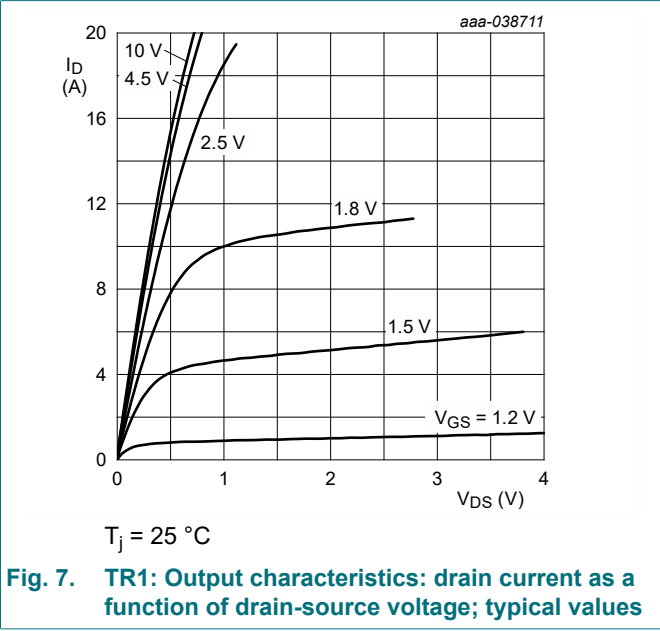


Fig. 7. TR1: Output characteristics: drain current as a function of drain-source voltage; typical values

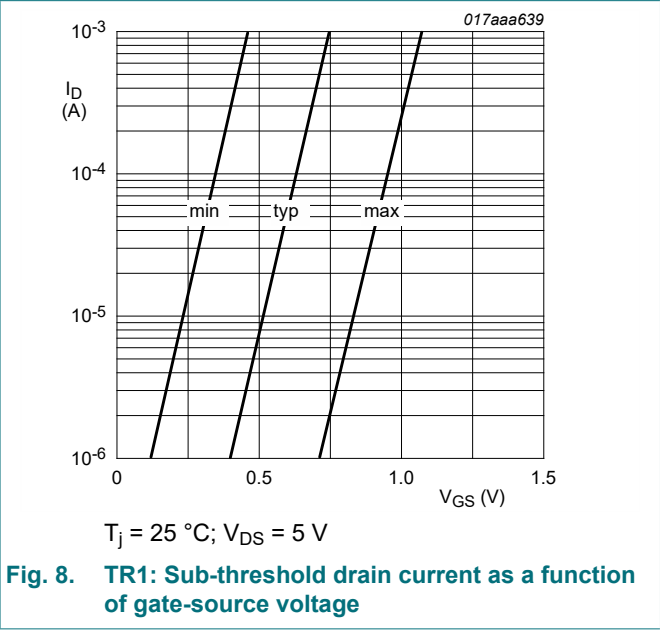


Fig. 8. TR1: Sub-threshold drain current as a function of gate-source voltage

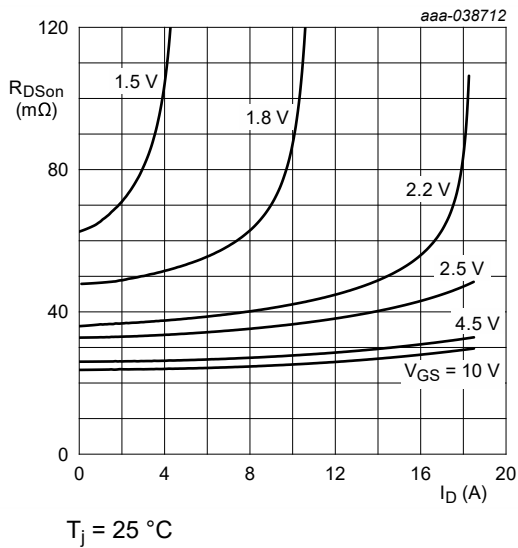


Fig. 9. TR1: Drain-source on-state resistance as a function of drain current; typical values

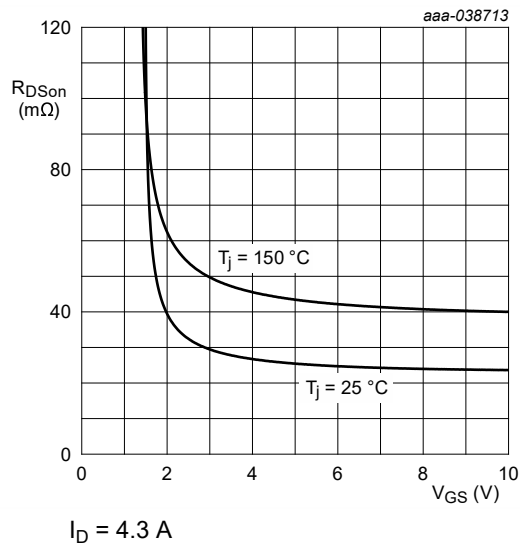


Fig. 10. TR1: Drain-source on-state resistance as a function of gate-source voltage; typical values

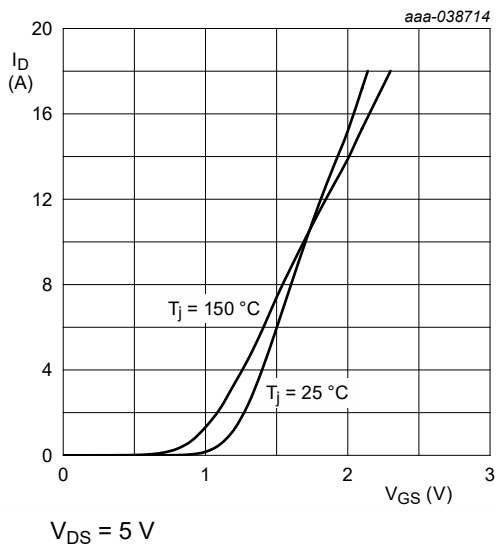


Fig. 11. TR1: Transfer characteristics: drain current as a function of gate-source voltage; typical values

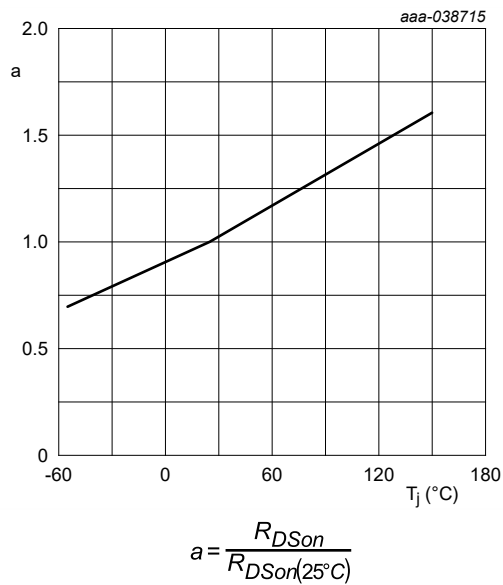


Fig. 12. TR1: Normalized drain-source on-state resistance as a function of junction temperature; typical values

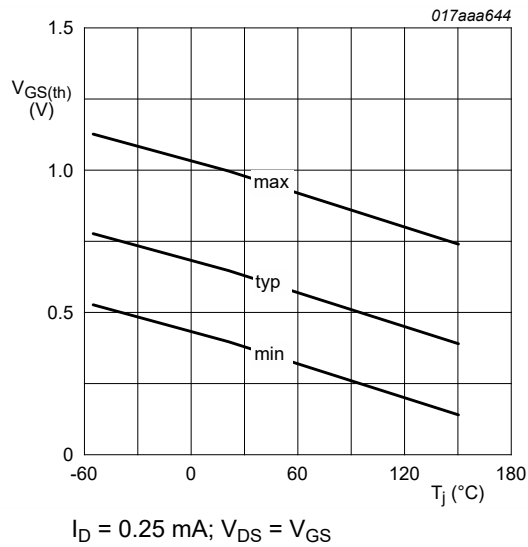


Fig. 13. TR1: Gate-source threshold voltage as a function of junction temperature

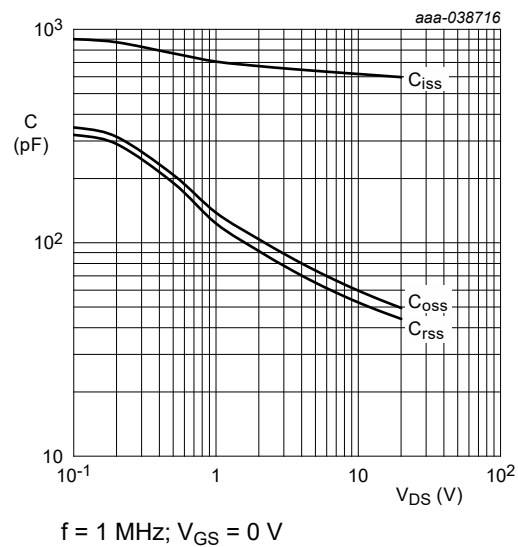


Fig. 14. TR1: Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

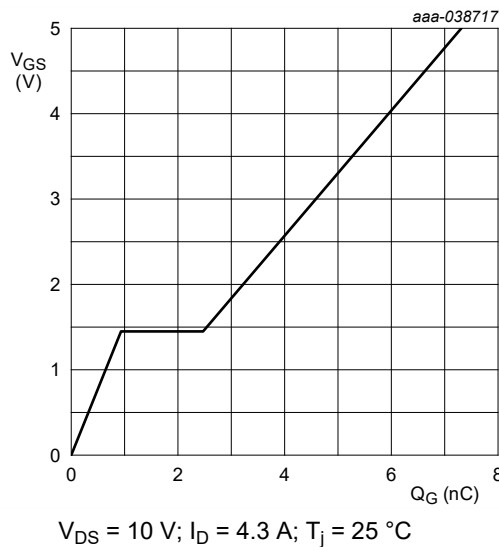


Fig. 15. TR1: Gate-source voltage as a function of gate charge; typical values

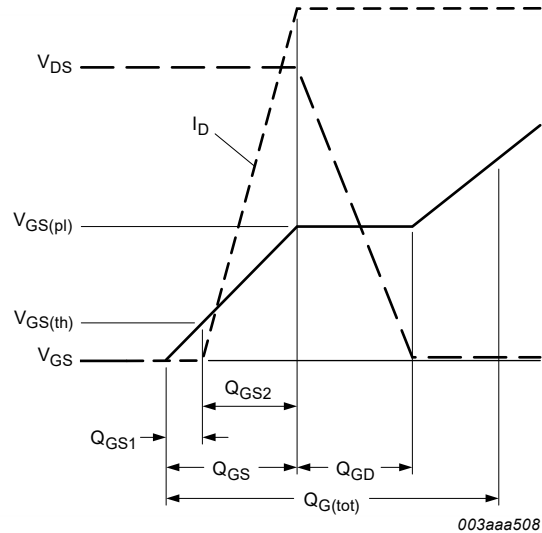
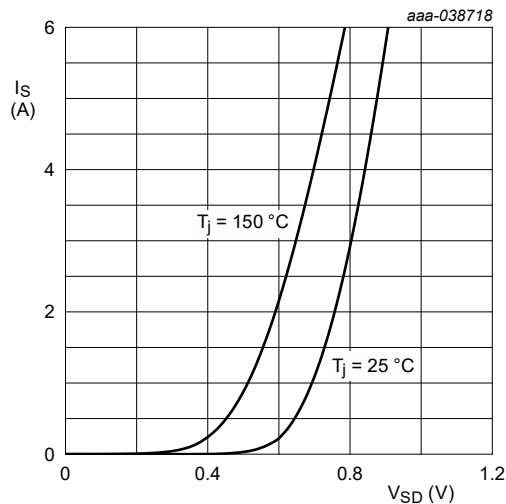


Fig. 16. Gate charge waveform definitions



$V_{GS} = 0\text{ V}$

Fig. 17. TR1: Source current as a function of source-drain voltage; typical values

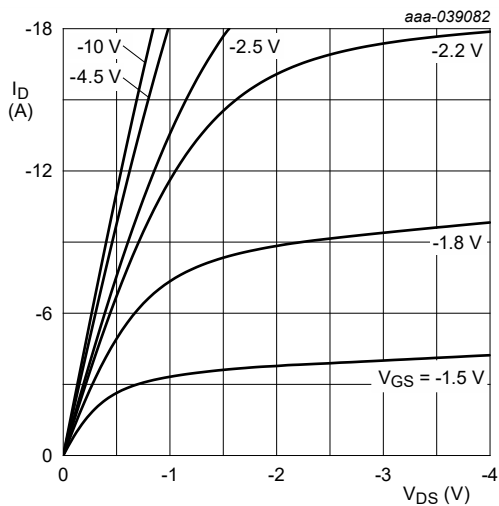


Fig. 18. TR2: Output characteristics: drain current as a function of drain-source voltage; typical values

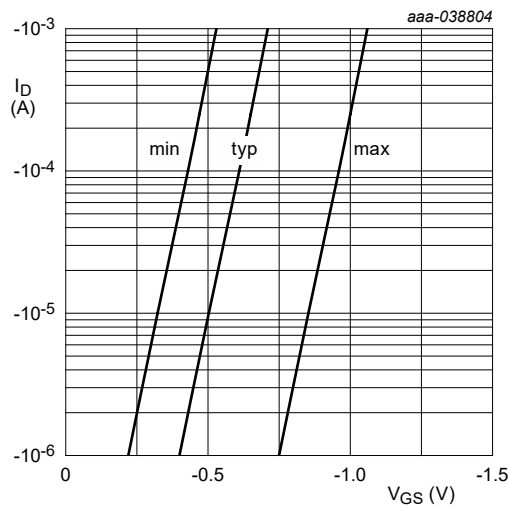


Fig. 19. TR2: Sub-threshold drain current as a function of gate-source voltage

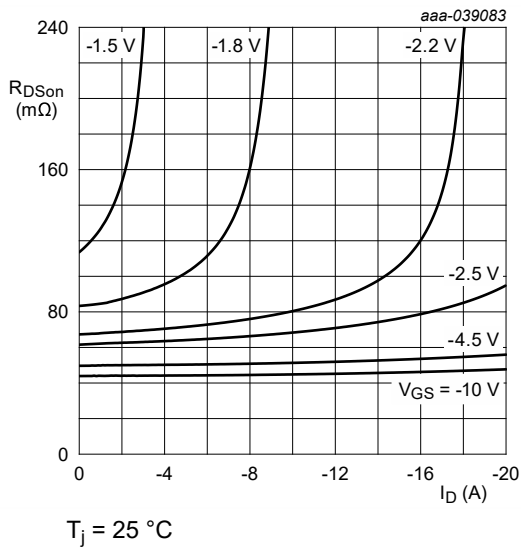


Fig. 20. TR2: Drain-source on-state resistance as a function of drain current; typical values

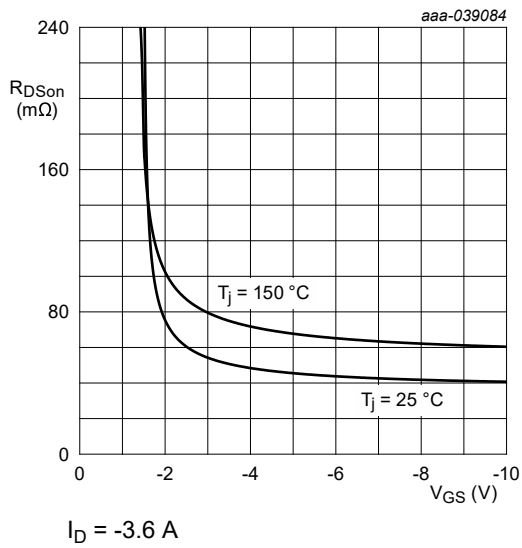


Fig. 21. TR2: Drain-source on-state resistance as a function of gate-source voltage; typical values

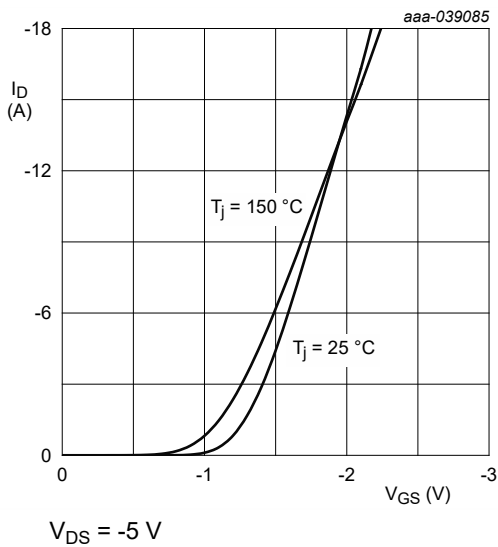


Fig. 22. TR2: Transfer characteristics: drain current as a function of gate-source voltage; typical values

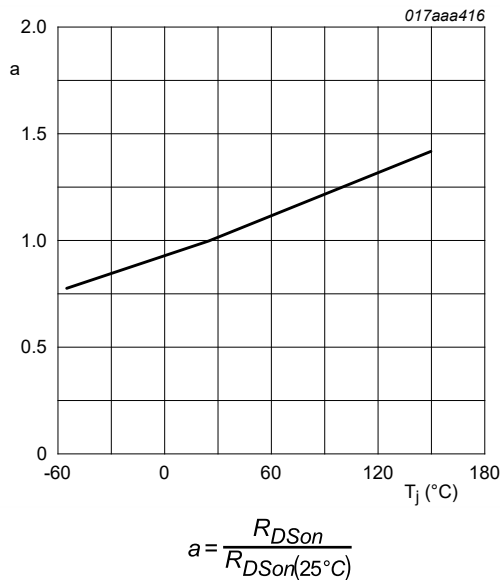


Fig. 23. TR2: Normalized drain-source on-state resistance as a function of junction temperature; typical values

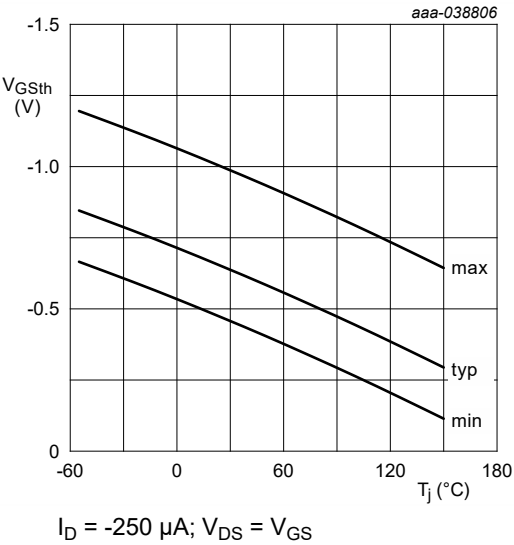


Fig. 24. TR2: Gate-source threshold voltage as a function of junction temperature

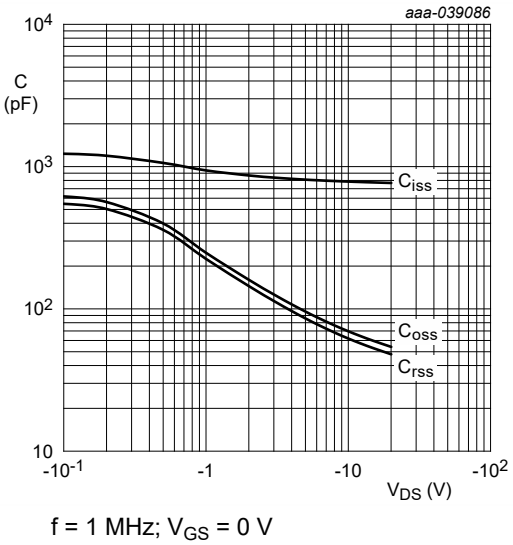


Fig. 25. TR2: Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

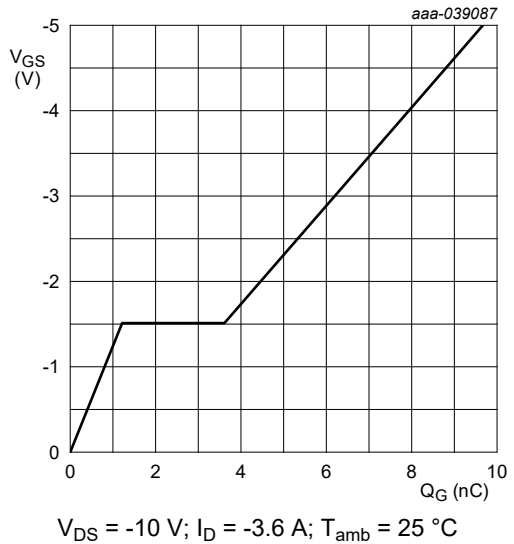


Fig. 26. TR2: Gate-source voltage as a function of gate charge; typical values

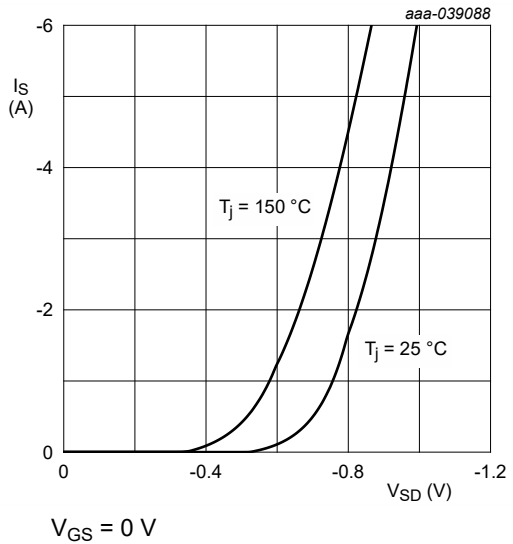
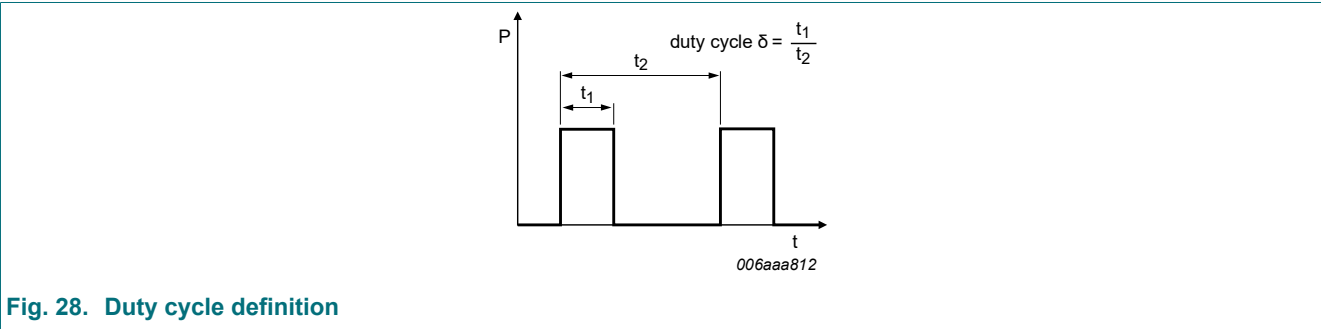


Fig. 27. TR2: Source current as a function of source-drain voltage; typical values

11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

HUSON6: plastic, thermal enhanced ultra thin small outline package; no leads;
6 terminals; body 2 x 2 x 0.65 mm

SOT1118

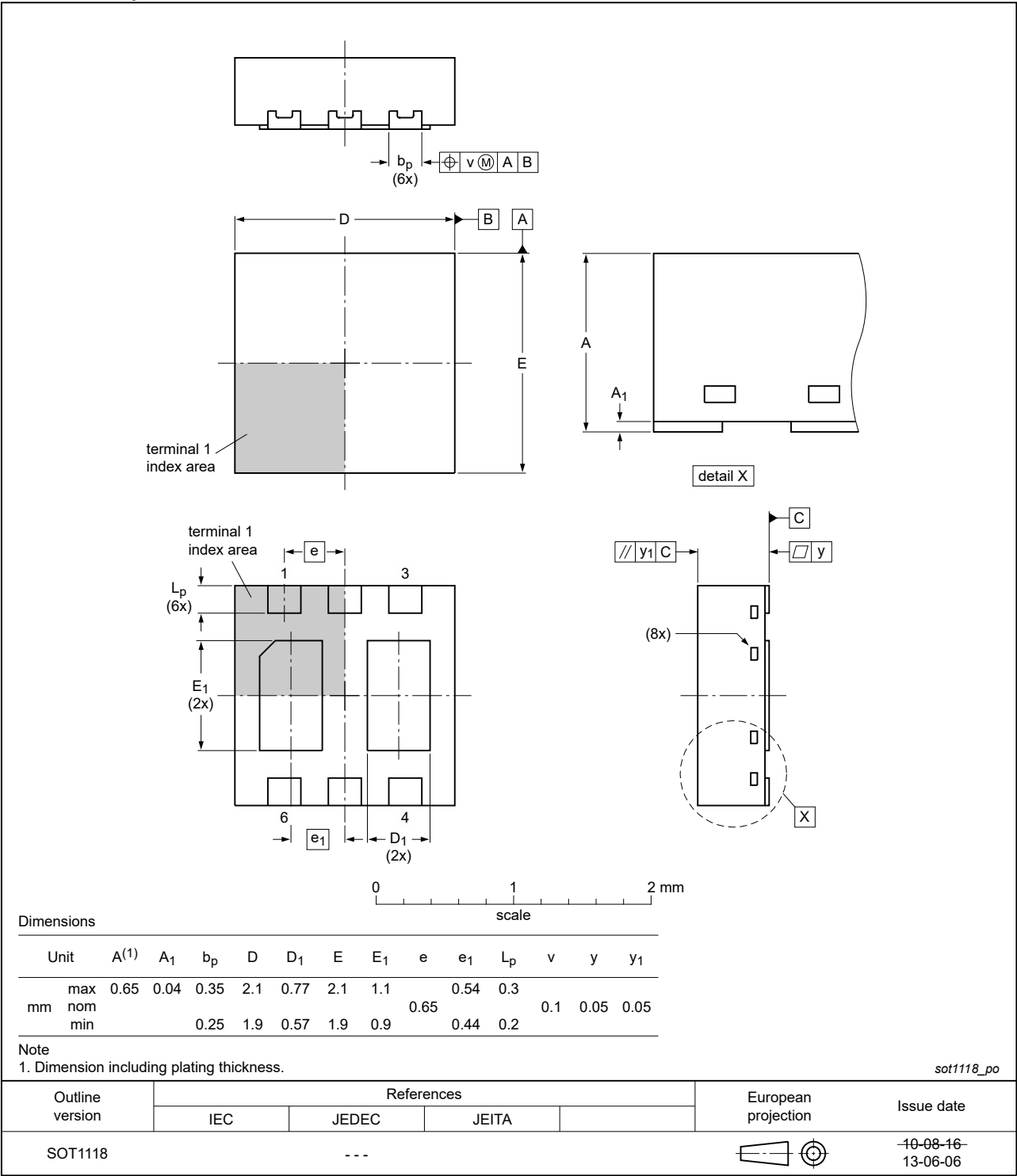


Fig. 29. Package outline DFN2020-6 (SOT1118)

13. Soldering

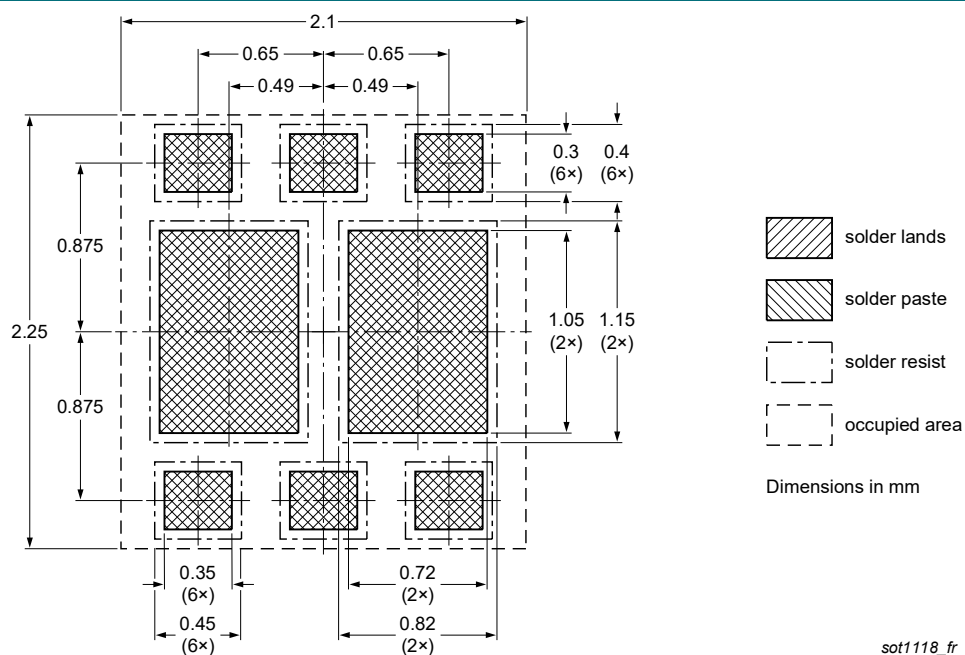


Fig. 30. Reflow soldering footprint for DFN2020-6 (SOT1118)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMCPB5530XA v.1	20240318	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This Nexperia product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or

equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 3

9. Thermal characteristics..... 6

10. Characteristics..... 8

11. Test information..... 15

12. Package outline..... 16

13. Soldering..... 17

14. Revision history..... 18

15. Legal information..... 19

© Nexperia B.V. 2024. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 18 March 2024