

MDT0430A0ISC-RGB	480 x 272	RGB Interface	TFT Module
Specification			
Version: 1		Date: 27/02/2022	
Revision			
1	25/02/2022	First issue	

Display Features			
Display Size	4.30"		
Resolution	480 x 272		
Orientation	Landscape		
Appearance	RGB		
Logic Voltage	3.3V		
Interface	RGB		
Brightness	400 cd/m ²		
Touchscreen	CTP		
Module Size	105.50 x 67.20 x 4.60 mm		
Operating Temperature	-30°C ~ +80°C		
Pinout	40 way FFC		
Pitch	0.50mm	Box Quantity	Weight / Display
		---	---

* - For full design functionality, please use this specification in conjunction with the SC7283 specification.(Provided Separately)

Display Accessories	
Part Number	Description
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.
MDIB-CC1	Interconnect board for standard pitch pinouts to fine pitch wires. Providing pinouts for 2.54 pinout. 1.27, 1, 0.845, 0.8, 0.7, 0.65, 0.62, 0.6, 0.5 & 0.3 pads.

Optional Variants	
Appearances	Voltage

Summary

TFT 4.3" is a IPS transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT_LCD module, It is usually designed for industrial application and this module follows RoHs,

General Specifications

- Size: 4.3 inch
- Dot Matrix: 480 x RGBx272(TFT) dots
- Module dimension: 105.5(W) x 67.2(H) x 4.6(D) mm
- Active area: 95.04 x 53.856 mm
- Pixel pitch: 0.198(H) x 0.198(V) mm
- LCD type: TFT, Normally Black, Transmissive
- Viewing Angle: 80/80/80/80
- Aspect Ratio: 16:9
- TFT Driver IC: SC7283 or equivalent
- TFT Interface: 24-bit RGB
- Backlight Type: LED, Normally White
- CTP FW Version: 0x07.0x00.0x00.0x00.0x01.0x0C.0x11.0x43
- CTP IC: ILI2130 or equivalent
- CTP Interface: I2C
- CTP Resolution: 16384*16384
- With /Without TP: With CTP
- Surface: Glare

*Color tone slight changed by temperature and driving voltage.



Interface

1. LCM PIN Definition

Pin	Symbol	Function	Remark
1	LED-	Power for LED backlight cathode	
2	LED+	Power for LED backlight anode	
3	GND	Power ground	
4	VCC	Power voltage	
5	R0	Red data	
6	R1	Red data	
7	R2	Red data	
8	R3	Red data	
9	R4	Red data	
10	R5	Red data	
11	R6	Red data	
12	R7	Red data	
13	G0	Green data	
14	G1	Green data	
15	G2	Green data	
16	G3	Green data	
17	G4	Green data	
18	G5	Green data	
19	G6	Green data	
20	G7	Green data	
21	B0	Blue data	
22	B1	Blue data	
23	B2	Blue data	
24	B3	Blue data	
25	B4	Blue data	
26	B5	Blue data	
27	B6	Blue data	
28	B7	Blue data	
29	GND	Power ground	
30	CLK	Pixel clock input pin (DCLK)	
31	DISP	DISP sets the display mode.	

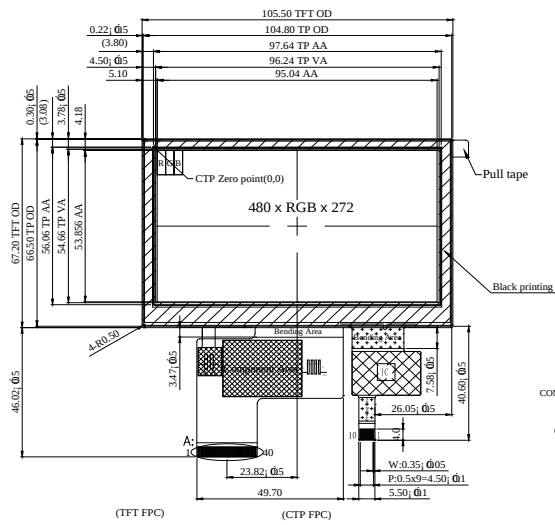


		<table><tr><th>DISP</th><th>Function Description</th></tr><tr><td>L</td><td>Standby mode</td></tr><tr><td>H</td><td>Normal display mode</td></tr></table>	DISP	Function Description	L	Standby mode	H	Normal display mode	
DISP	Function Description								
L	Standby mode								
H	Normal display mode								
32	HSYNC	Horizontal sync signal, default is negative polarity.							
33	VSYNC	Vertical sync signal, default is negative polarity.							
34	DE	Data input enable.							
35	NC	No connection							
36	GND	Power ground							
37	NC	No connection							
38	NC	No connection							
39	NC	No connection							
40	NC	No connection							

2. CTP PIN Definition

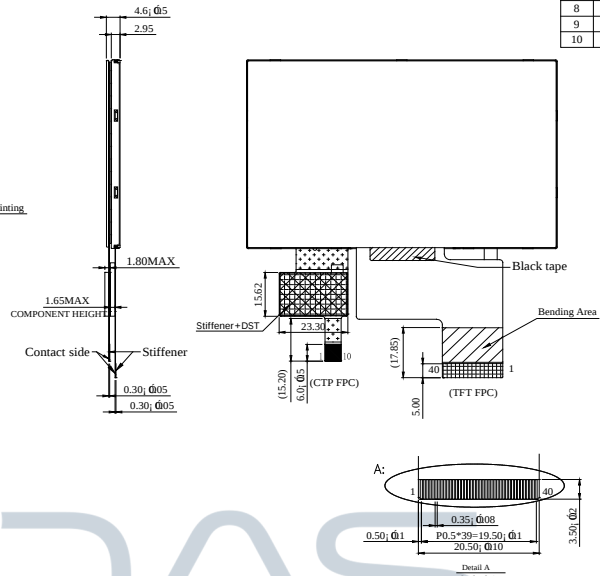
Pin	Symbol	Function	Remark
1	VSS	Ground for analog circuit	
2	VDDT	Power Supply : +3.3V	
3	SCL	I2C clock input	
4	NC	No connection	
5	SDA	I2C data input and output	
6	NC	No connection	
7	/RST	External Reset, Low is active	
8	NC	No connection	
9	/INT	External interrupt to the host	
10	VSS	Ground for analog circuit	

Contour Drawing



Note.

380nm transmittance(UV Cut at 380nm)<1.0%



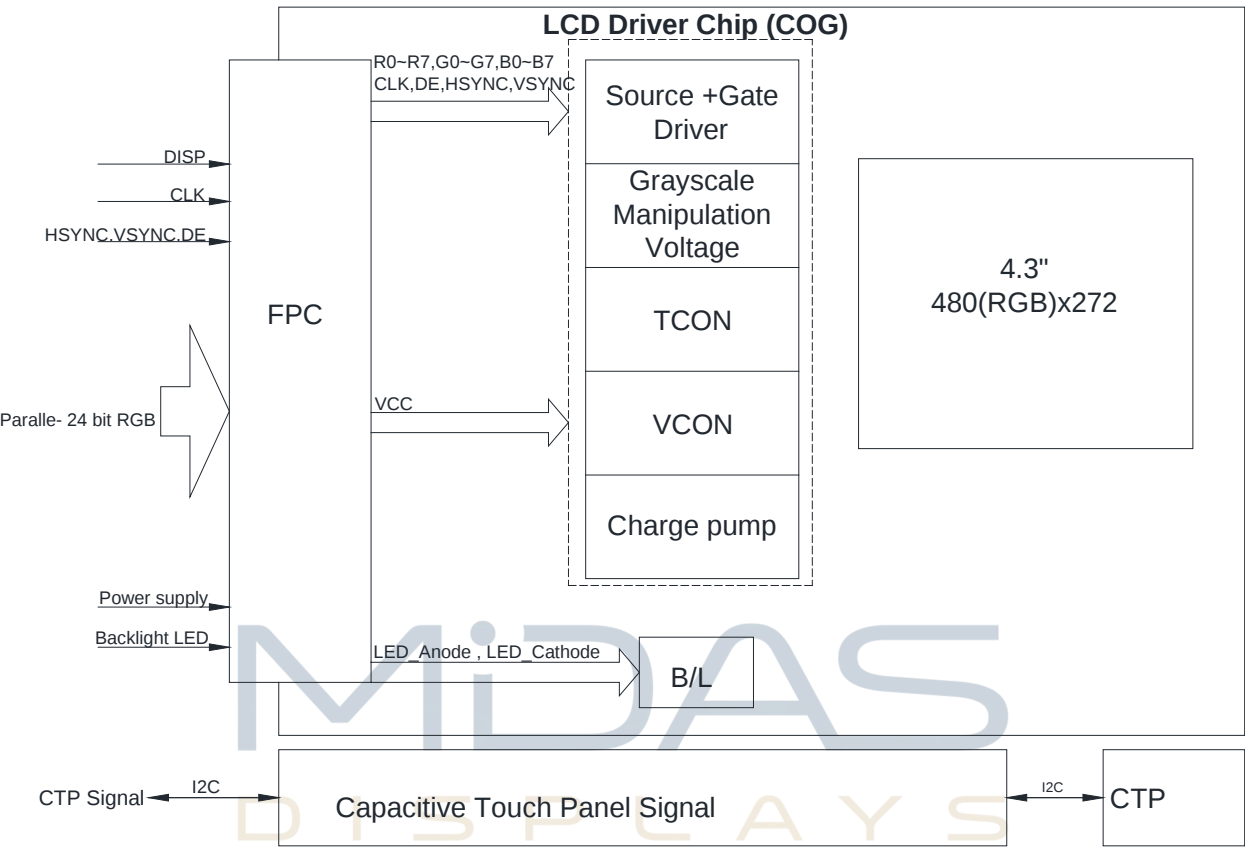
CTP		TFT	
PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	VSS	1	LED-
2	VDDT	2	LED+
3	SCL	3	GND
4	NC	4	VCC
5	SDA	5	R0
6	NC	6	R1
7	/RST	7	R2
8	NC	8	R3
9	/INT	9	R4
10	VSS	10	R5
		11	R6
		12	R7
		13	G0
		14	G1
		15	G2
		16	G3
		17	G4
		18	G5
		19	G6
		20	G7
		21	B0
		22	B1
		23	B2
		24	B3
		25	B4
		26	B5
		27	B6
		28	B7
		29	GND
		30	CLK
		31	DISP
		32	HSYNC
		33	VSYNC
		34	DE
		35	NC
		36	GND
		37	NC
		38	NC
		39	NC
		40	NC

The non-specified tolerance of dimension is ; ± 3 mm .

DESIGN • MANUFACTURE • SUPPLY



Block Diagram



DESIGN • MANUFACTURE • SUPPLY



Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

- Temp. $\leq 60^{\circ}\text{C}$, 90% RH MAX. Temp. $> 60^{\circ}\text{C}$, Absolute humidity shall be less than 90% RH at 60°C

Electrical Characteristics

1. Operating conditions:

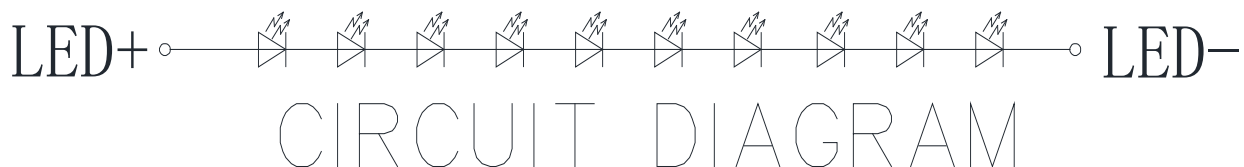
Item	Symbol	Min	Typ	Max	Unit	Remark
Supply Voltage For LCM	VCC	3.0	-	3.6	V	
Supply Current For LCM	ICC	-	25	37.5	mA	Note1
Supply CTP	VDDT	3.1	3.3	3.5	V	
	ICTP	—	52	78	mA	ICTP

Note 1 : This value is test for VCC=3.3V , Ta=25 °C only

2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	-	-	20	-	mA	-
Power Consumption	-	540	640	680	mW	-
LED voltage	VLED+	27	32	34	V	Note 1
LED Life Time	-	-	50,000	-	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



Note 2 : Ta = 25 °C

Note 3 : Brightness to be decreased to 50% of the initial value

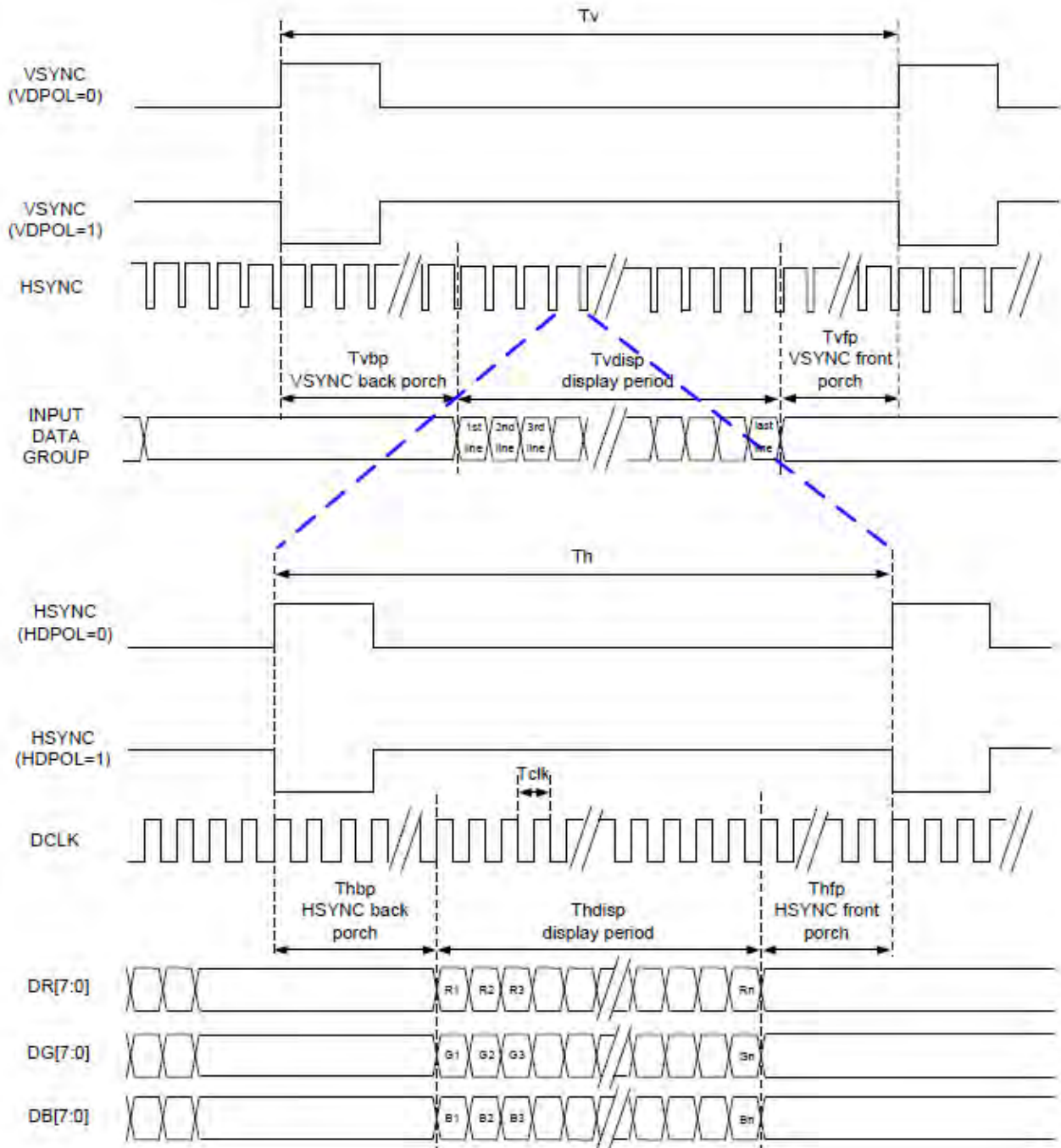
Note 4 : The single LED lamp case

AC Characteristics

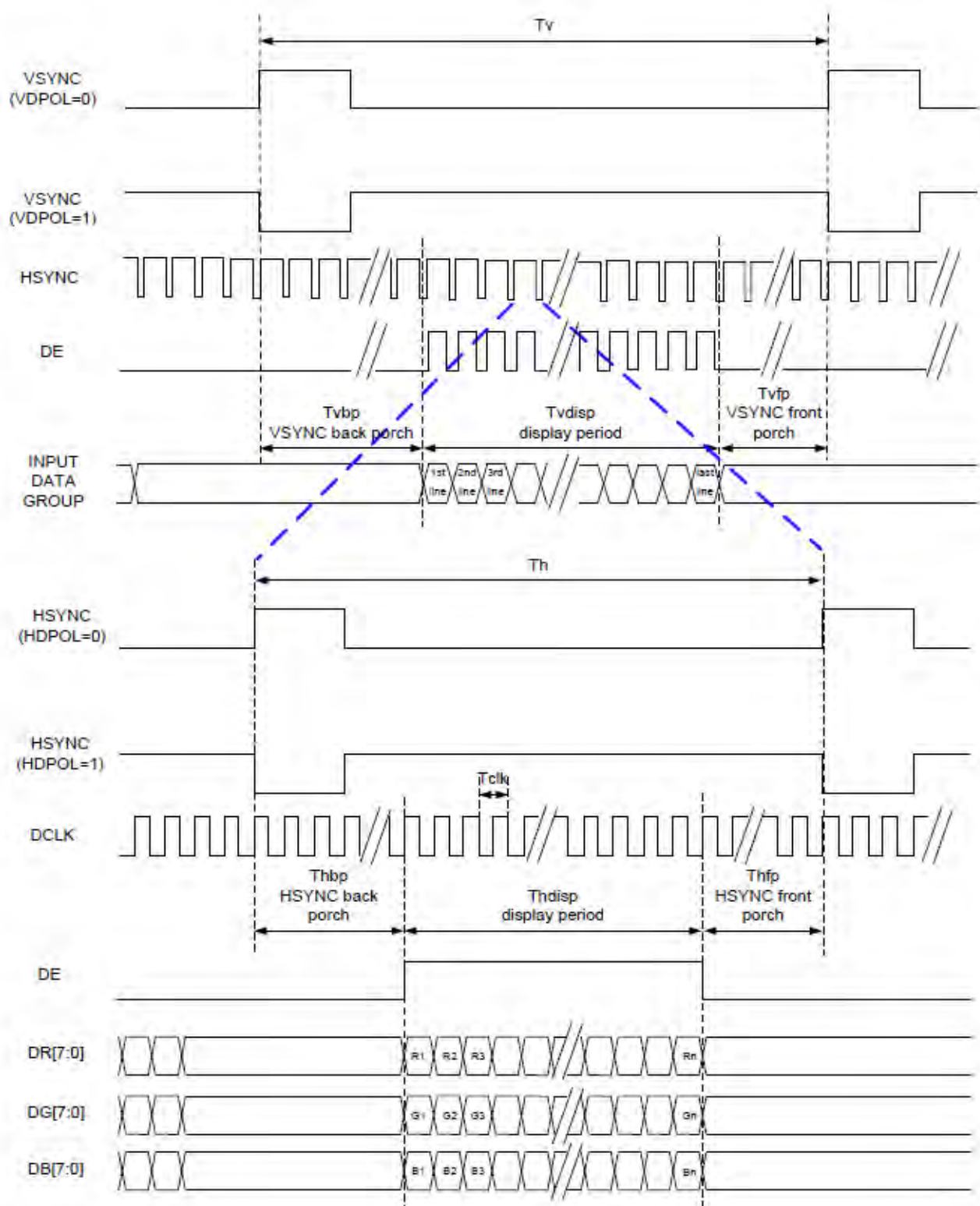
AC Electrical Characteristics (VCC= 3.3V, GND= 0V, TA=25°C)

1. RGB Interface

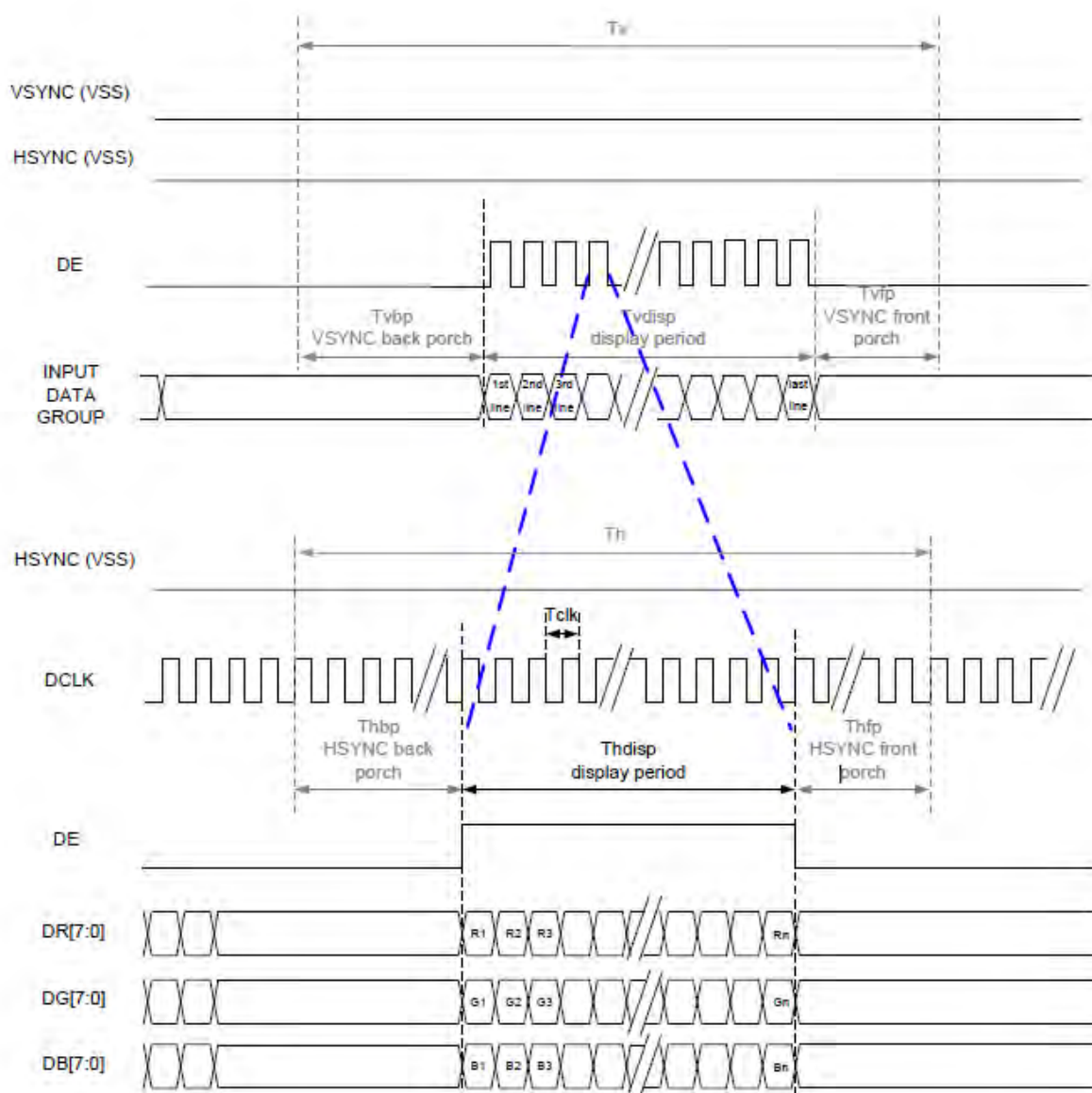
1. SYNC Mode



2. SYNC-DE Mode



3. DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

2. Parallel 24 bit RGB Input Timing Table

(Parallel 24-bit RGB Input Timing (VCC= 3.3V, GND= 0V, TA=25°C))

480RGB X 272 Resolution Timing Table							
Item		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency		Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	Thbp of SYNC mode set by H_BLANKING[7:0]
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp		272		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	Tvbp of SYNC mode set by V_BLANKING[7:0]
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

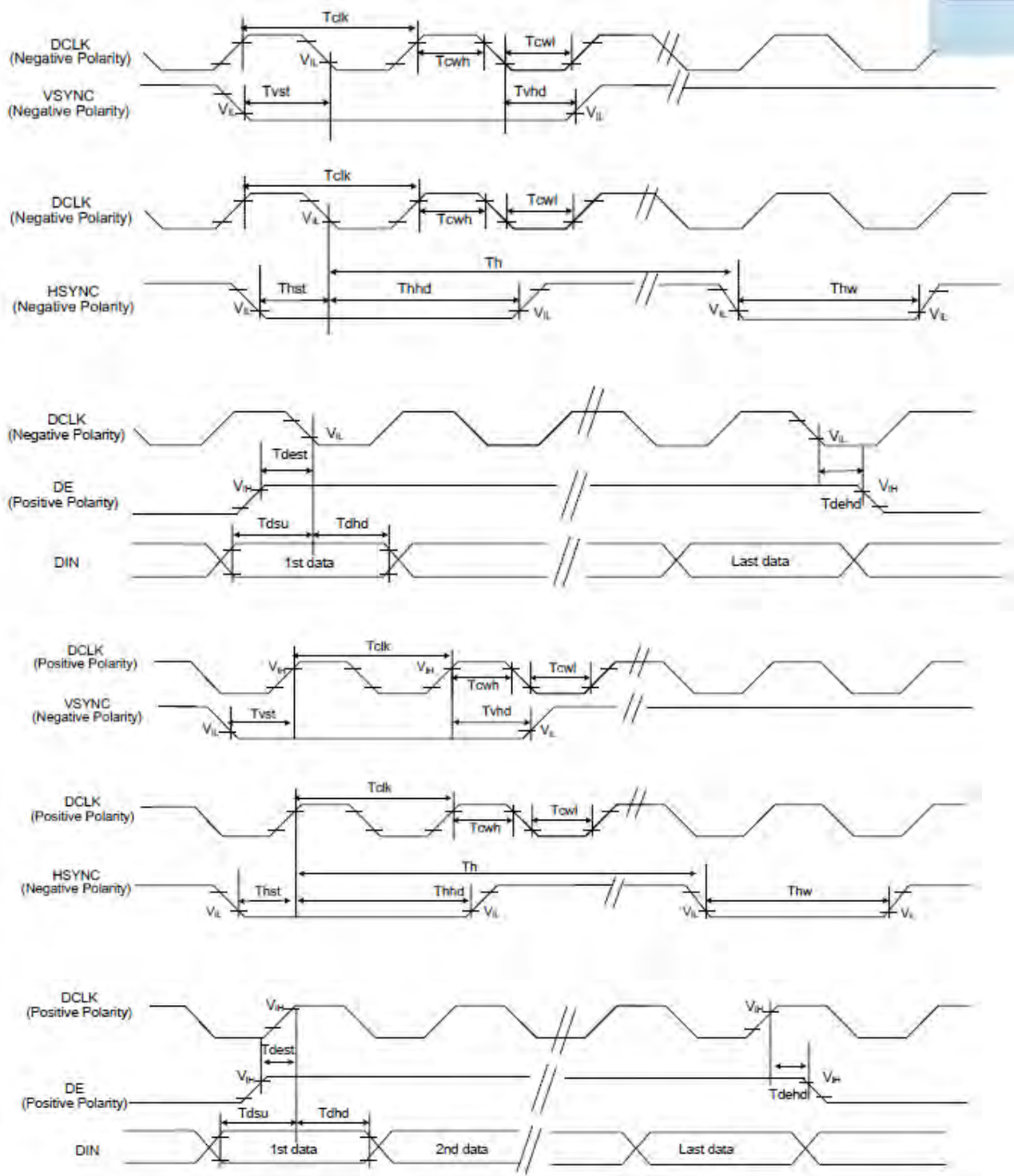
Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

DESIGN • MANUFACTURE • SUPPLY



3. System Bus Timing for RGB Interface

(VCC= 3.3V, GND= 0V, TA=25°C)



Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	Tcw	40	50	60	%	
HSYNC Width	Thw	2	-	-	DCLK	
VSYNC Setup Time	Tvst	12	-	-	ns	
VSYNC Hold Time	Tvhd	12	-	-	ns	
HSYNC Setup Time	Thst	12	-	-	ns	
HSYNC Hold Time	Thhd	12	-	-	ns	
Data Setup Time	Tdsu	12	-	-	ns	
Data Hold Time	Tdhd	12	-	-	ns	
DE Setup Time	Tdest	12	-	-	ns	
DE Hold Time	Tdehd	12	-	-	ns	

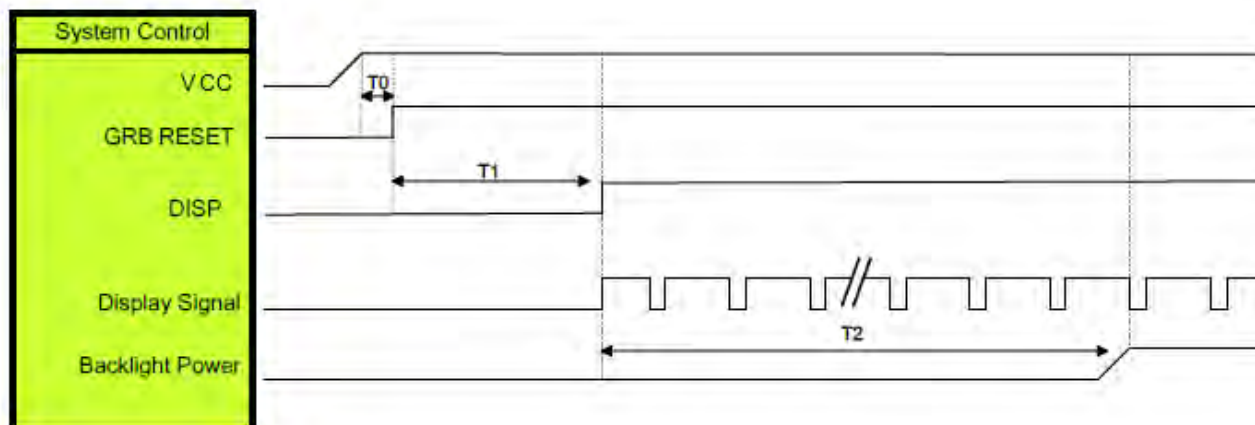
MIDAS
DISPLAYS

DESIGN • MANUFACTURE • SUPPLY



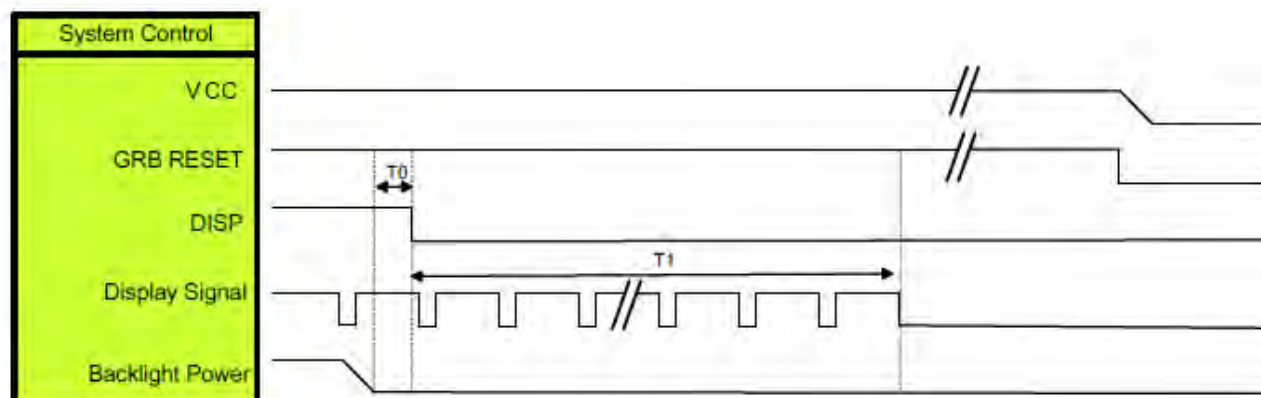
Power sequence

1. Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

2. Power Off Sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time	Tr+ Tf	$\theta=0^\circ$ 、 $\Phi=0^\circ$	-	30	40	ms	Note 3
Contrast ratio	CR	At optimized viewing angle	640	800	-	-	Note 4
Color Chromaticity	White	Wx	0.27	0.32	0.37	-	Note 2,6,7
		Wy	0.295	0.345	0.395	-	
Viewing angle	Hor.	Θ_R	70	80	-	Deg.	Note 1
		Θ_L	70	80	-		
	Ver.	Φ_T	70	80	-		
		Φ_B	70	80	-		
Brightness	-	-	300	400	-	cd/m ²	Center of display
Uniformity	(U)	-	75	-	-	%	Note 5

Ta=25±2℃, IL=40mA

Note 1: Definition of viewing angle range

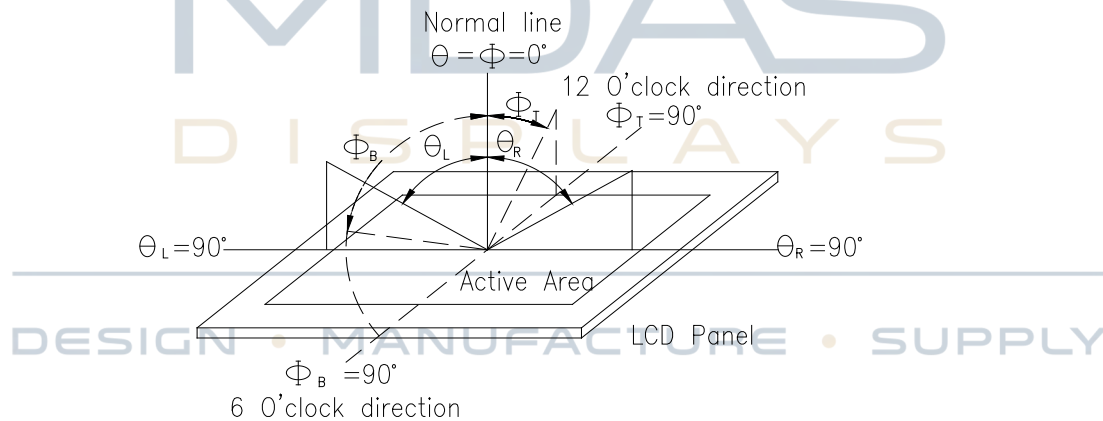


Fig. 11.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



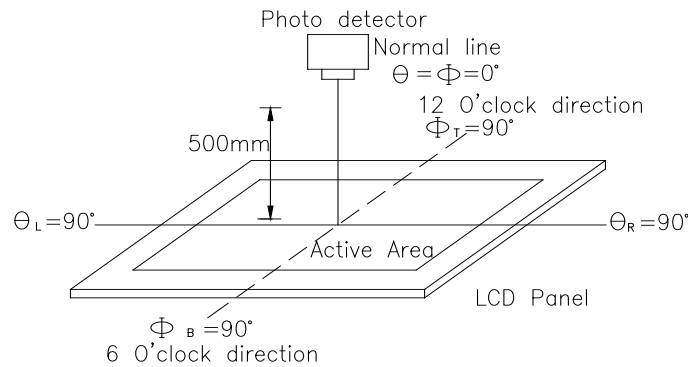
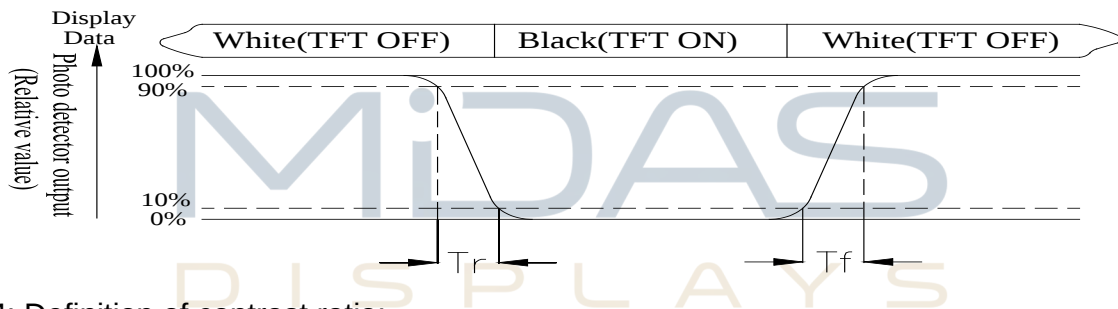


Fig. 11.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

DESIGN • MANUFACTURE • SUPPLY

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = $L_{min}/L_{max} \times 100\%$

L = Active area length

W = Active area width

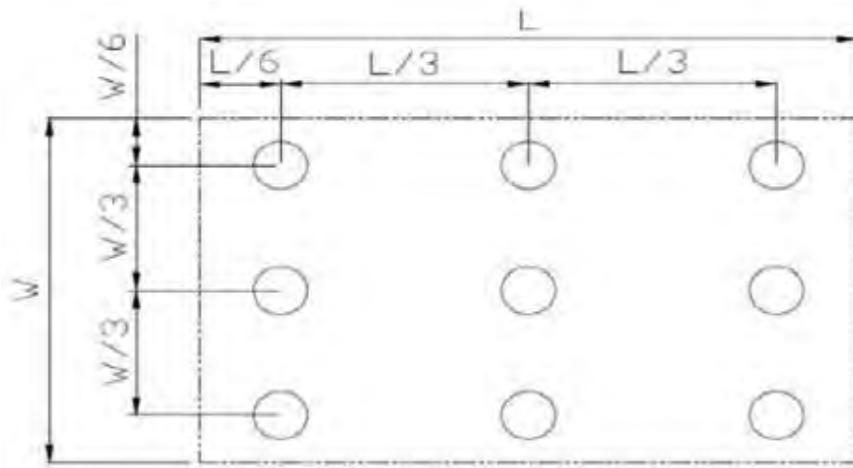


Fig 11.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

DESIGN • MANUFACTURE • SUPPLY



Reliability

Content of Reliability Test (Super Wide temperature, -30°C~80°C)

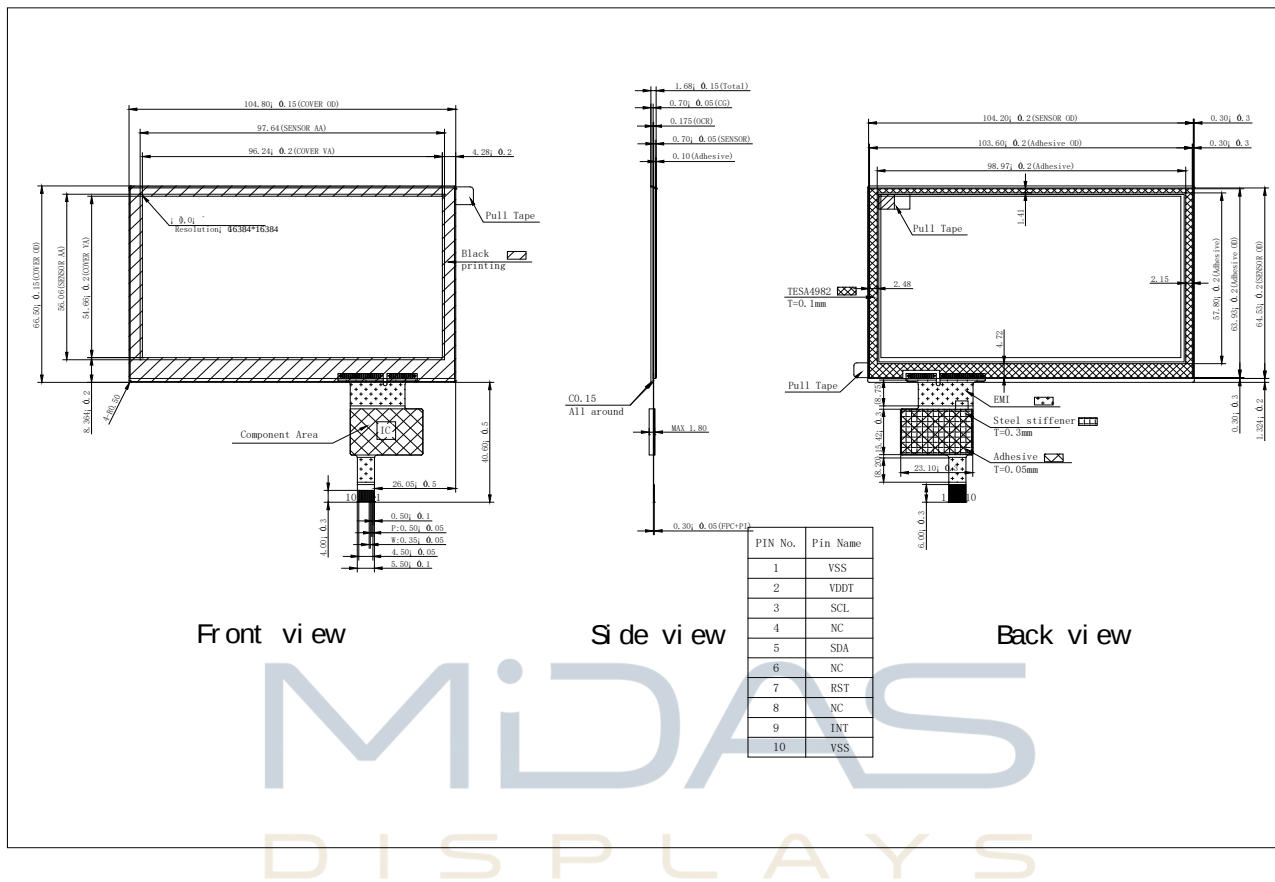
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-30°C 200hrs	1
High Temperature/Humidity storage	The module should be allowed to stand at 60°C,90%RH max	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -30°C 25°C 80°C 30min 5min 30min 1 cycle	-30°C/80°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

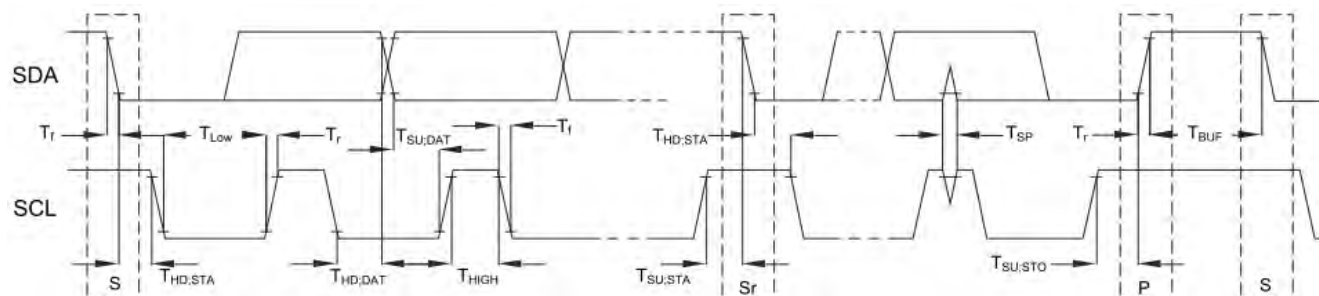
Note3: The packing have to including into the vibration testing.

Touch Panel Information



DESIGN • MANUFACTURE • SUPPLY

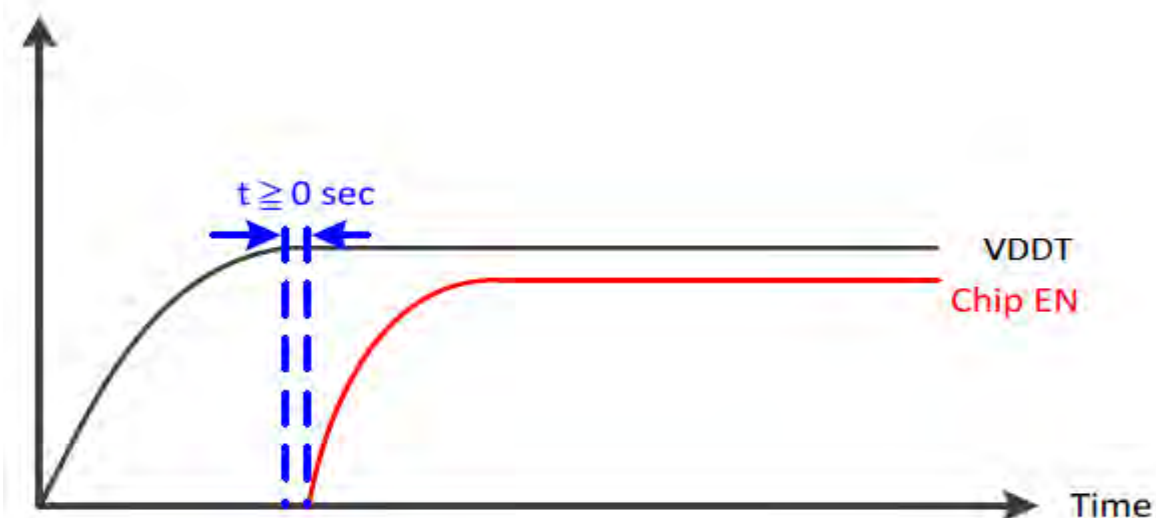
1. I2C AC Characteristics



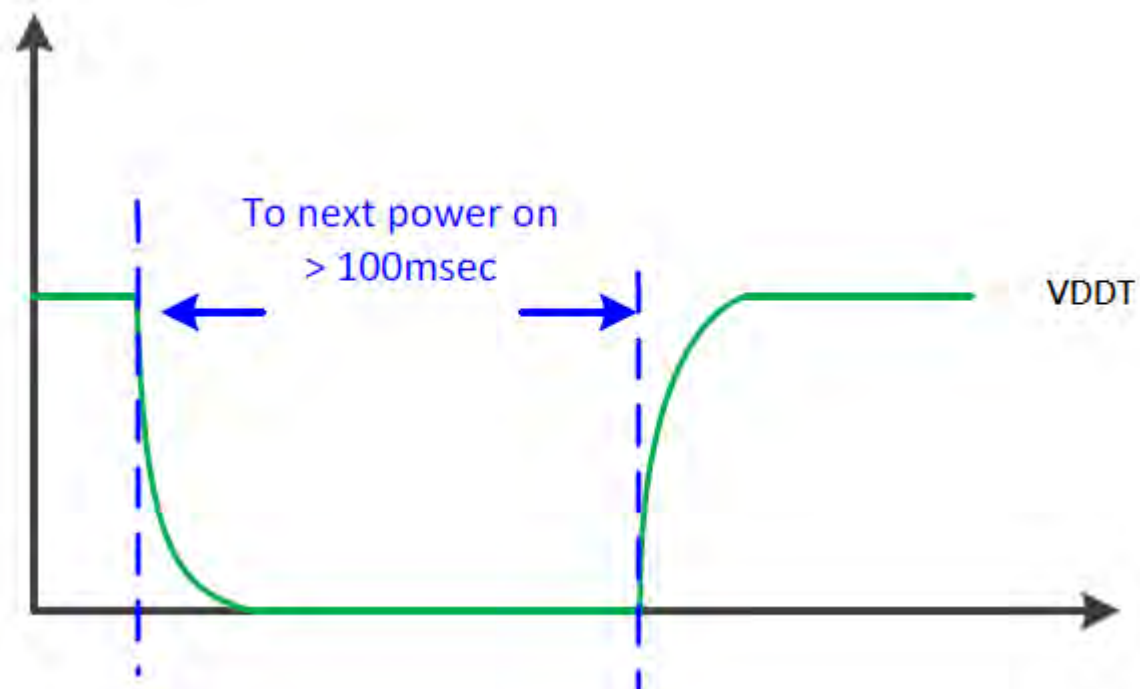
Item	Symbol	100kHz		400kHz		Unit
		Min.	Max.	Min.	Max.	
SCL standard mode clock frequency	F _{SCL}	0	100	0	400	kHz
Hold time (repeated) START condition. After this period, the first clock is generated.	T _{HD;STA}	4	—	0.6	—	us
LOW period of the SCL clock	T _{LOW}	4.7	—	1.3	—	us
HIGH period of the SCL clock	T _{HIGH}	4	—	0.6	—	us
Setup time for a repeat START condition.	T _{SU;STA}	4.7	—	0.6	—	us
Data hold time	T _{HD;DAT}	0	3.45	0	0.9	us
Data setup time	T _{SU;DAT}	250	—	100	—	ns
Rising time of both SDA and SCL signals	T _r	—	1000	—	300	ns
Falling time of both SDA and SCL signals	T _f	—	300	—	300	ns
Setup time for STOP condition.	T _{SU;STO}	4	—	0.6	—	us
Free time between STOP and START condition	T _{BUF}	4.7	—	1.3	—	us
Pulse width of spikes which must be suppressed by input filter	T _{SP}	—	—	0	50	ns

2. Power On Sequence

DESIGN • MANUFACTURE • SUPPLY



3. Power Off to Power On Sequence



MIDAS
DISPLAYS

DESIGN • MANUFACTURE • SUPPLY