

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}$	I_D $T_A = 25^\circ C$
100V	250m Ω @ $V_{GS} = 10V$	2.9A
	300m Ω @ $V_{GS} = 6V$	2.6A

Description and Applications

This new generation trench MOSFET features a unique structure that combines the benefits of low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

- DC-DC converters
- Power management functions
- Disconnect switches
- Motor control

Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

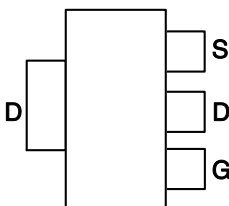
Mechanical Data

- Package: SOT223 (Type DN)
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.112 grams (Approximate)

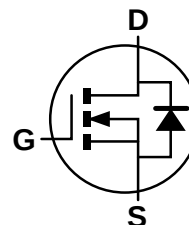
SOT223 (Type DN)



Top View



Pin Out - Top View



Equivalent Circuit

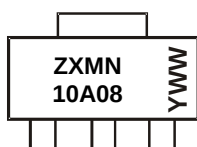
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
ZXMN10A08GTA	SOT223 (Type DN)	1,000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>

Marking Information

SOT223



ZXMN10A08 = Product Type Marking Code
YWW = Date Code Marking
Y or $\bar{Y}$ = Last Digit of Year (ex: 2 = 2022)
WW or $\bar{WW}$ = Week Code (01-53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	100	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($V_{GS}=10\text{V}$; $T_A = +25^\circ\text{C}$) (Note 6)	I_D	2.9	A
($V_{GS}=10\text{V}$; $T_A = +70^\circ\text{C}$) (Note 6)		2.3	
($V_{GS}=10\text{V}$; $T_A = +25^\circ\text{C}$) (Note 5)		2.0	
Pulsed Drain Current (Note 7)	I_{DM}	11	A
Continuous Source Current (Body Diode) (Note 6)	I_S	2.9	A
Pulsed Source Current (Body Diode) (Note 7)	I_{SM}	11	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

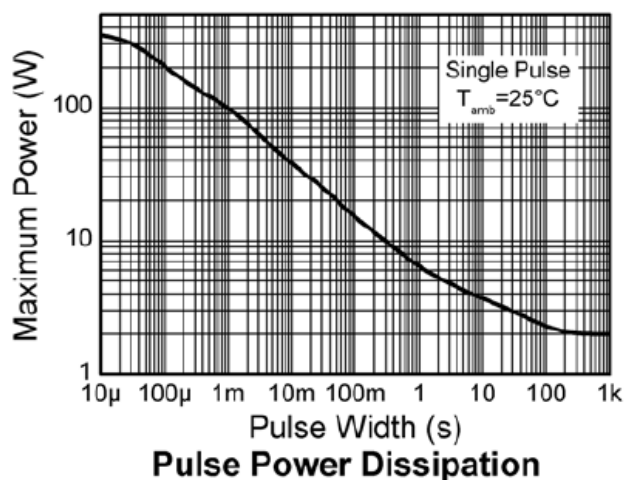
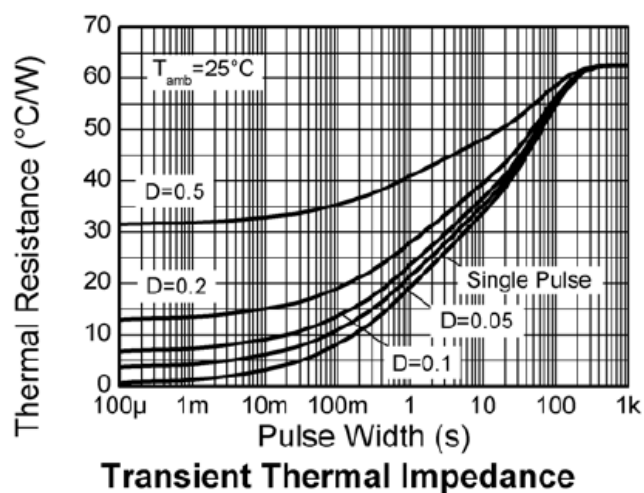
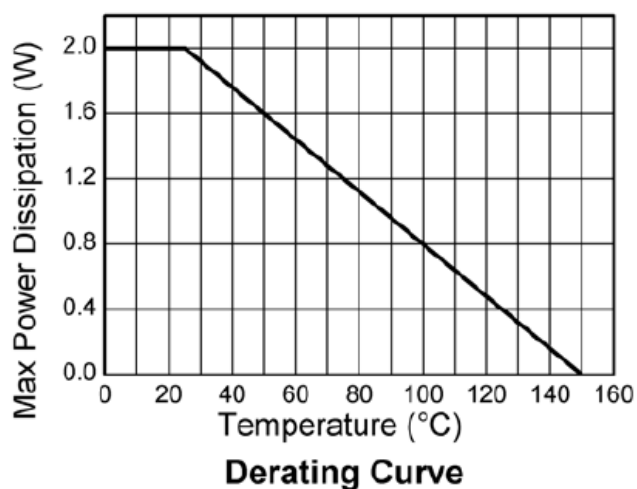
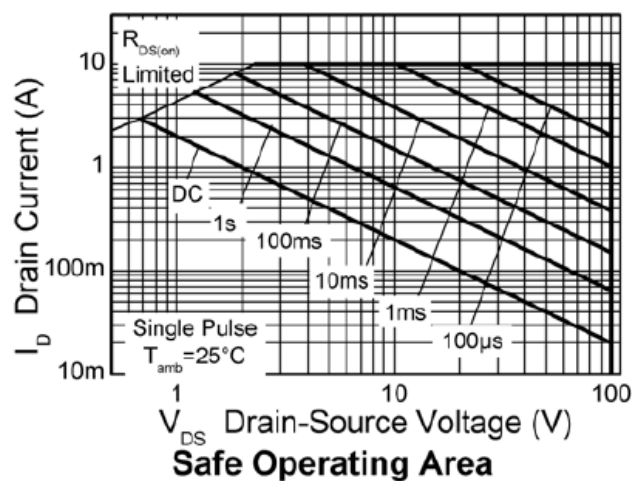
Characteristic	Symbol	Value	Unit
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 5)	P_D	2.0	W
Linear Derating Factor		16	mW/ $^\circ\text{C}$
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 6)	P_D	3.9	W
Linear Derating Factor		31	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	32	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

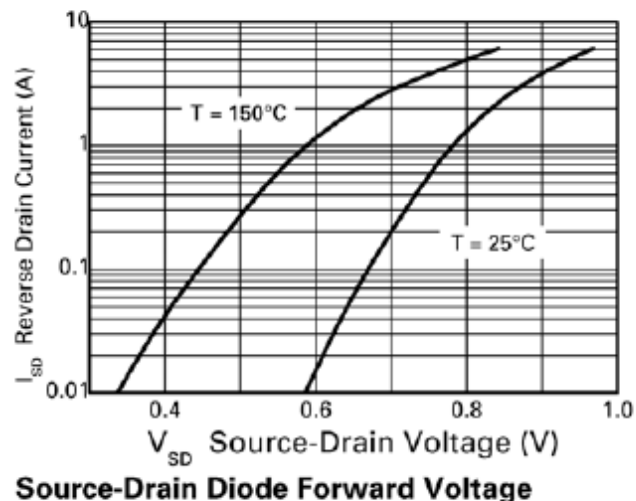
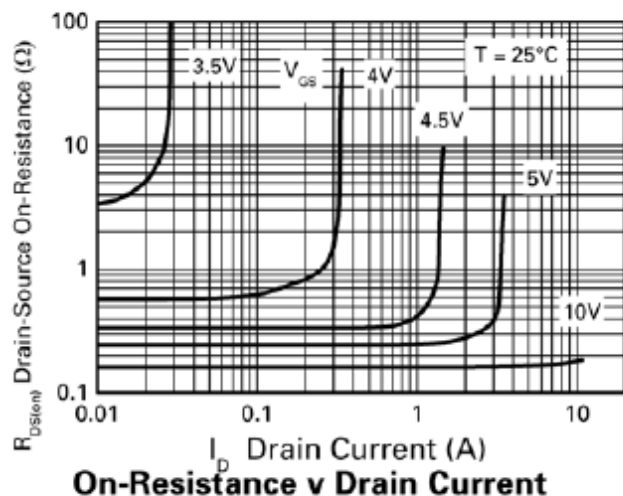
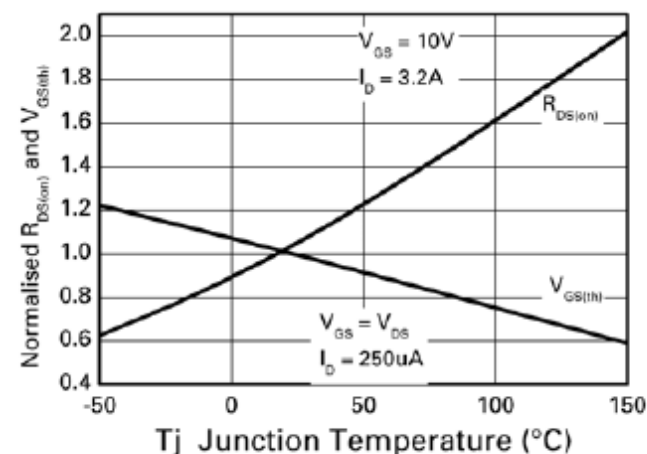
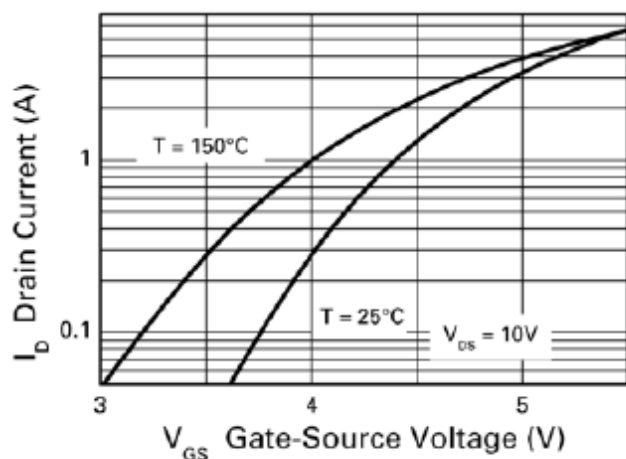
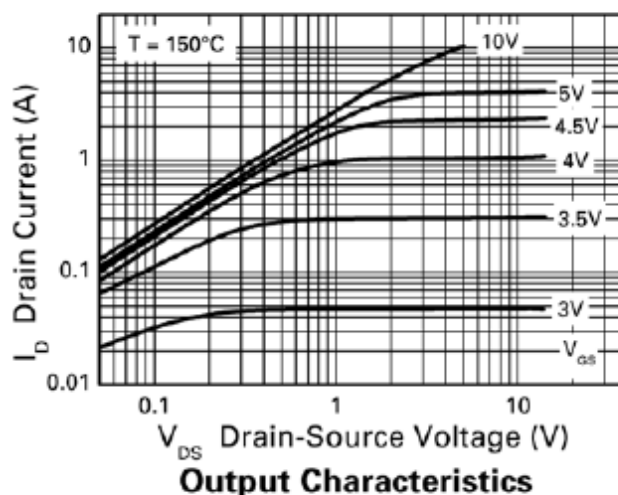
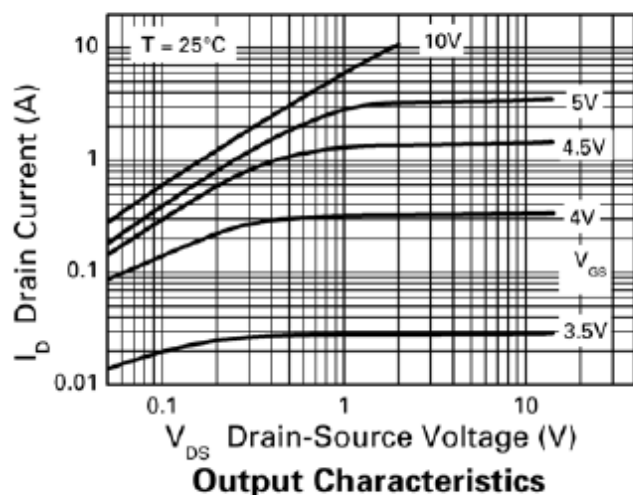
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	100	-	-	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	0.5	μA	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	-	-	100	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(th)}$	2	-	-	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance (Note 8)	$R_{DS(on)}$	-	-	0.25 0.30	Ω	$V_{GS} = 10\text{V}, I_D = 3.2\text{A}$ $V_{GS} = 6\text{V}, I_D = 2.6\text{A}$
Forward Transconductance (Notes 8, 10)	g_{fs}	-	5	-	S	$V_{DS} = 15\text{V}, I_D = 3.2\text{A}$
Diode Forward Voltage (Note 8)	V_{SD}	-	0.87	0.95	V	$T_J = 25^\circ\text{C}, I_S = 3.2\text{A}, V_{GS} = 0\text{V}$
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C_{iss}	-	405	-	pF	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	-	28.2	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	14.2	-	pF	
Turn-On Delay Time (Note 9)	$t_{D(on)}$	-	3.4	-	ns	$V_{DD} = 30\text{V}, I_D = 1.2\text{A}, V_{GS} = 10\text{V}, R_G = 6\Omega$
Turn-On Rise Time (Note 9)	t_R	-	2.2	-	ns	
Turn-Off Delay Time (Note 9)	$t_{D(off)}$	-	8	-	ns	
Turn-Off Fall Time (Note 9)	t_F	-	3.2	-	ns	
Gate Charge (Note 9)	Q_g	-	4.2	-	nC	$V_{DS} = 50\text{V}, V_{GS} = 5\text{V}, I_D = 1.2\text{A}$
Total Gate Charge (Note 9)	Q_g	-	7.7	-	nC	
Gate-Source Charge (Note 9)	Q_{gs}	-	1.8	-	nC	
Gate-Drain Charge (Note 9)	Q_{gd}	-	2.1	-	nC	
Reverse Recovery Time	t_{rr}	-	27	-	ns	$T_J = 25^\circ\text{C}, I_S = 1.2\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	Q_{rr}	-	32	-	nC	

- Notes:
5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 6. For a device surface mounted on FR4 PCB measured at $t \leq 10$ secs.
 7. Repetitive rating - 25mm x 25mm FR4 PCB, $D=0.02$, pulse width 300 μs - pulse width limited by maximum junction temperature.
 8. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.
 9. Switching characteristics are independent of operating junction temperature.
 10. For design aid only, not subject to production testing.

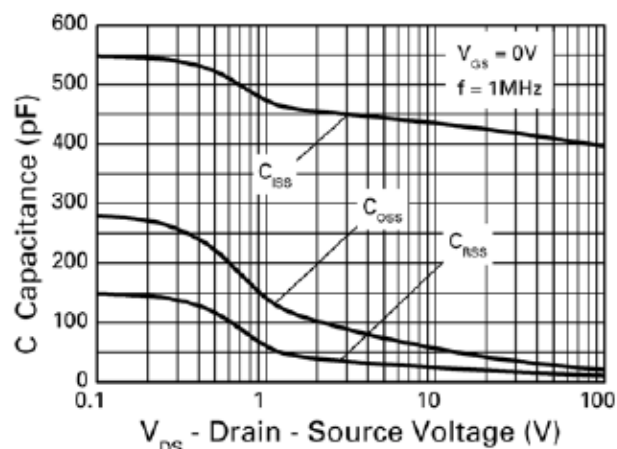
Thermal Characteristics



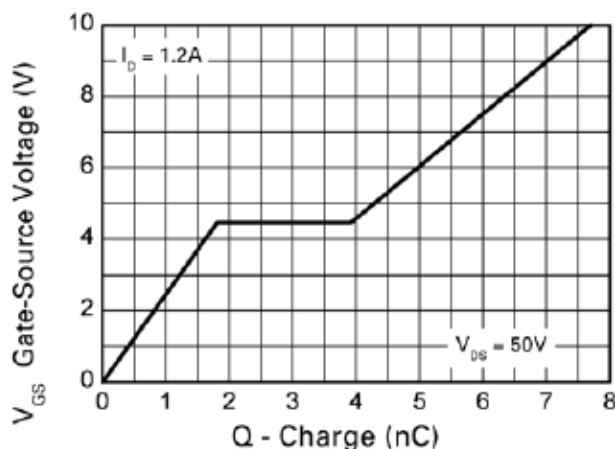
Typical Characteristics



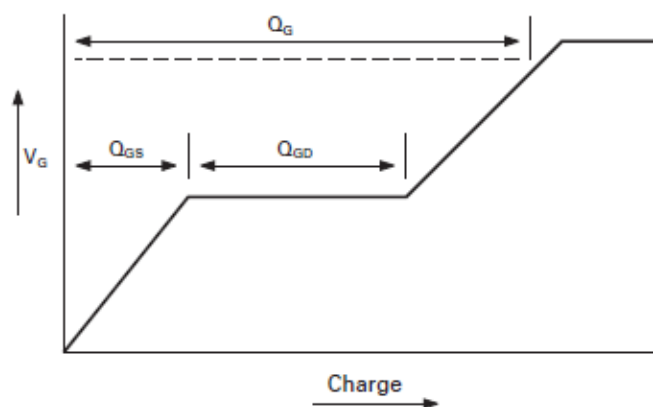
Typical Characteristics (continued)



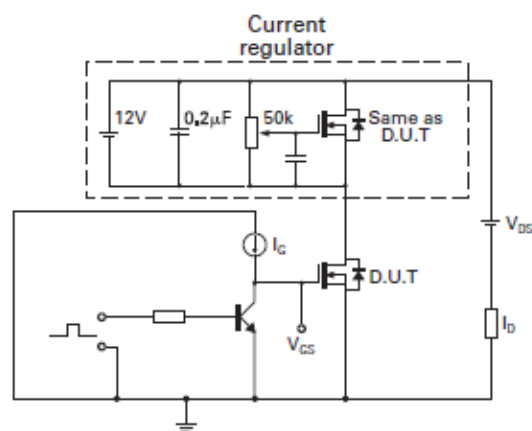
Capacitance v Drain-Source Voltage



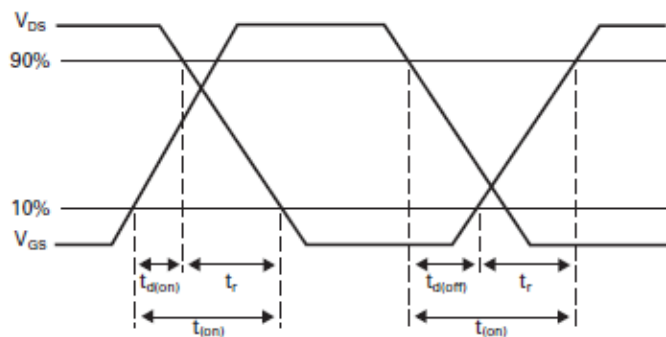
Gate-Source Voltage v Gate Charge



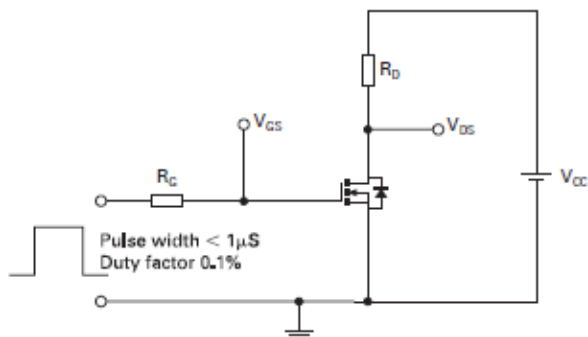
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

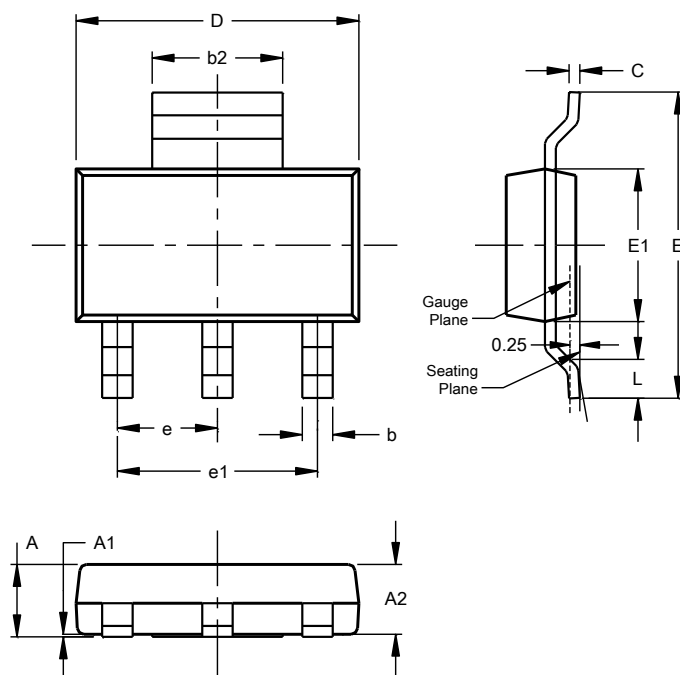


Switching time test circuit

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)

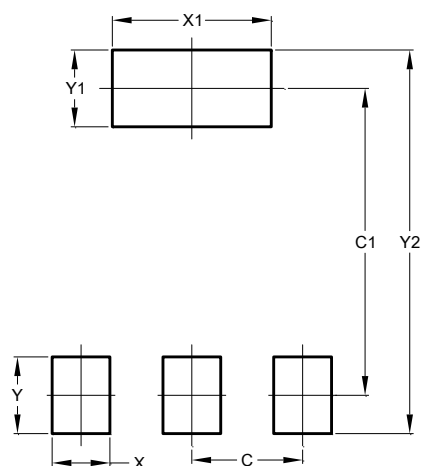


SOT223 (Type DN)			
Dim	Min	Max	Typ
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (<https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/>) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2022 Diodes Incorporated

www.diodes.com