

Product Summary

BV_{DSS}	$R_{DS(ON)}$ max	$I_{D \max}$ $T_A = +25^\circ\text{C}$
100V	220m Ω @ $V_{GS} = 10\text{V}$	1.6A
	250m Ω @ $V_{GS} = 4.5\text{V}$	1.3A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Load Switch

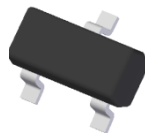
Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**
- PPAP Capable (Note 4)**

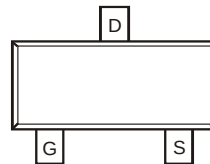
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.009 grams (Approximate)

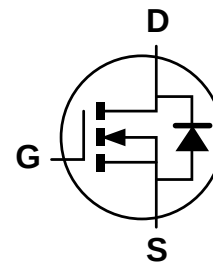
SOT23



Top View



Pin Configuration



Equivalent Circuit

Ordering Information (Note 5)

Part Number	Case	Packaging
DMN10H220LQ-7	SOT23	3,000/Tape & Reel
DMN10H220LQ-13	SOT23	10,000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
 - For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



N1H = Marking Code
 YM = Date Code Marking
 Y or $\bar{Y}$ = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022
Code	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	100	V
Gate-Source Voltage			V _{GSS}	±16	V
Continuous Drain Current (Note 6) V _{GS} = 10V	(Note 7)	T _A = +25°C	I _D	1.6	A
		T _A = +70°C		1.3	
	(Note 6)	T _A = +25°C	I _D	1.4	A
		T _A = +70°C		1.1	
Maximum Continuous Body Diode Forward Current (Note 7)			I _S	0.6	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	8	A

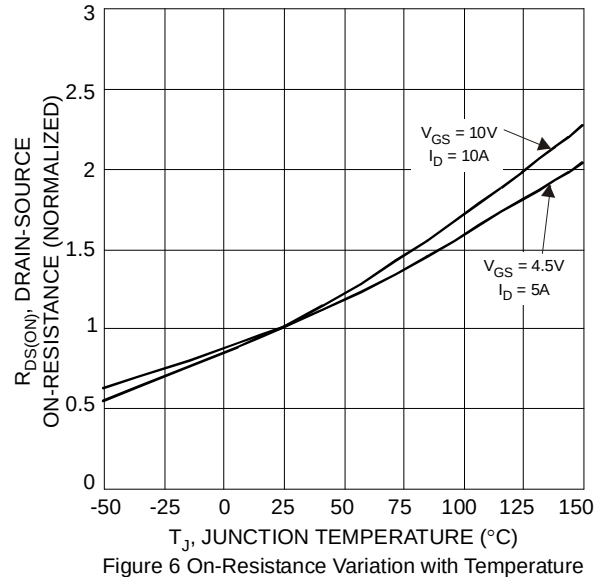
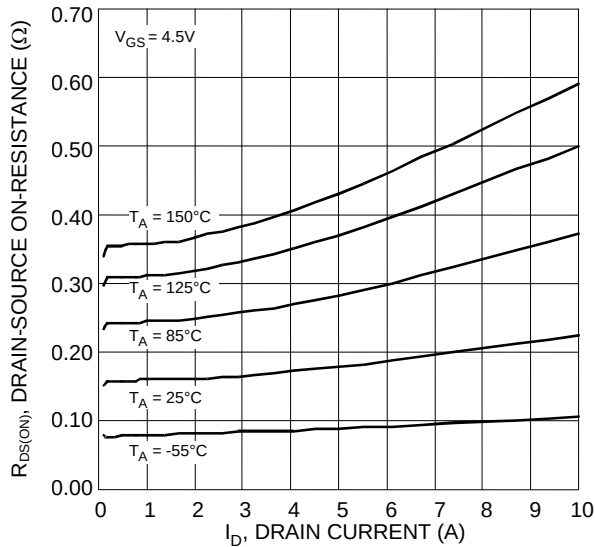
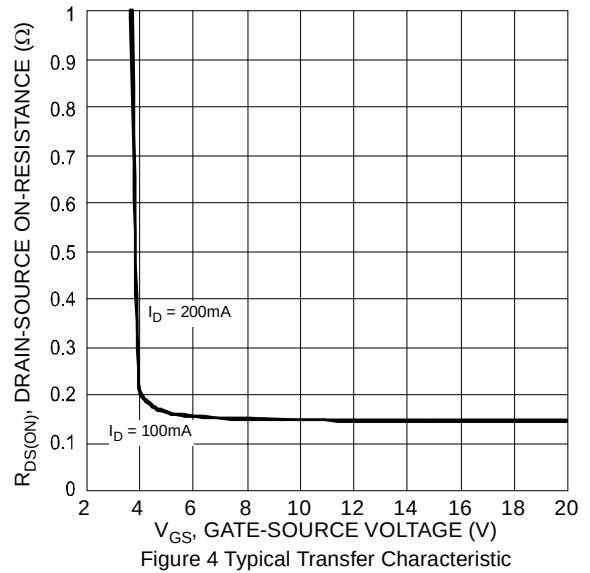
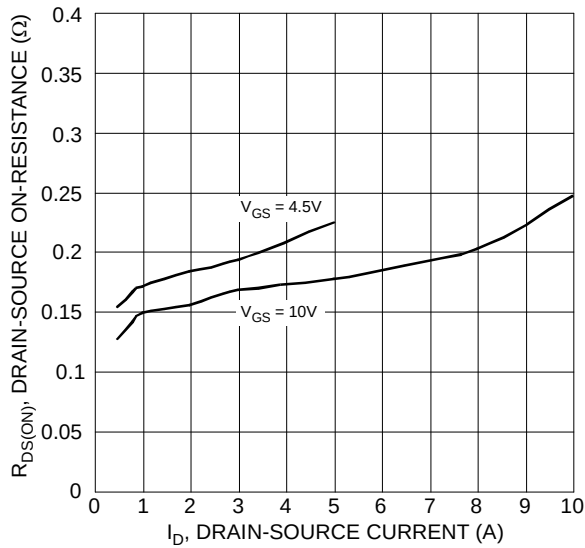
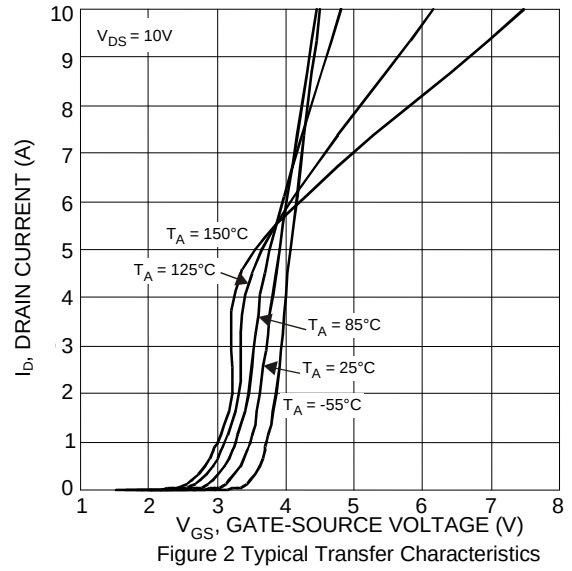
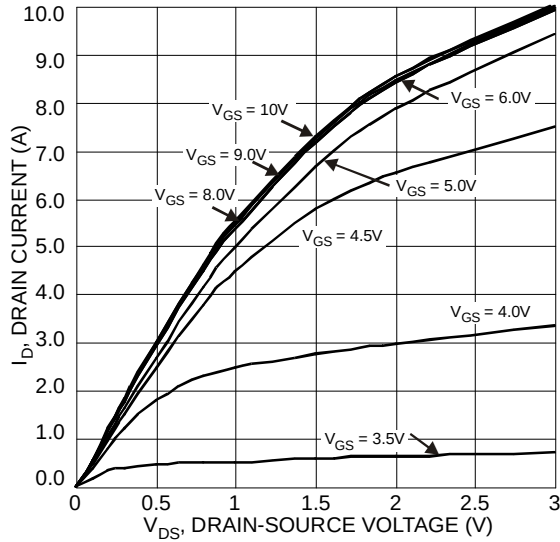
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

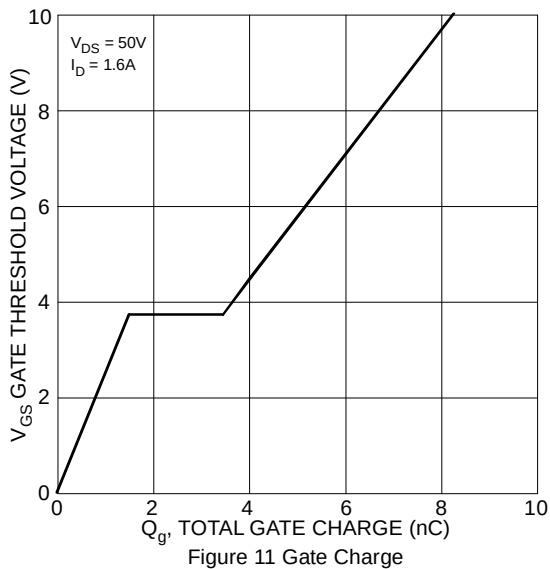
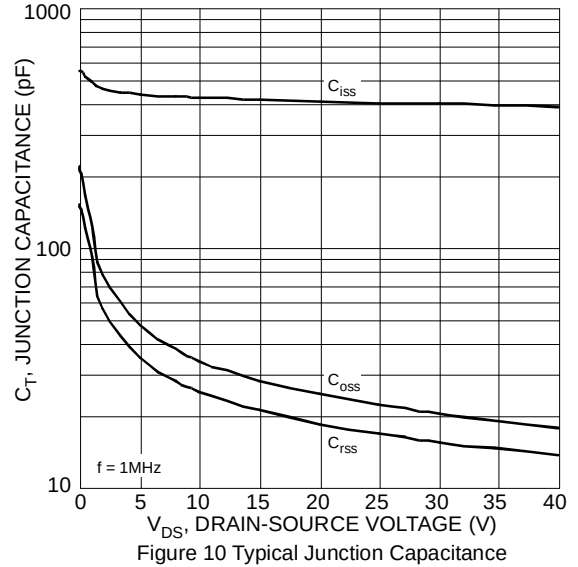
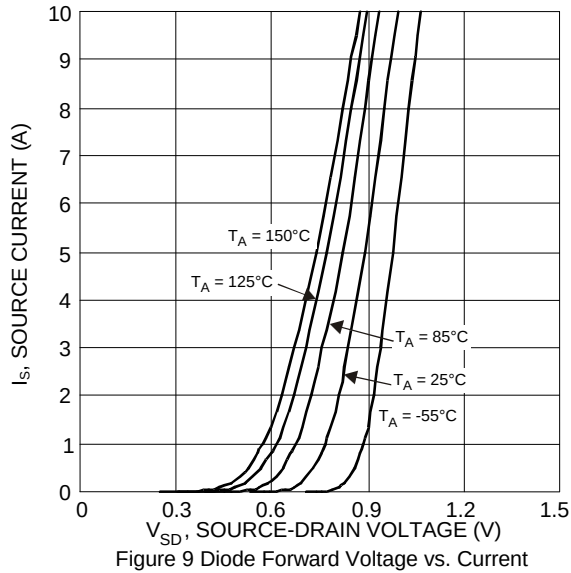
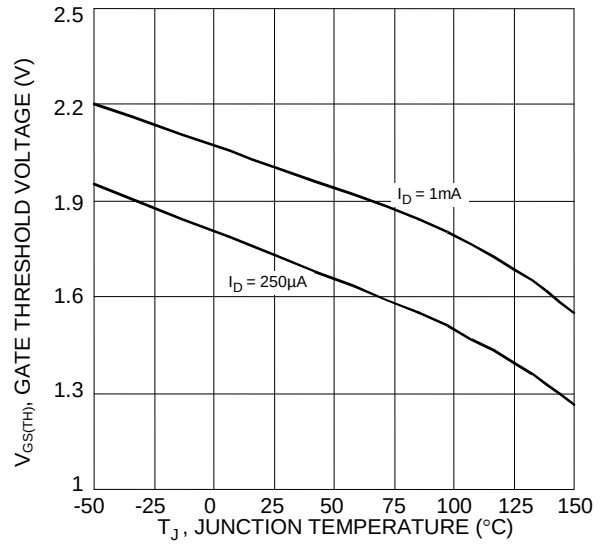
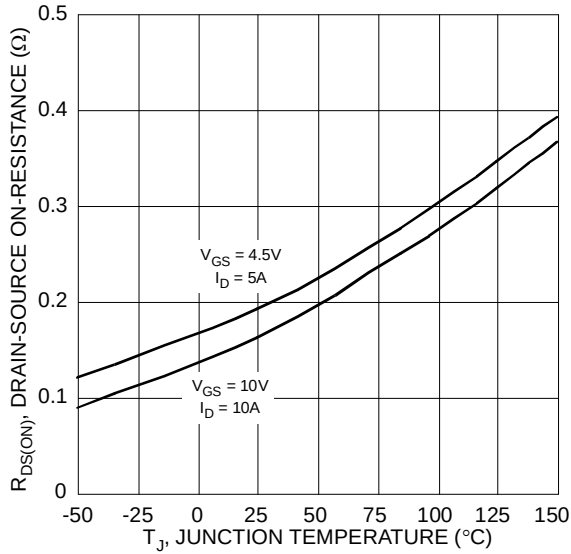
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 7)	T _A = +25°C	P _D	1.3	W
	T _A = +70°C		0.8	
Thermal Resistance, Junction to Ambient	(Note 6)	R _{θJA}	94	°C/W
	(Note 7)		177	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

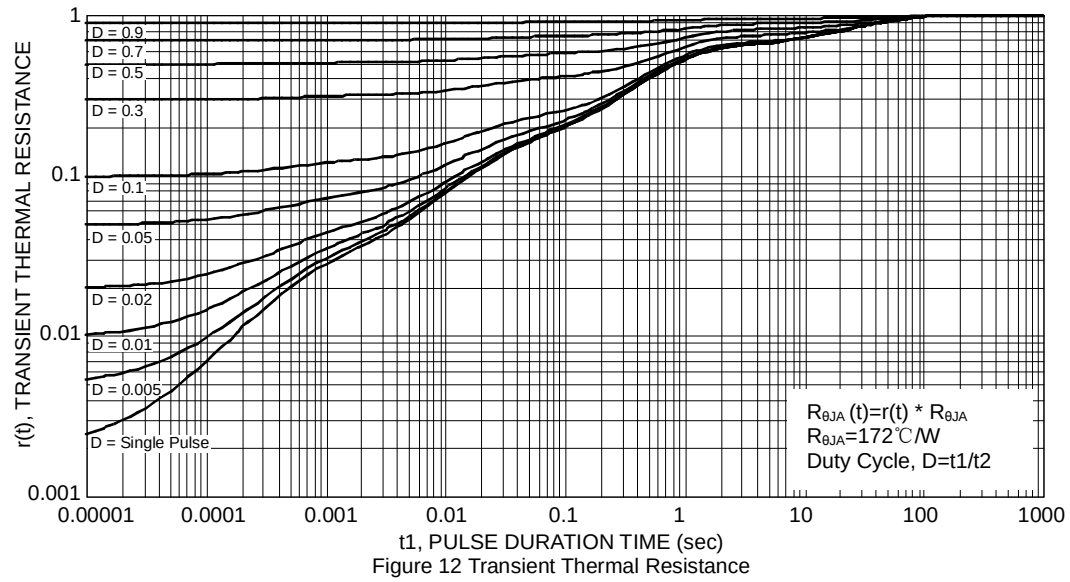
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 100V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	220	mΩ	V _{GS} = 10V, I _D = 1.6A
			—	250		V _{GS} = 4.5V, I _D = 1.3A
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 1.1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	401	—	pF	V _{DS} = 25V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	22	—		
Reverse Transfer Capacitance	C _{rss}	—	17	—		
Gate Resistance	R _g	—	2.1	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	4.1	—	nC	V _{DS} = 50V, I _D = 1.6A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	8.3	—		
Gate-Source Charge	Q _{gs}	—	1.5	—		
Gate-Drain Charge	Q _{gd}	—	2	—		
Turn-On Delay Time	t _{D(ON)}	—	6.8	—	ns	V _{DS} = 50V, V _{GS} = 4.5V, R _G = 6.8Ω, I _D = 1A
Turn-On Rise Time	t _R	—	8.2	—		
Turn-Off Delay Time	t _{D(OFF)}	—	7.9	—		
Turn-Off Fall Time	t _F	—	3.6	—		
Reverse Recovery Time	t _{RR}	—	17	—	ns	I _F = 1.1A, di/dt = 100A/μs
Reverse Recovery Charge	Q _{RR}	—	9.8	—	nC	

- Notes:
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to production testing.



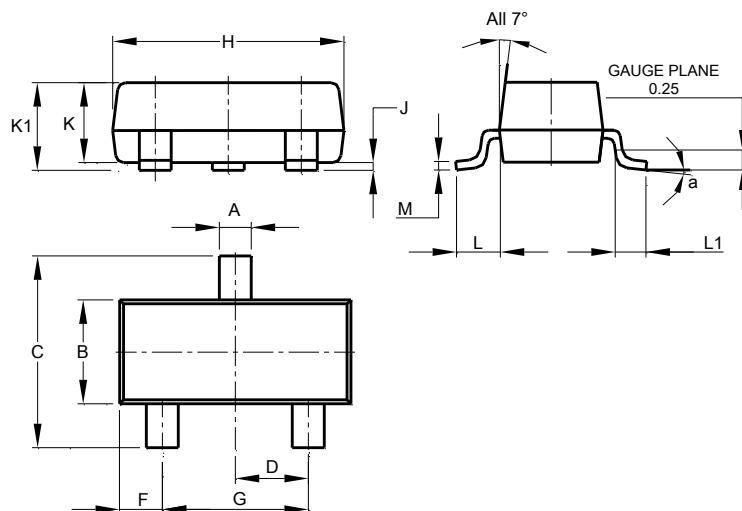




Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

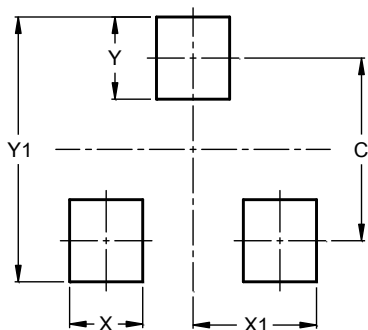


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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