



# PMV28ENE

30 V, N-channel Trench MOSFET

29 November 2021

Product data sheet

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Logic-level compatible
- Extended temperature range  $T_j = 175\text{ °C}$
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection  $> 1.5\text{ kV HBM (class H1C)}$

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

## 4. Quick reference data

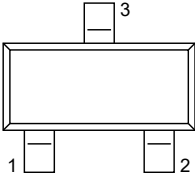
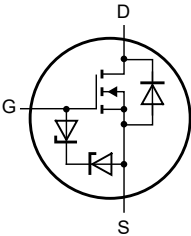
Table 1. Quick reference data

| Symbol                        | Parameter                        | Conditions                                                     |     | Min | Typ | Max | Unit |
|-------------------------------|----------------------------------|----------------------------------------------------------------|-----|-----|-----|-----|------|
| $V_{DS}$                      | drain-source voltage             | $T_j = 25\text{ °C}$                                           |     | -   | -   | 30  | V    |
| $V_{GS}$                      | gate-source voltage              |                                                                |     | -20 | -   | 20  | V    |
| $I_D$                         | drain current                    | $V_{GS} = 10\text{ V}; T_{amb} = 25\text{ °C}$                 | [1] | -   | -   | 4.4 | A    |
| <b>Static characteristics</b> |                                  |                                                                |     |     |     |     |      |
| $R_{DS(on)}$                  | drain-source on-state resistance | $V_{GS} = 10\text{ V}; I_D = 4.4\text{ A}; T_j = 25\text{ °C}$ |     | -   | 28  | 37  | mΩ   |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain  $6\text{ cm}^2$ .

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline                                                                         | Graphic symbol                                                                                   |
|-----|--------|-------------|--------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------|
| 1   | G      | gate        | <br>SOT23 | <br>017aaa255 |
| 2   | S      | source      |                                                                                            |                                                                                                  |
| 3   | D      | drain       |                                                                                            |                                                                                                  |

6. Ordering information

Table 3. Ordering information

| Type number | Package |                                                                                          |         |
|-------------|---------|------------------------------------------------------------------------------------------|---------|
|             | Name    | Description                                                                              | Version |
| PMV28ENE    | SOT23   | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23   |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| PMV28ENE    | %4D             |

[1] % = placeholder for manufacturing site code

## 8. Limiting values

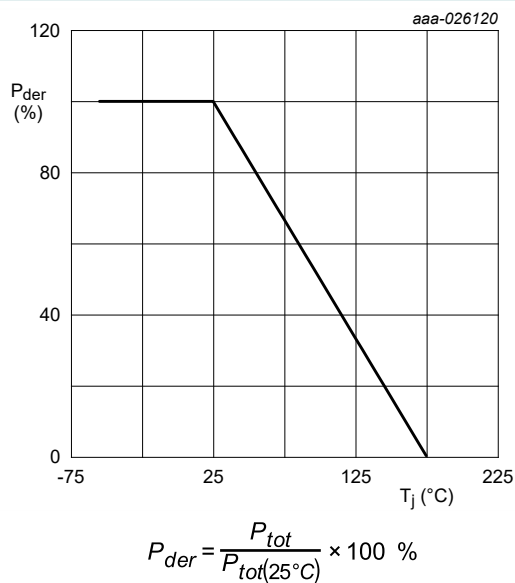
**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

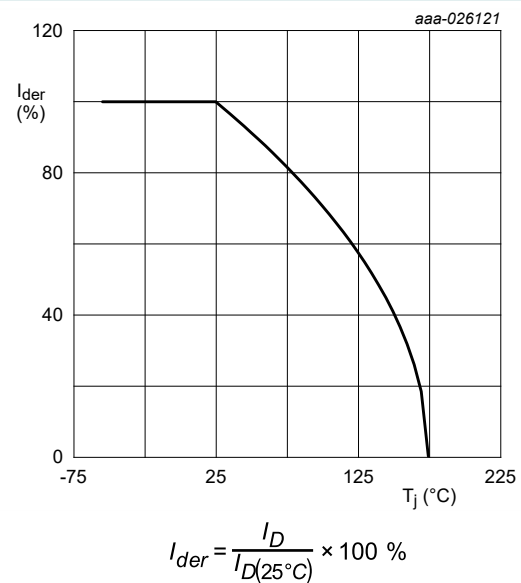
| Symbol                    | Parameter               | Conditions                                                     |     | Min | Max  | Unit |
|---------------------------|-------------------------|----------------------------------------------------------------|-----|-----|------|------|
| V <sub>DS</sub>           | drain-source voltage    | T <sub>j</sub> = 25 °C                                         |     | -   | 30   | V    |
| V <sub>GS</sub>           | gate-source voltage     |                                                                |     | -20 | 20   | V    |
| I <sub>D</sub>            | drain current           | V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C               | [1] | -   | 4.4  | A    |
|                           |                         | V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C              | [1] | -   | 3.1  | A    |
| I <sub>DM</sub>           | peak drain current      | T <sub>amb</sub> = 25 °C; single pulse; t <sub>p</sub> ≤ 10 μs |     | -   | 18   | A    |
| P <sub>tot</sub>          | total power dissipation | T <sub>amb</sub> = 25 °C                                       | [2] | -   | 660  | mW   |
|                           |                         |                                                                | [1] | -   | 1.25 | W    |
|                           |                         | T <sub>sp</sub> = 25 °C                                        |     | -   | 8.3  | W    |
| T <sub>j</sub>            | junction temperature    |                                                                |     | -55 | 175  | °C   |
| T <sub>amb</sub>          | ambient temperature     |                                                                |     | -55 | 175  | °C   |
| T <sub>stg</sub>          | storage temperature     |                                                                |     | -65 | 175  | °C   |
| <b>Source-drain diode</b> |                         |                                                                |     |     |      |      |
| I <sub>S</sub>            | source current          | T <sub>amb</sub> = 25 °C                                       | [1] | -   | 1.1  | A    |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

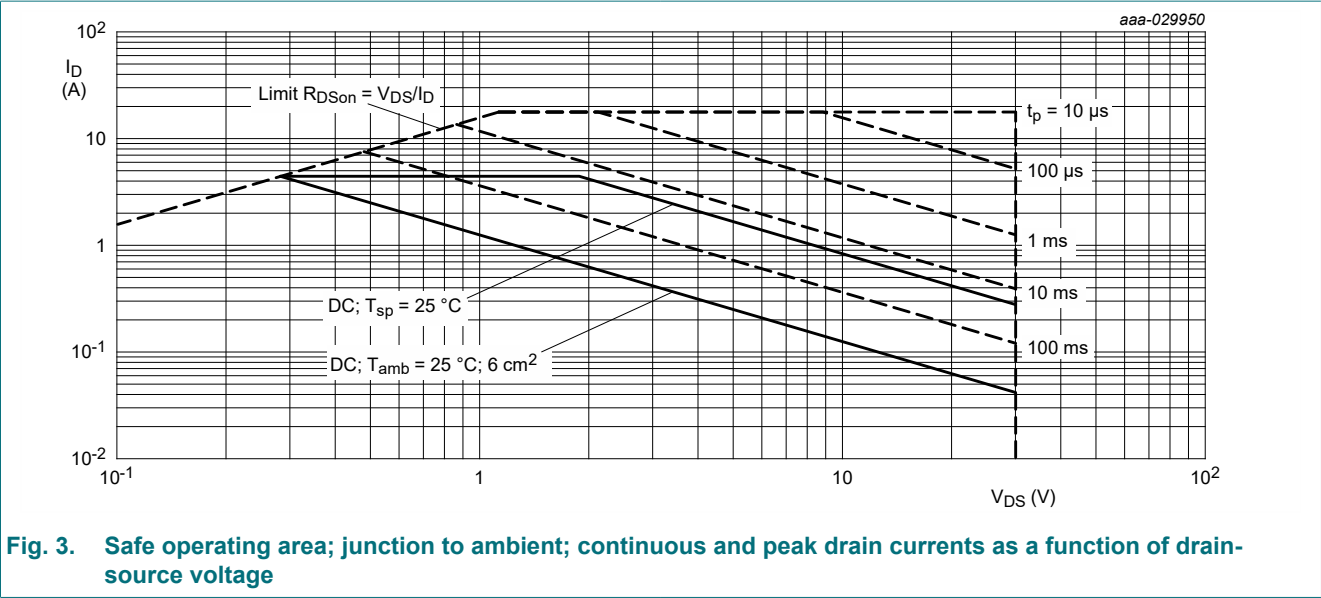
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



**Fig. 1. Normalized total power dissipation as a function of junction temperature**



**Fig. 2. Normalized continuous drain current as a function of junction temperature**



9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol         | Parameter                                        | Conditions  |     | Min | Typ | Max | Unit |
|----------------|--------------------------------------------------|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$  | thermal resistance from junction to ambient      | in free air | [1] | -   | 190 | 225 | K/W  |
|                |                                                  |             | [2] | -   | 100 | 120 | K/W  |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point |             |     | -   | 13  | 18  | K/W  |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

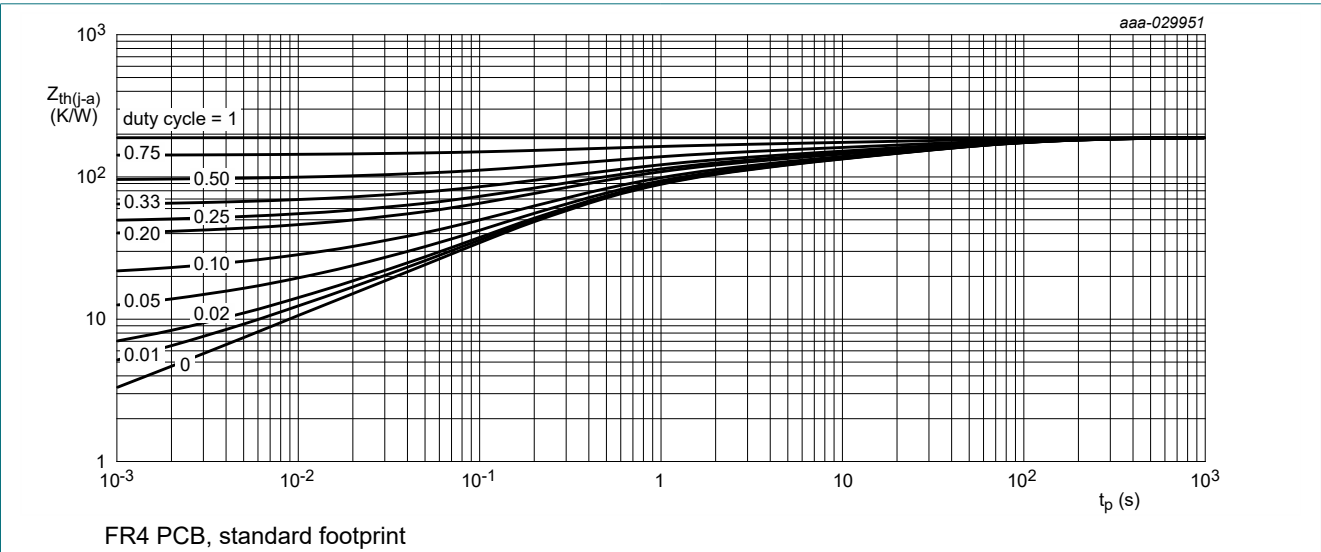


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

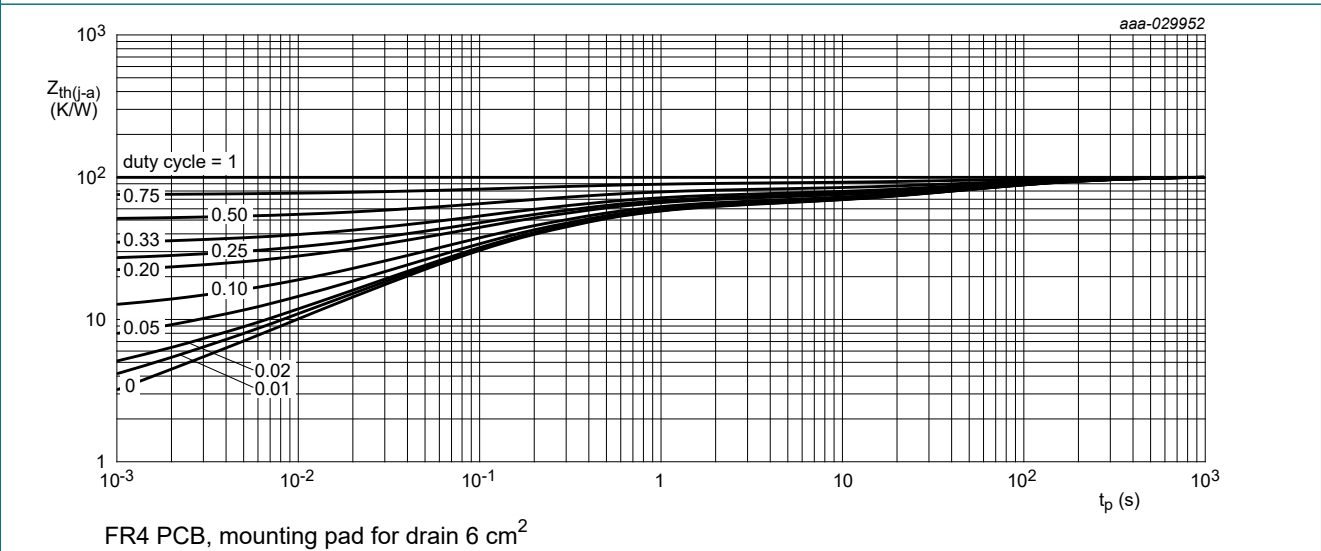


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

Table 7. Characteristics

| Symbol                  | Parameter                        | Conditions                                                                                                                |  | Min | Typ  | Max | Unit |
|-------------------------|----------------------------------|---------------------------------------------------------------------------------------------------------------------------|--|-----|------|-----|------|
| Static characteristics  |                                  |                                                                                                                           |  |     |      |     |      |
| V <sub>(BR)DSS</sub>    | drain-source breakdown voltage   | I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                    |  | 30  | -    | -   | V    |
| V <sub>GSth</sub>       | gate-source threshold voltage    | I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C                                       |  | 1   | 1.5  | 2.5 | V    |
| I <sub>DSS</sub>        | drain leakage current            | V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                     |  | -   | -    | 1   | μA   |
| I <sub>GSS</sub>        | gate leakage current             | V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                     |  | -   | -    | 10  | μA   |
|                         |                                  | V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                    |  | -   | -    | -10 | μA   |
|                         |                                  | V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                     |  | -   | -    | 2   | μA   |
|                         |                                  | V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                    |  | -   | -    | -2  | μA   |
| R <sub>DSon</sub>       | drain-source on-state resistance | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 4.4 A; T <sub>j</sub> = 25 °C                                                    |  | -   | 28   | 37  | mΩ   |
|                         |                                  | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 4.4 A; T <sub>j</sub> = 175 °C                                                   |  | -   | 48   | 64  | mΩ   |
|                         |                                  | V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 3.8 A; T <sub>j</sub> = 25 °C                                                   |  | -   | 36   | 51  | mΩ   |
| g <sub>fs</sub>         | forward transconductance         | V <sub>DS</sub> = 10 V; I <sub>D</sub> = 4.4 A; T <sub>j</sub> = 25 °C                                                    |  | -   | 12.5 | -   | S    |
| R <sub>G</sub>          | gate resistance                  | f = 1 MHz                                                                                                                 |  | -   | 2    | -   | Ω    |
| Dynamic characteristics |                                  |                                                                                                                           |  |     |      |     |      |
| Q <sub>G(tot)</sub>     | total gate charge                | V <sub>DS</sub> = 15 V; I <sub>D</sub> = 4.4 A; V <sub>GS</sub> = 10 V; T <sub>j</sub> = 25 °C                            |  | -   | 5.3  | 8   | nC   |
| Q <sub>GS</sub>         | gate-source charge               |                                                                                                                           |  | -   | 0.7  | -   | nC   |
| Q <sub>GD</sub>         | gate-drain charge                |                                                                                                                           |  | -   | 1.2  | -   | nC   |
| C <sub>iss</sub>        | input capacitance                | V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C                                          |  | -   | 266  | -   | pF   |
| C <sub>oss</sub>        | output capacitance               |                                                                                                                           |  | -   | 70   | -   | pF   |
| C <sub>rss</sub>        | reverse transfer capacitance     |                                                                                                                           |  | -   | 42   | -   | pF   |
| t <sub>d(on)</sub>      | turn-on delay time               | V <sub>DS</sub> = 15 V; I <sub>D</sub> = 4.4 A; V <sub>GS</sub> = 10 V; R <sub>G(ext)</sub> = 6 Ω; T <sub>j</sub> = 25 °C |  | -   | 3    | -   | ns   |
| t <sub>r</sub>          | rise time                        |                                                                                                                           |  | -   | 16   | -   | ns   |
| t <sub>d(off)</sub>     | turn-off delay time              |                                                                                                                           |  | -   | 10   | -   | ns   |
| t <sub>f</sub>          | fall time                        |                                                                                                                           |  | -   | 4    | -   | ns   |
| Source-drain diode      |                                  |                                                                                                                           |  |     |      |     |      |
| V <sub>SD</sub>         | source-drain voltage             | I <sub>S</sub> = 1.1 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C                                                     |  | -   | 0.7  | 1.2 | V    |

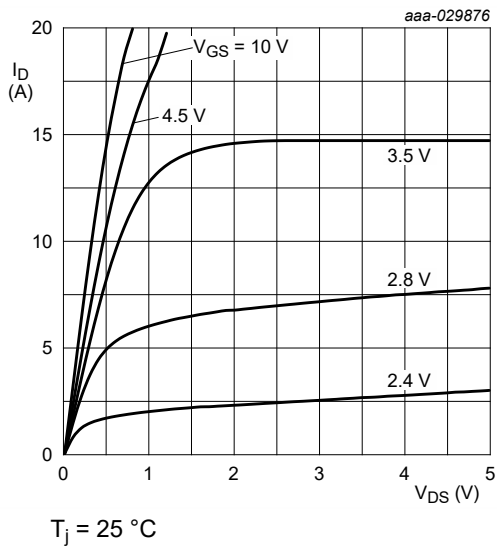


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

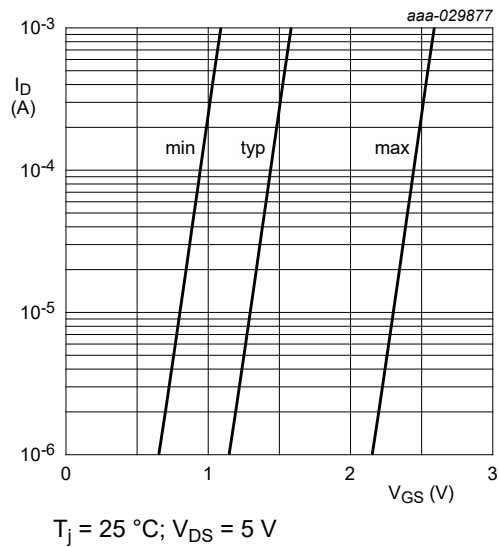


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

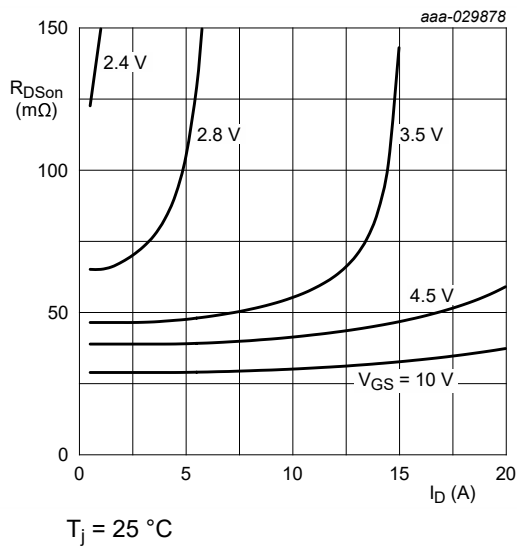


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

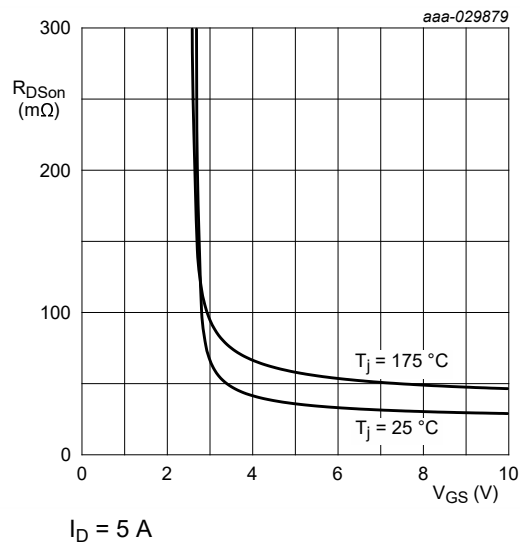
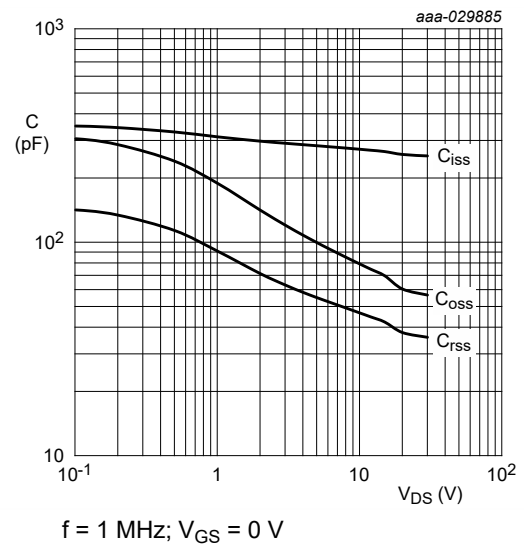
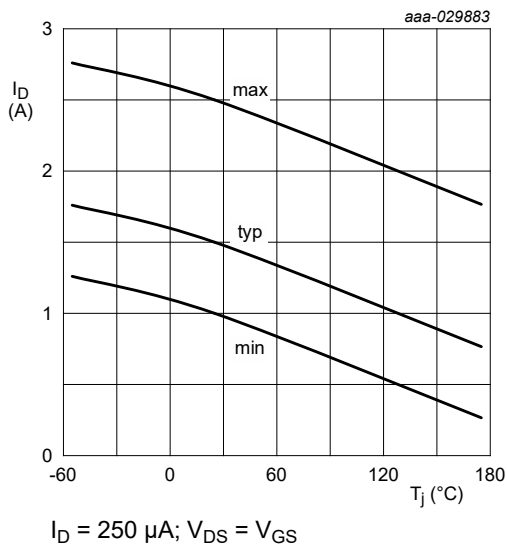
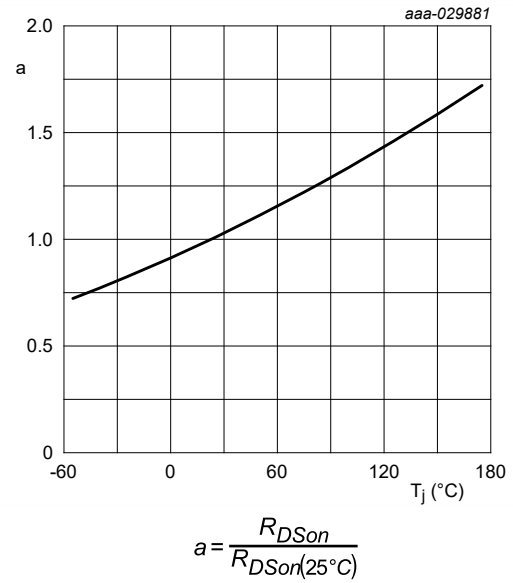
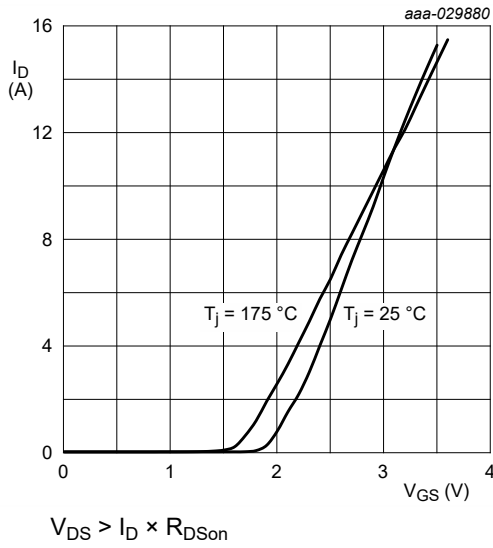


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



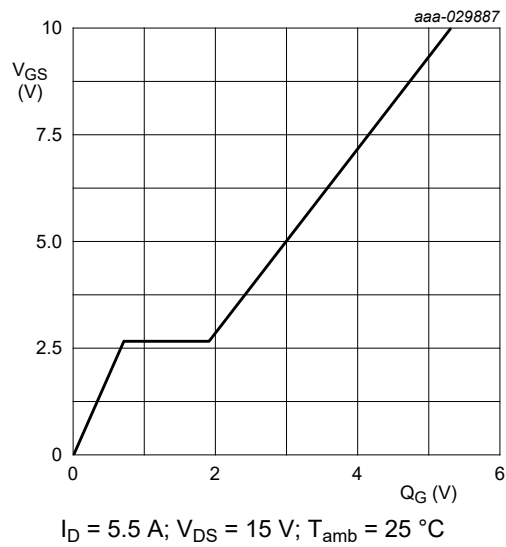


Fig. 14. Gate-source voltage as a function of gate charge; typical values

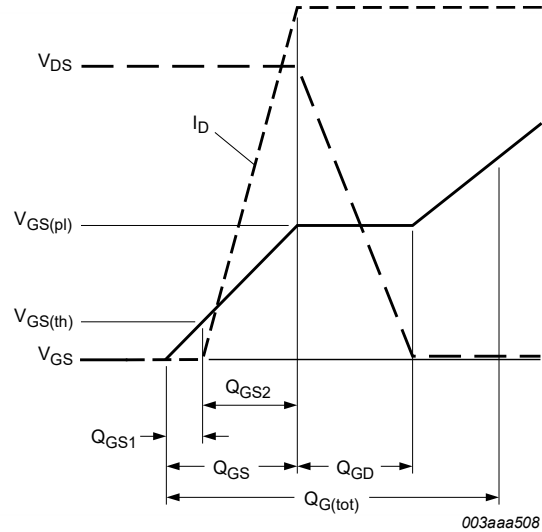


Fig. 15. Gate charge waveform definitions

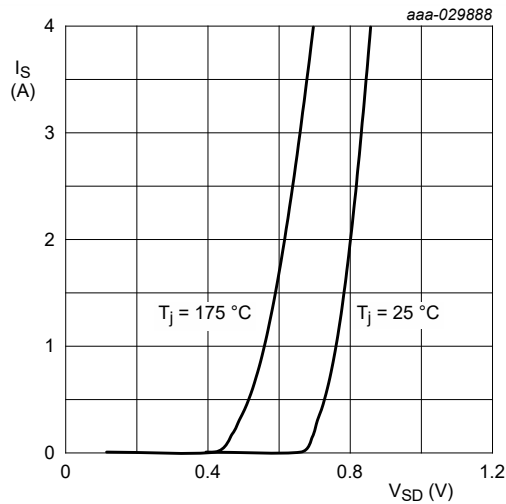


Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

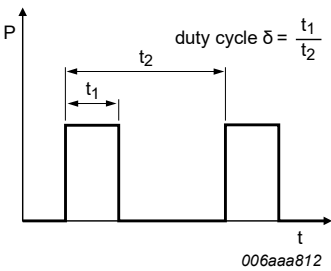


Fig. 17. Duty cycle definition

12. Package outline

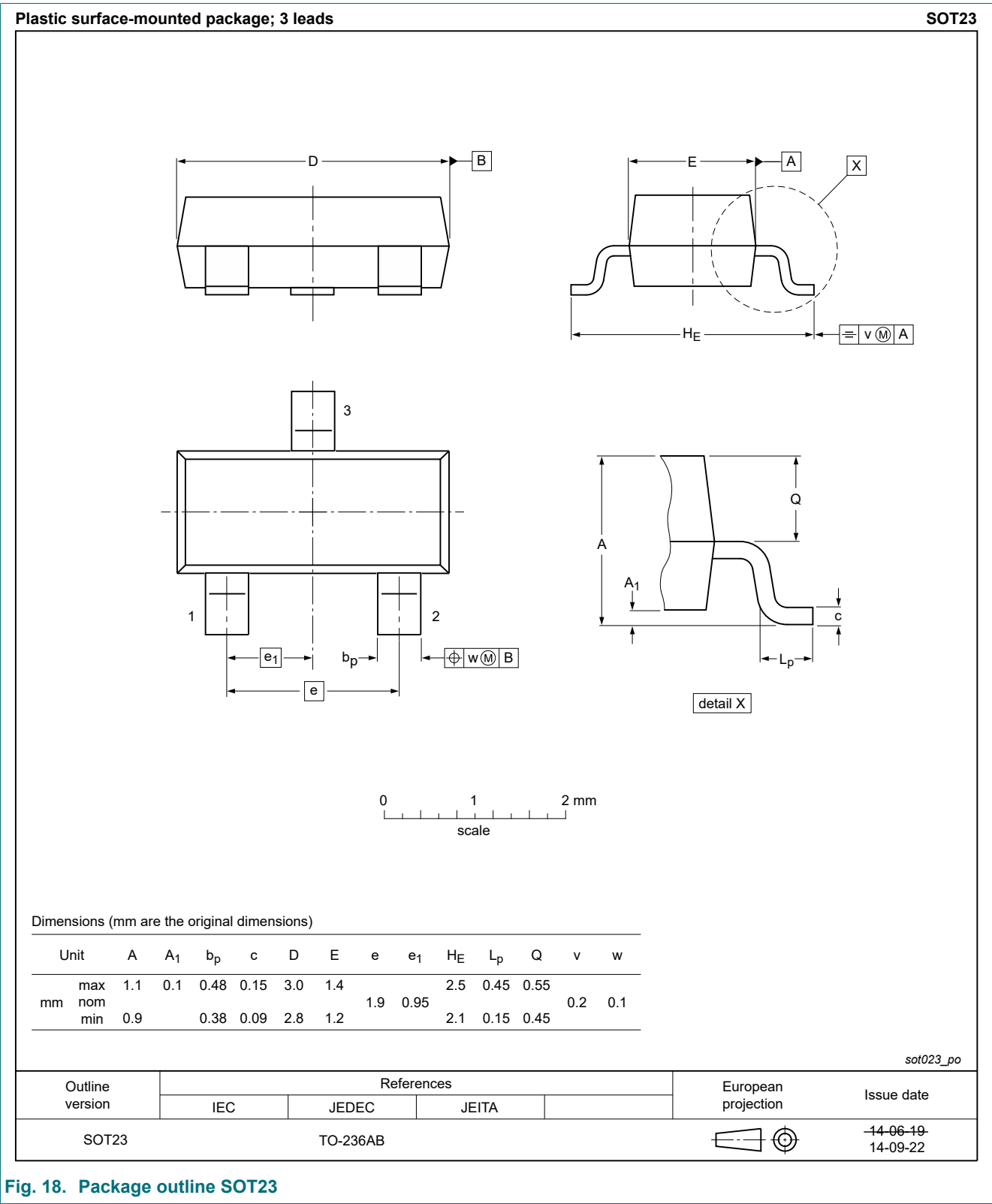


Fig. 18. Package outline SOT23

13. Soldering

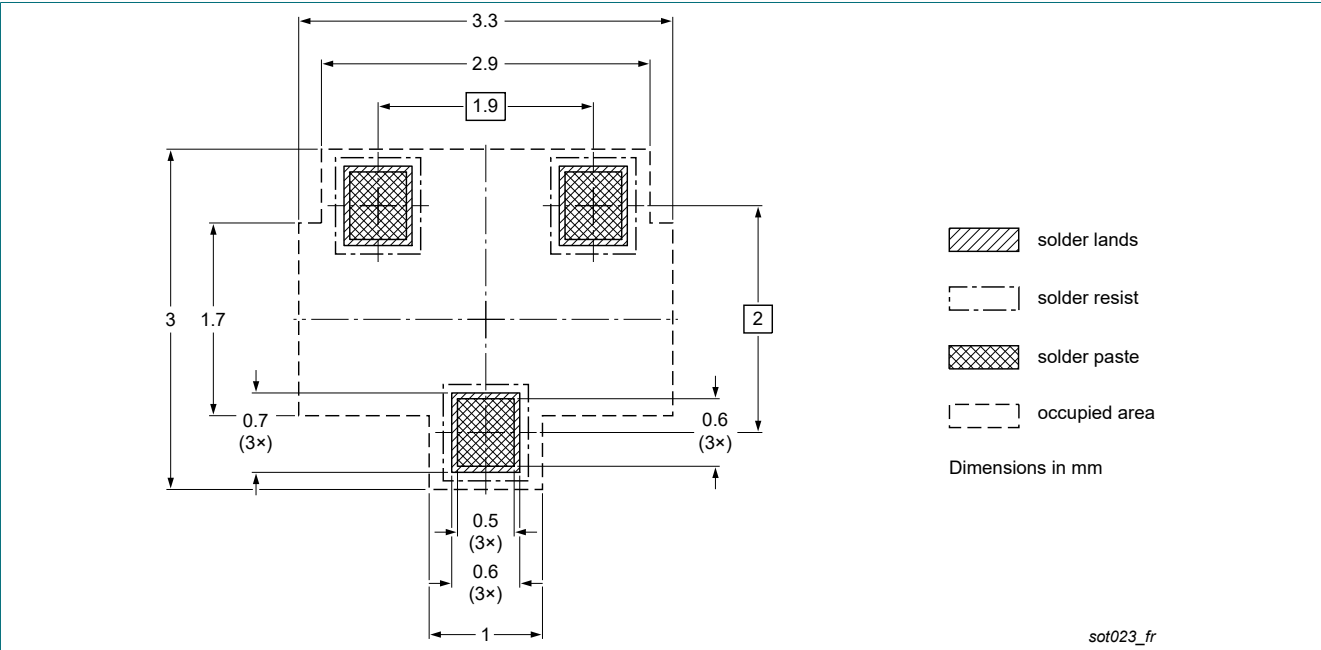


Fig. 19. Reflow soldering footprint for SOT23

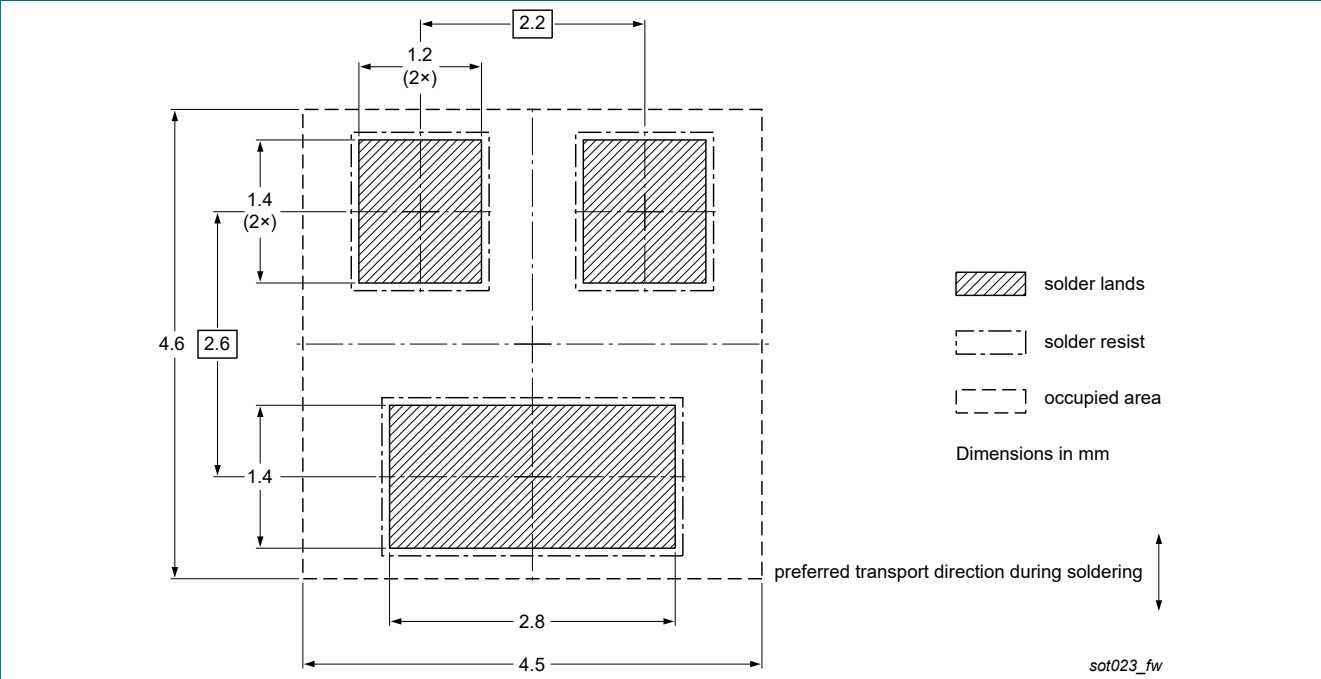


Fig. 20. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status  | Change notice | Supersedes |
|---------------|--------------|--------------------|---------------|------------|
| PMV28ENE v.1  | 20211129     | Product data sheet | -             | -          |

## 15. Legal information

### Data sheet status

| Document status<br>[1][2]      | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
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