



Features

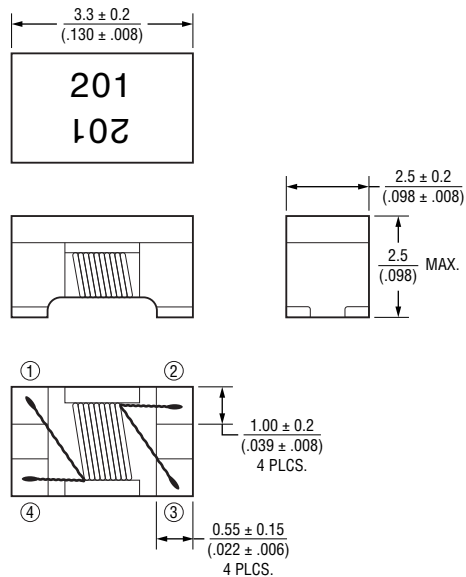
- Shielded construction – low radiation
- Bifilar wound
- Meets Open Alliance IEEE 100Base-T1 requirements
- AEC-Q200 compliant
- RoHS compliant* and halogen free**

SRF3225TABM Series – Common Mode Chip Inductor

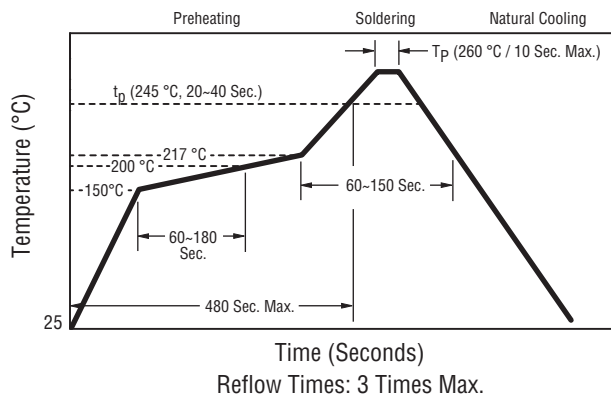
Electrical Specifications @ 25 °C

Bourns Part Number	Inductance @ 100 KHz / 0.1 V		Leakage Inductance (μ H) Typ. @ 100 kHz / 0.1 V	DCR (Ω) Max.	IDC (mA) Typ.	Cp 1-4 (pF) Max. 100 kHz / 0.1 V	Common Mode Impedance (Ω) @ 10 MHz	
	L (μ H)	Tol. (%)					Typ.	Min.
SRF3225TABM-201Y	200	+30/-10	0.40	5.5	70	20	10,000	7,000

Product Dimensions



Soldering Profile



WARNING
Cancer and Reproductive Harm
www.P65Warnings.ca.gov

* RoHS Directive 2015/863, Mar 31, 2015 and Annex.

** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less.

Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

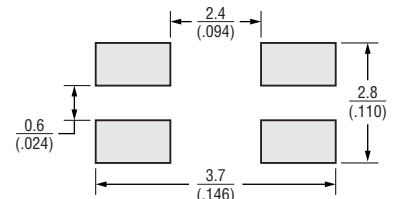
General Specifications

Rated Voltage 80 VDC
Insulation Resistance
..... 10 M Ω (min.) @ 50 VDC
Operating Temperature
..... -40 °C to +125 °C
(Temperature rise included)
Storage Temperature
..... -40 °C to +125 °C
Temperature Rise 40 °C at rated IDC

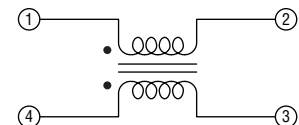
Materials

Core Ferrite
Wire Enameled copper
Terminal Finish Sn
Packaging 2000 pcs. per 7-inch reel

Recommended Layout



Electrical Schematic



How to Order

SRF3225TABM - 201 Y

Model
Value Code
Tolerance

BOURNS®

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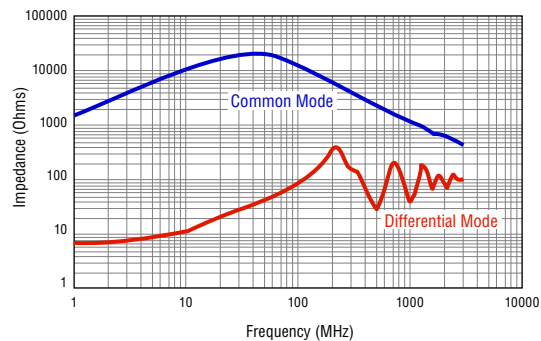
The Americas: Tel: +1-951 781-5500
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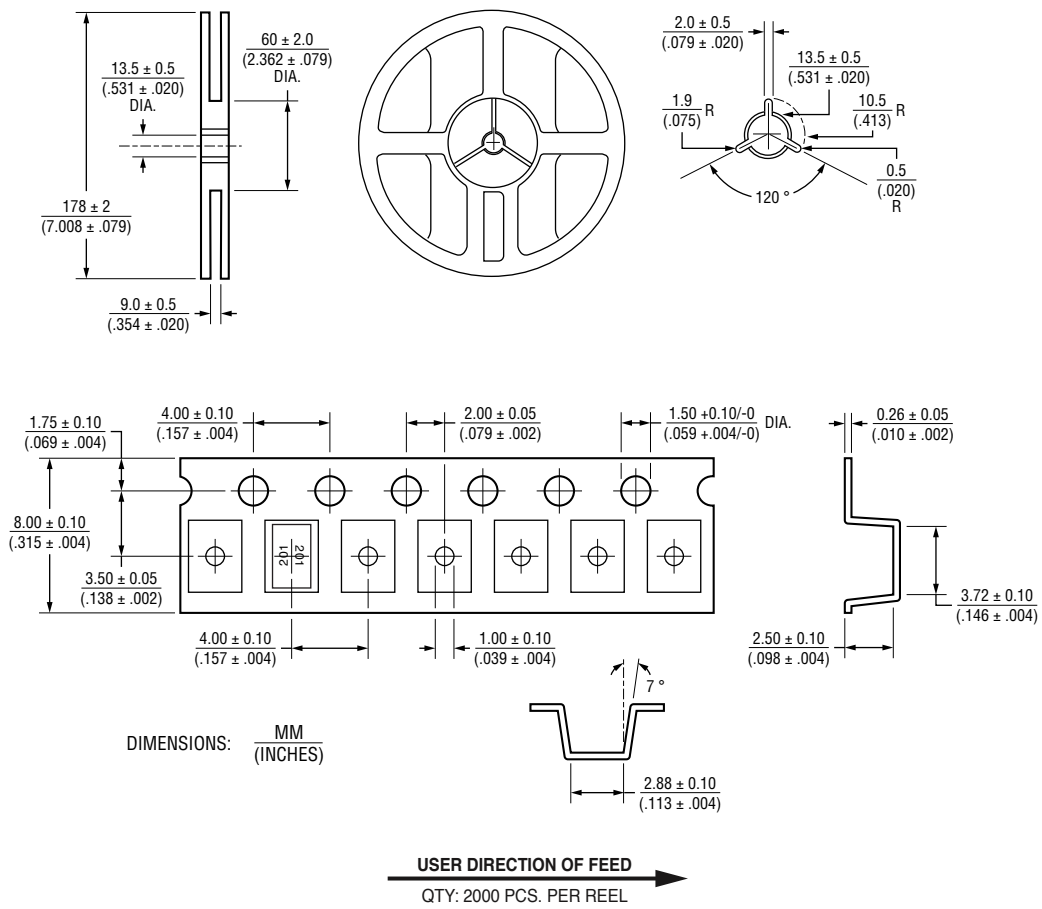
SRF3225TABM Series – Common Mode Chip Inductor

BOURNS®

Typical Impedance vs. Frequency Curves



Packaging Specifications



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