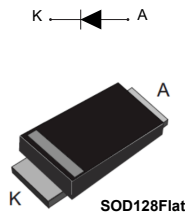


## 100 V - 3 A power Schottky trench diode



### Features

- ST trench process
- High junction temperature capability
- Low forward voltage drop
- Low recovery charges
- Reduces conduction, reverse and switching losses
- Flat package
- ECOPACK2 compliant

### Applications

- DC/DC converter
- Auxiliary power supply
- High switching frequency converter
- Flyback topology
- Reverse polarity protection
- Freewheeling function

### Description

This 3 A, 100 V rectifier is based on ST trench technology that achieves the best in class  $V_F/I_R$  trade-off for a given silicon surface.

Integrated in flat packages, this STPST3H100 trench device is intended to be used in high frequency miniature switched mode power supplies such as adaptors. It is also an ideal candidate for auxiliary power supply in telecom, server, lighting or smart metering and can be the perfect companion device to our VIPer products.



#### Product status link

[STPST3H100](#)

#### Product summary

$I_{F(AV)}$	3 A
$V_{RRM}$	100 V
$T_j$ (max.)	175 °C
$V_F$ (typ.)	0.600 V

# 1 Characteristics

**Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified)**

Symbol	Parameter		Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage		100	V
I <sub>F(AV)</sub>	Average forward current, $\delta = 0.5$ square wave	T <sub>I</sub> = 130 °C <sup>(1)</sup>	3	A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms sinusoidal	50	A
T <sub>stg</sub>	Storage temperature range		-65 to +175	°C
T <sub>j</sub>	Maximum operating junction temperature <sup>(2)</sup>		+175	°C

1. Value based on R<sub>th(j-l)</sub>(max).

2. (dP<sub>tot</sub>/dT<sub>j</sub>) < (1/R<sub>th(j-a)</sub>) condition to avoid thermal runaway for a diode on its own heatsink.

**Table 2. Thermal resistance parameter**

Symbol	Parameter		Typ. value	Unit
R <sub>th(j-l)</sub>	Junction to lead	SOD128Flat	13	°C/W

For more information, please refer to the following application note:

- AN5088: Rectifiers thermal management, handling and mounting recommendations

**Table 3. Static electrical characteristics**

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 125 °C	V <sub>R</sub> = 70 V	-	0.45	1.4	mA
		T <sub>j</sub> = 25 °C	V <sub>R</sub> = 100 V	-		5.7	μA
		T <sub>j</sub> = 125 °C		-	0.8	3.0	mA
V <sub>F</sub> <sup>(2)</sup>	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 1.5 A	-	0.565	0.625	V
		T <sub>j</sub> = 125 °C		-	0.500	0.555	
		T <sub>j</sub> = 25 °C	I <sub>F</sub> = 3 A	-	0.685	0.755	
		T <sub>j</sub> = 125 °C		-	0.600	0.650	

1. Pulse test: t<sub>p</sub> = 5 ms,  $\delta < 2\%$

2. Pulse test: t<sub>p</sub> = 380 μs,  $\delta < 2\%$

To evaluate the conduction losses, use the following equation:

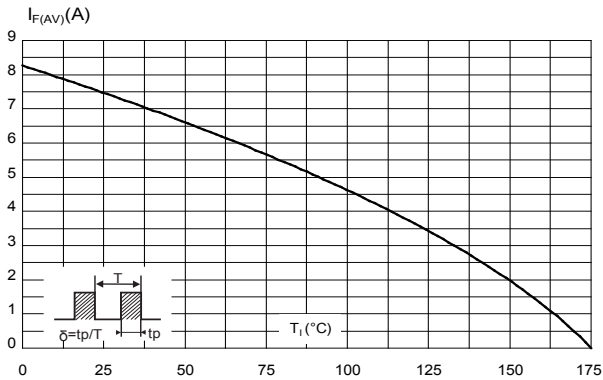
$$P = 0.46 \times I_{F(AV)} + 0.0633 \times I_{F(RMS)}^2$$

For more information, please refer to the following application notes related to the power losses :

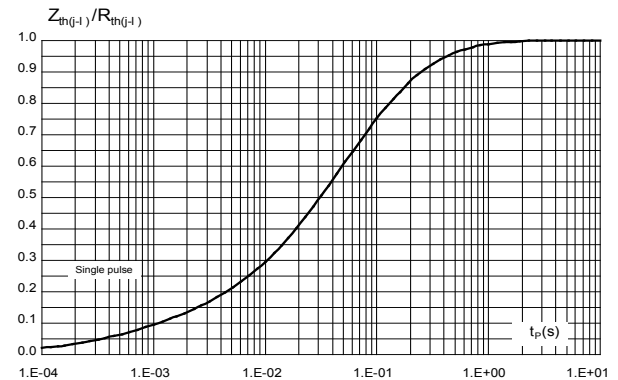
- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses on a power diode

## 1.1 Characteristics (curves)

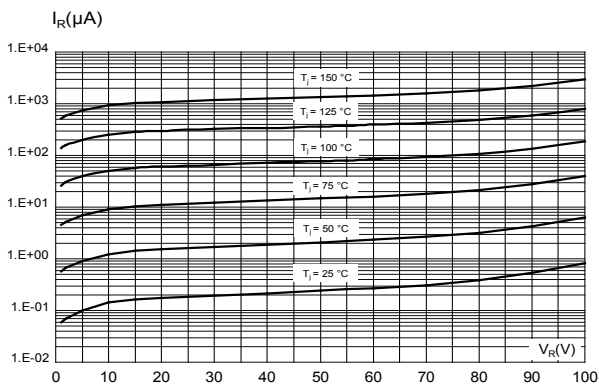
**Figure 1. Average forward current versus lead temperature ( $\delta = 0.5$ )**



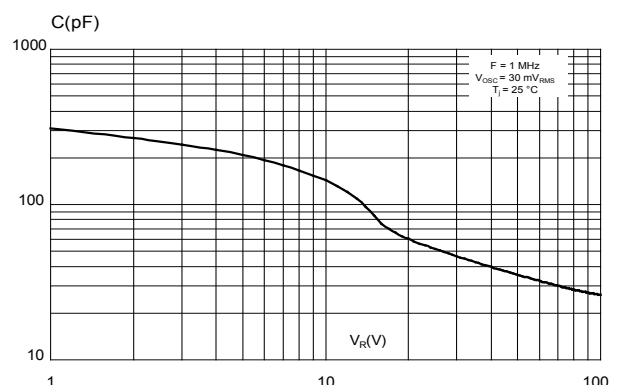
**Figure 2. Relative variation of thermal impedance junction to lead versus pulse duration**



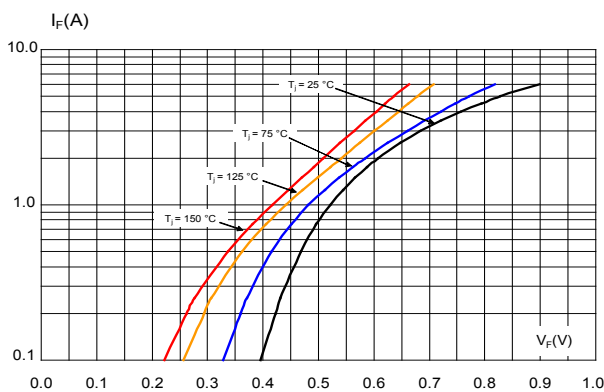
**Figure 3. Reverse leakage current versus reverse voltage applied (typical values)**



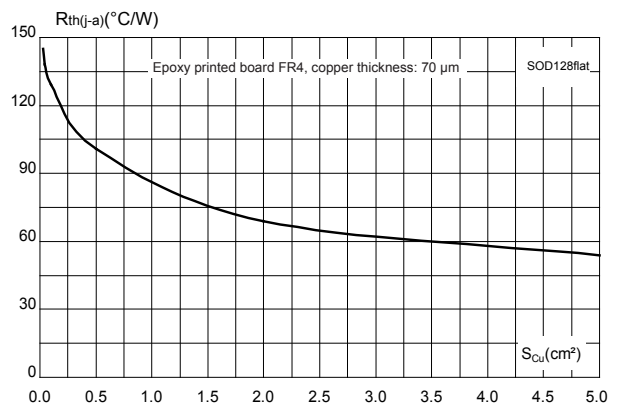
**Figure 4. Junction capacitance versus reverse voltage applied (typical values)**



**Figure 5. Forward voltage drop versus forward current (typical values)**



**Figure 6. Thermal resistance junction to ambient versus copper surface under each lead (typical values, epoxy printed board FR4,  $e_{Cu} = 70 \mu\text{m}$ )**



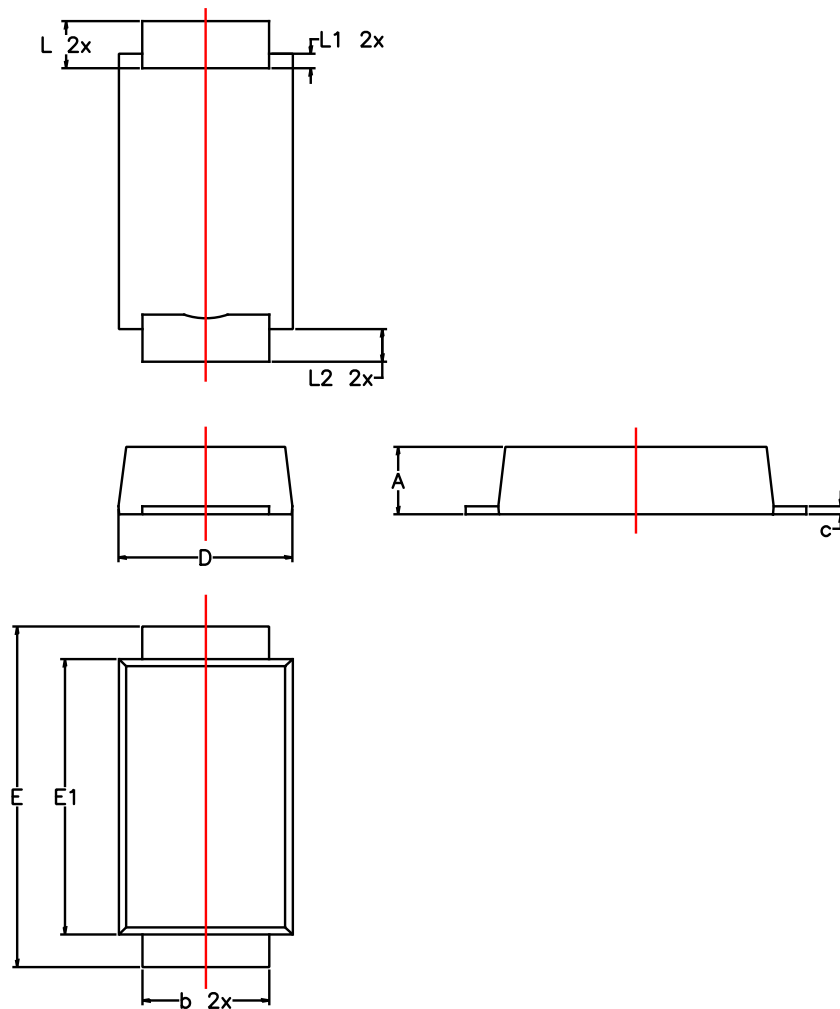
## 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of **ECOPACK** packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK is an ST trademark.

### 2.1 SOD128Flat package information

- Epoxy meets UL94, V0
- Lead-free package

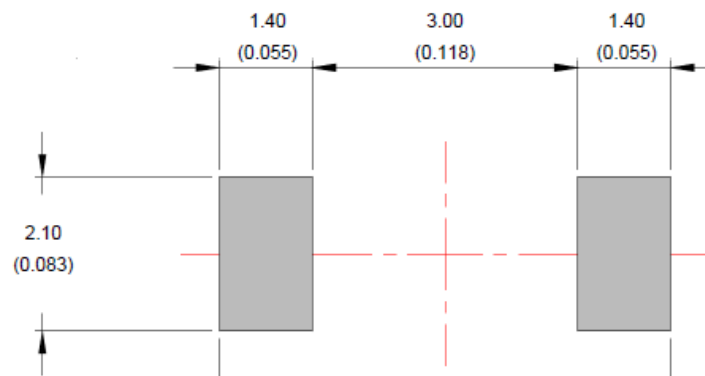
**Figure 7. SOD128Flat package outline**



**Table 4. SOD128Flat package mechanical data**

Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.93	1.03	0.037	0.041
b	1.69	1.81	0.067	0.071
c	0.10	0.22	0.004	0.009
D	2.30	2.50	0.091	0.098
E	4.60	4.80	0.181	0.189
E1	3.70	3.90	0.146	0.154
L	0.55	0.85	0.026	0.033
L1	0.30 typ.		0.012 typ.	
L2	0.45 typ.		0.018 typ.	

**Figure 8. SOD128Flat footprint in mm (inches)**



*Note:* For package and tape orientation, reel and inner box dimensions and tape outline please check [TN1173](#)

### 3 Ordering information

**Table 5. Ordering information**

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPST3H100AF	T3H1	SOD128Flat	26.4 mg	3000	Tape and reel

## Revision history

**Table 6. Document revision history**

Date	Version	Changes
28-Jan-2021	1	Initial release.

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