

MC21605FA6WE-BNMLW	2 x 16	5mm Character Height	LCD Module
Specification			
Version: 2		Date: 15/10/2020	
Revision			
1	23/07/2020	First Issue	
2	14/10/2020	Updated Electrical-Optical Characteristics	

Display Features			
Character Count	2 x 16		
Appearance	White on Blue		
Logic Voltage	5V		
Interface	Parallel		
Font Set	English / European		
Display Mode	Transflective		
Character Height	4.67mm		
LC Type	FSTN		
Module Size	59.00 x 29.00 x 5.50mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COB		
LED Backlight	White		
		Box Quantity	Weight / Display

* - For full design functionality, please use this specification in conjunction with the ST7065 specification. (Provided Separately)

Display Accessories	
Part Number	Description
MCCMDB-16SIL	LCD Interconnect board, can be driven from either a PC or a single Board computer with a USB output.
MCCBL1A16SLIP-16DILS-150	16 Way, Single in-line to Dual In-line connector Cable.
MCCBL1A16SLIP-16SILS-150	16 Way, Single in-line to Single In-line connector Cable.

Optional Variants		
Fonts	Appearances	Voltage

1. FUNCTIONS & FEATURES

- Viewing Direction : 6 O'clock
- Driving Scheme : 1/16 Duty Cycle, 1/5 Bias
- Power Supply Voltage : 5V
- VLCD : 3.71V
- Display contents : 16 Characters x 2 line
- Internal Memory : CGROM (13200bits)
: CGRAM (64 x 8bits)
: DDRAM (80 x 8 bits for Digits)
: CGROM of the ST7066U-0B
- CGROM
- Easy Interface with a 4-bit or 8-bit MPU
- RoHS Compliant

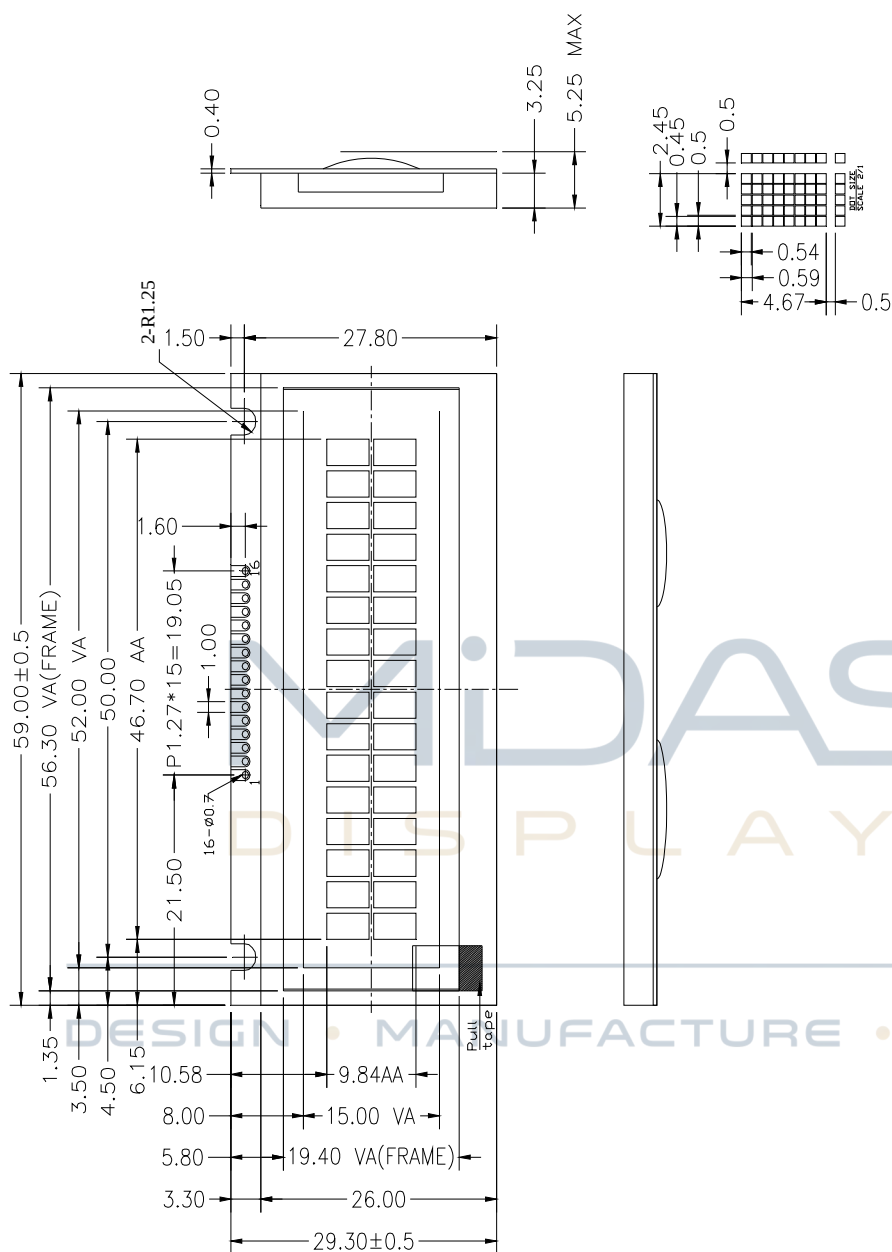
2. MECHANICAL SPECIFICATIONS

- Module Size : 59(L) x29(W) x 5.5(H)mm
- Viewing area : 52(L) x15 (W) mm
- Active area : 46.7(L) x 9.84(W) mm
- Dot Size : 0.45 (L) x 0.54 (W) mm
- Dot Gap : 0.50mm

DESIGN • MANUFACTURE • SUPPLY



EXTERNAL DIMENSIONS



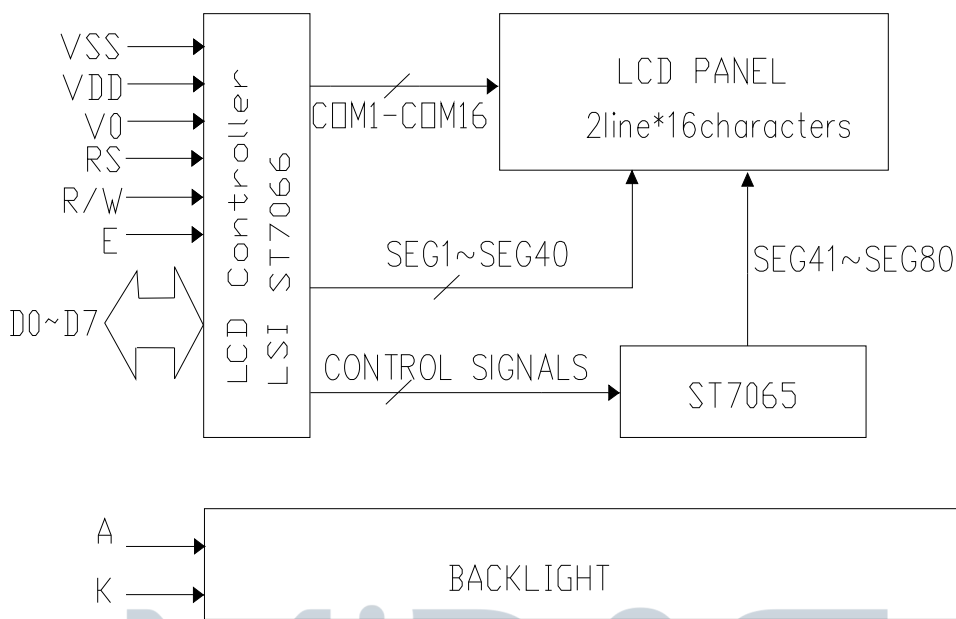
REMARKS:

1. Unmarked tolerance is ± 0.3

2. All materials comply with RoHS



BLOCK DIAGRAM



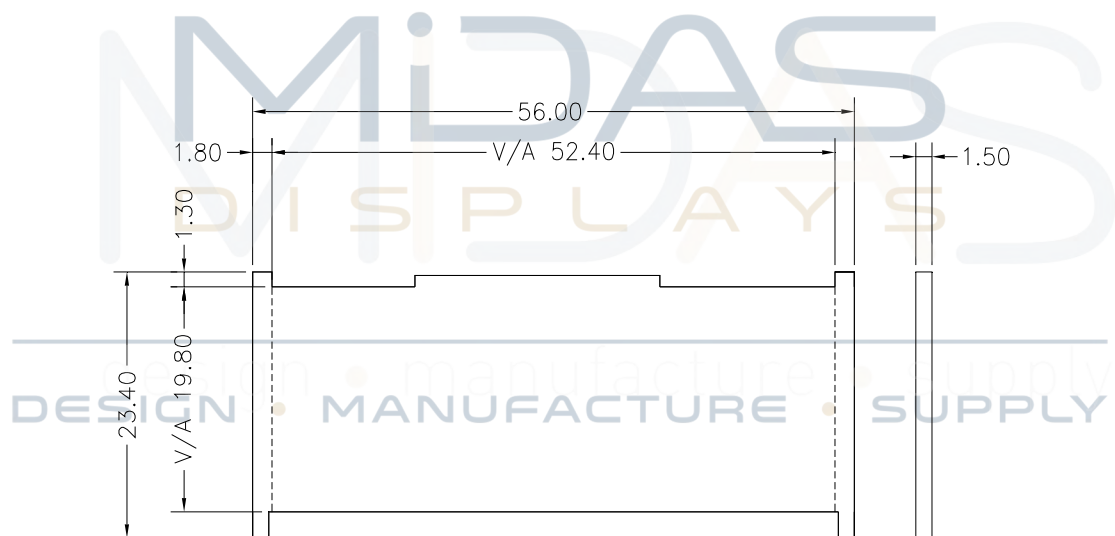
PIN ASSIGNMENT

Pin No.	Symbol	Function
1	VLED(-)	Power supply for B/L(-)
2	VSS	Ground
3	VDD	Supply Voltage for logic
4	V0	Operating voltage for LCD
5	RS	H:DATA,L:instruction code
6	R/W	H:Read(Module→MPU) L: (MPU→Module)
7	E	Chip enable signal
8	DB0	Date bus line
9	DB1	
10	DB2	
11	DB3	
12	DB4	
13	DB5	
14	DB6	
15	DB7	
16	A	LED+

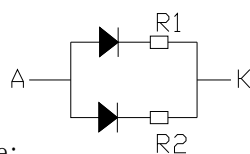


Electrical-Optical Characteristics

Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forward Curret(B00)	If		2		mA	Vf=3.2 V
Forward Curret(B01)	If	5*2	10*2	20*2	mA	
Colour Coordinate	x	0.23	0.26	0.33		
	y	0.23	0.26	0.33		
Uniformity	Avg		70		%	
Luminance(B00)	Lv		150		cd/m ²	
Luminance(B01)	Lv	400	600		cd/m ²	



Curcuit Diagram



Note:

BNMLW:R1=R2=100Ω

REMARKS:

1, UNMARKED TOLERANCE IS ± 0.3 ,

2, COLOR: WHITE,

3, THE MATERIAL COMPLY WITH

ROHS. 4, LED ON THE MODULE PCB



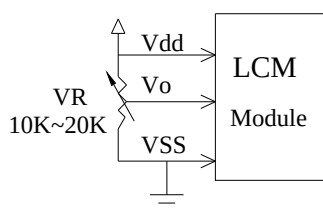
MAXIMUM ABSOLUTE POWER RATINGS

Item	Symbol	Standard value	Unit
Supply voltage for logic	$V_{DD} - V_{SS}$	-0.3~+7.0	V
Supply voltage for LCD	$V_{LCD} - V_{SS}$	-0.3-13	V
Input voltage	V_{IN}	$V_{SS} \sim V_{DD}$	V
Operating temperature	T_{opr}	-20~+70	°C
Storage temperature	T_{stg}	-30~+80	°C

DC CHARACTERISTICS

Item	Symbol	Standard Value			Test Condition	Unit
		MIN	TYP	MAX		
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	5	—	—	V
Supply Voltage For LCD *Note	$V_{DD}-V_0$	—	—	—	$T_a=-20^{\circ}\text{C}$	V
		—	3.71	—	$T_a=25^{\circ}\text{C}$	V
		—	—	—	$T_a=70^{\circ}\text{C}$	V
Input High Volt.	V_{IH}	0.7 V_{DD}	—	V_{DD}	—	V
Input Low Volt.	V_{IL}	V_{SS}	—	0.6	—	V
Output High Volt.	V_{OH}	3.9	—	V_{DD}	—	V
Output Low Volt.	V_{OL}	0	—	0.4	—	V
Supply Current	I_{DD}	1.0	1.2	1.5	$V_{DD}=5\text{V}$	mA

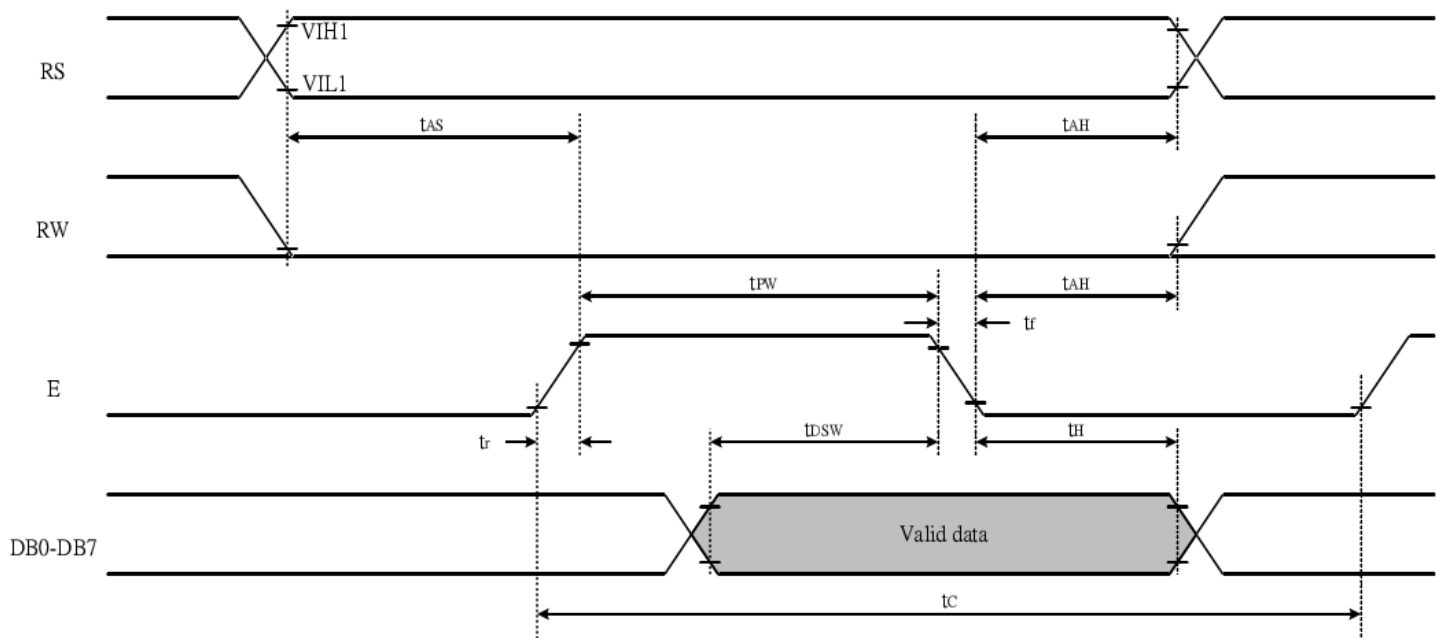
* Note: Please design the VOP adjustment circuit on customer's main board



AC CHARACTERISTICS

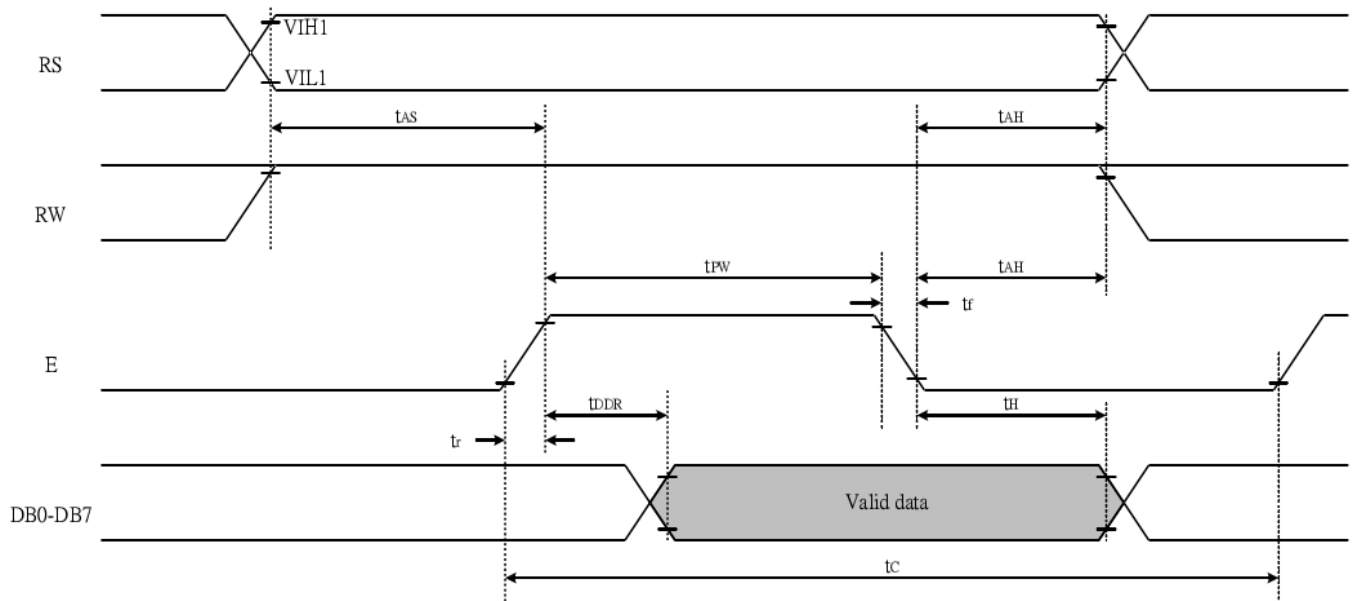
Write mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	t_C	1200	---	---	ns	E
Enable Pulse Time	T_{PW}	140	---	---	ns	E
Enable Rise/Fall Time	T_R, T_F	---	---	25	ns	E
Address Set-up Time	T_{AS}	0	---	---	ns	RW,RS,E
Address Hold Time	T_{AH}	10	---	---	ns	RW,RS,E
Data Set-up Time	T_{DSW}	40	---	---	ns	DB0~DB7
Data Hold Time	T_H	10	---	---	ns	DB0~DB7



Read Mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	t_C	1200	---	---	ns	E
Enable Pulse Time	T_{PW}	140	---	---	ns	E
Enable Rise/Fall Time	T_R, T_F	---	---	25	ns	E
Address Set-up Time	T_{AS}	0	---	---	ns	RW,RS,E
Address Hold Time	T_{AH}	10	---	---	ns	RW,RS,E
Data Set-up Time	T_{DDR}	---	---	100	ns	DB0~DB7
Data Hold Time	T_H	10	---	---	ns	DB0~DB7



MiDAS

DISPLAYS

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STANDARD CHARACTER PATTERN (ST7066U-0B)

NO.7066-0B

b7-b4 b3-b0		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)	±		0	0	P	'	P	G	E	A		I	A	B	T	
0001	(2)	≡	!	1	A	Q	a	9	Q	æ	i		J	+	y	U	
0010	(3)	7	"	2	B	R	b	r	e	E	ô	*	o	S	ô	X	
0011	(4)	Δ	#	3	C	S	c	s	a	ô	G		P	7	e	ψ	
0100	(5)	Y	\$	4	D	T	d	t	a	ô	¢		4	P	Z	o	
0101	(6)	Y	%	5	E	U	e	u	a	ô	E	h	†	Δ	η	7	
0110	(7)	Y	&	6	F	V	f	v	a	ô	¥	4	↓	0	0	*	
0111	(8)	J	'	7	G	W	g	w	G	U	R	X	→	Δ	L	4	
1000	(1)	J	(	8	H	X	h	x	e	9	÷	÷	÷	E	K	R	
1001	(2)	J	)	9	I	Y	i	y	e	0	i	Σ	Γ	Π	Λ	4	
1010	(3)	×	*	:	J	Z	j	z	e	0	Δ	Δ	7	Σ	μ	F	
1011	(4)	J	+	;	K	L	k	l	i	R	Δ	×	L	†	v	4	
1100	(5)	=	,	<	L	\	l	l	i	N	ô	×	J	Φ	Z	Q	
1101	(6)	o	-	=	M	I	m	]	i	Δ	ô	*	*	ψ	π	-	
1110	(7)	E	.	>	N	^	n	^	Δ	Q	Q	†	0	Q	p	Q	
1111	(8)	E	/	?	O	_	o	Δ	Δ	ô	Q	†	0	o	σ	Q	

INSTRUCTION TABLE

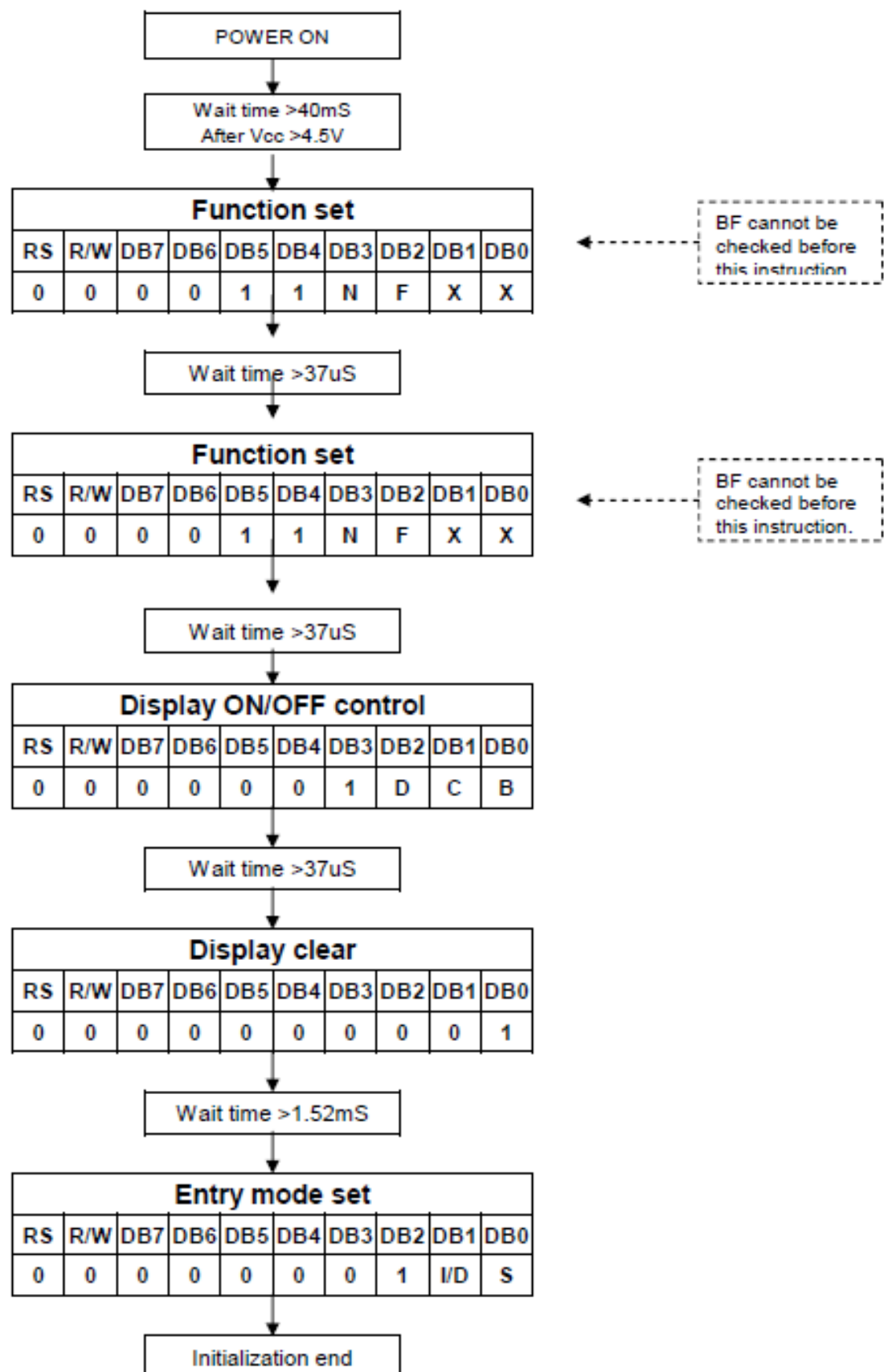
Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz)	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write "20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	x	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	C	B	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	N	F	x	x	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	0us	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

Note:

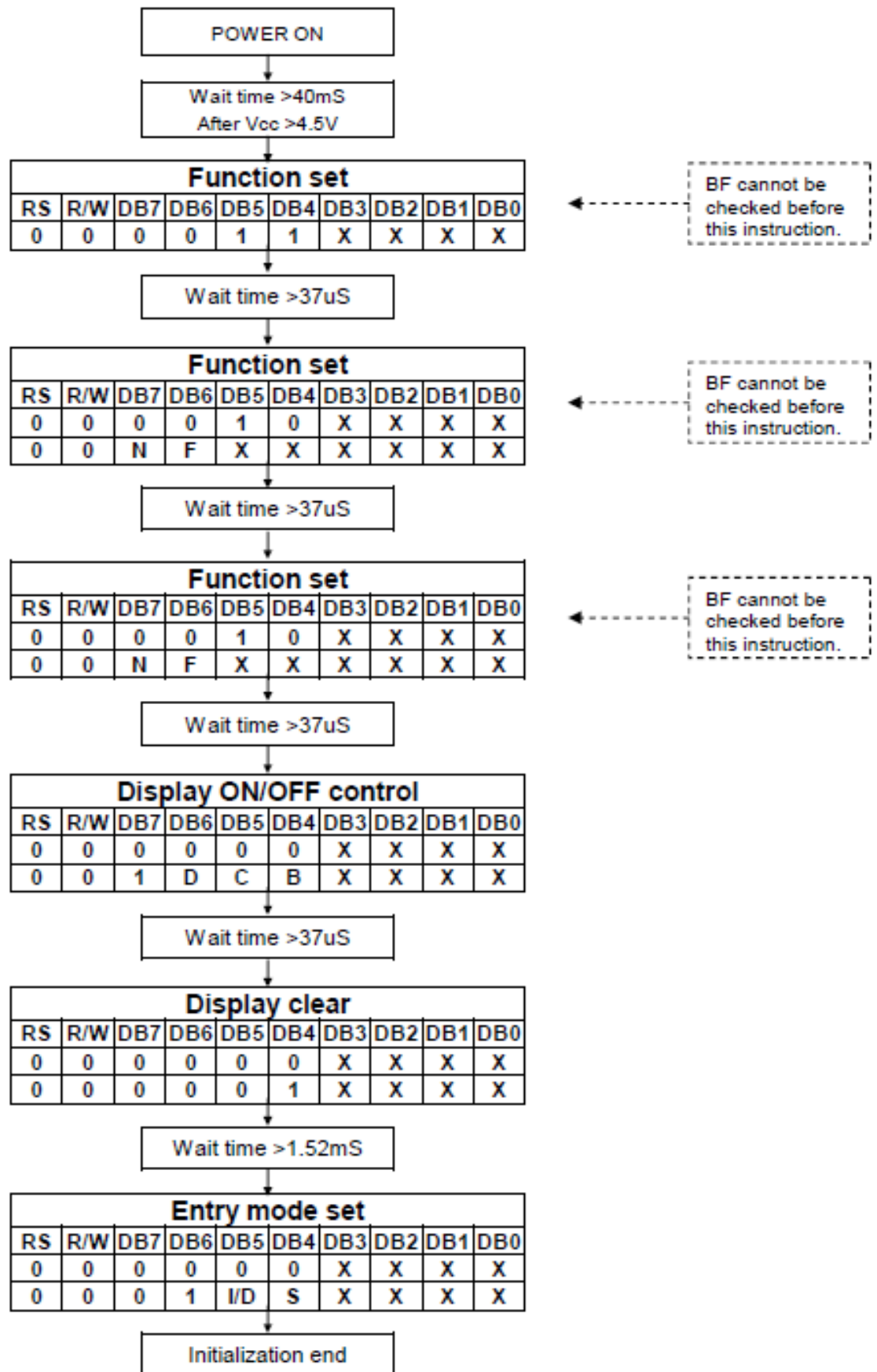
Be sure the ST7066U is not in the busy state (BF=00) before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

RESET FUNCTION

8-bit Interface ($f_{osc}=270\text{KHz}$)



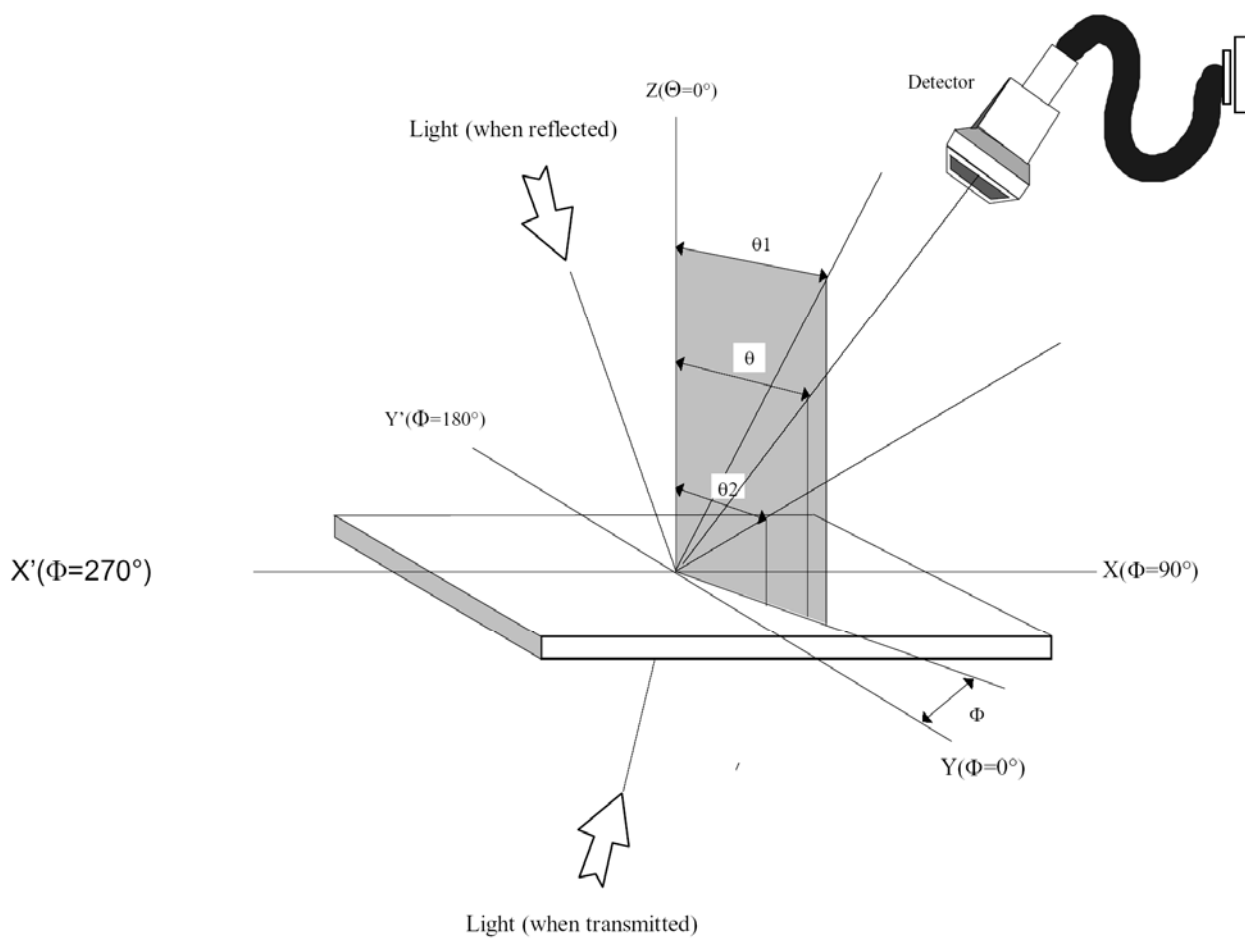
4-bit Interface (fosc=270KHz)



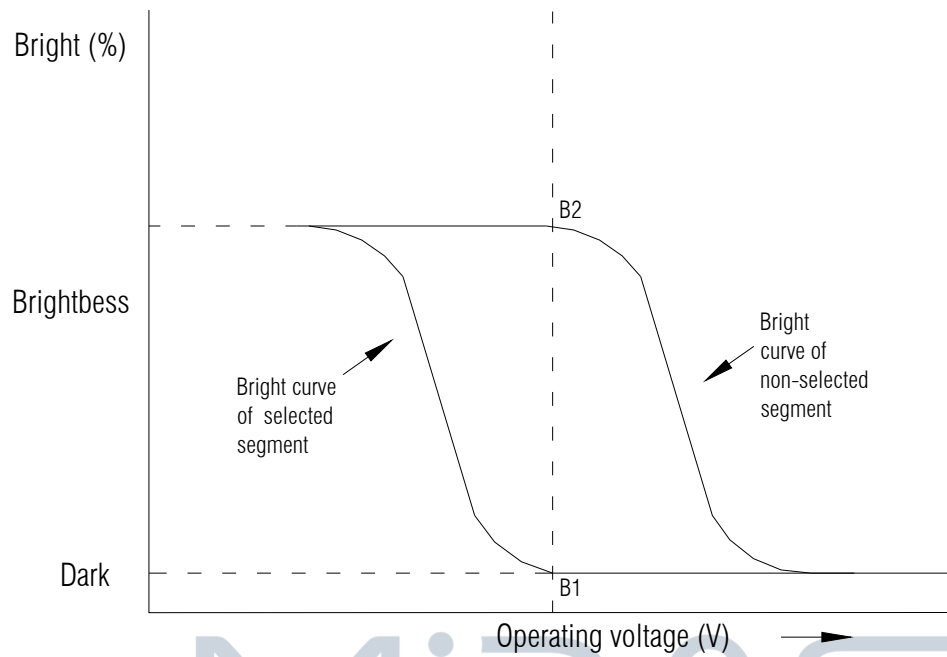
ELECTRO-OPTICAL DEFINITION

Optical Characteristics

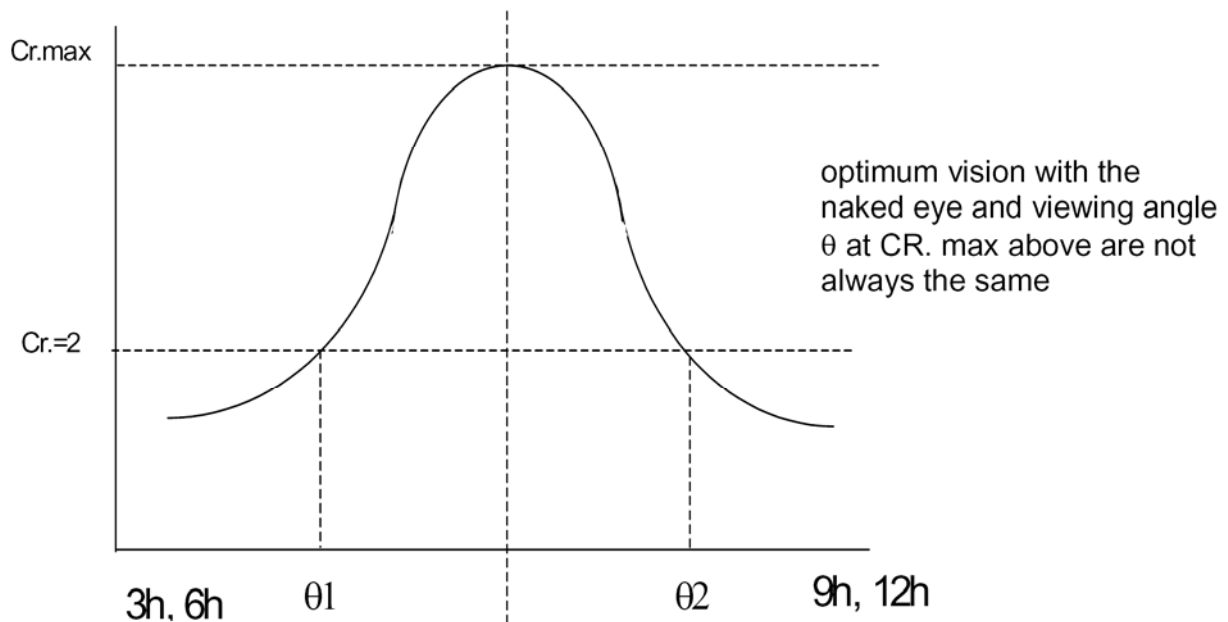
Item	Symbol	Description	Condition	Min	Typ	Max	Unt
Operating Voltage of LCD	V_{LCD}	---	$T_a=+25^{\circ}\text{C}$	---	3.71	---	V
Response time	T_r	Rise	25°C	150	200	---	ms
	T_f	Fall	25°C	150	200	---	ms
Contrast	Cr		25°C	---	3	---	
Viewing angle	θ	12 o'clock axis	$Cr \geq 2.0$	30	35	---	deg
		6 o'clock axis		35	40	---	deg
		3 o'clock axis		30	35	---	deg
		9 o'clock axis		30	35	---	deg



Definition of contrast $Cr. = \frac{B_2}{B_1} = \frac{\text{Bright curve of not selected segment}}{\text{Bright curve of selected segment}}$



Definition of viewing angle θ_1 and θ_2



THE MODULE ACCEPT QUALITY LEVEL (AQL)

1. AQL standard value: Critical defect =0.1, Major defect=0.65; Minor defect =2.5.

RELIABILITY TEST

Operating life time: 50,000 hours

(at room temperature without direct irradiation of sunlight)

Reliability characteristics shall meet following requirements.

Tests Item	Condition
High temperature storage	+80℃ x 96HRS (Without Polarizer)
Low temperature storage	-30℃ x 4HRS
High temperature operation	+70℃ x 96HRS
Low temperature operation	-20℃ x 4HRS
High temperature, High humidity	+60℃ x 95%RH x 96HRS (Without Polarizer)
Thermal shock	-20℃ x 30min → 25℃ x 10s → +70℃ x 30 min x 5 cycles
Vibration test	Frequency x Swing x Time 40Hz x 4mm x 4hrs
Drop test	Drop height*Times 1.0m * 6times

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