

# Switch-mode Power Rectifier

## DPAK Surface Mount Package

# MURD620CT, NRVUD620CT, SRVUD620CT, SNRVUD620CT

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

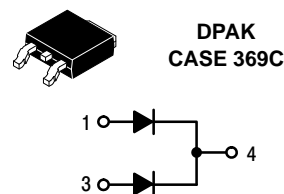
### Features

- Ultrafast 35 Nanosecond Recovery Time
- Low Forward Voltage Drop
- Low Leakage
- ESD Rating:
  - ◆ Human Body Model = 3B (> 8 kV)
  - ◆ Machine Model = C (> 400 V)
- NRVUD, SRVUD and SNRVUD Prefixes for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

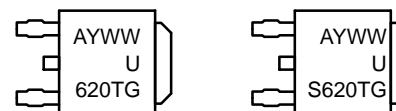
### Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260 °C Max. for 10 Seconds

## ULTRAFAST RECTIFIER 6.0 AMPERES 200 VOLTS



### MARKING DIAGRAMS



A = Assembly Location\*  
 Y = Year  
 WW = Work Week  
 U620T = Device Code (MURD/NRVUD/SNRVUD620CT)  
 US620T = Device Code (SRVUD620CT)  
 G = Pb-Free Package

\* The Assembly Location Code (A) is front side optional. In cases where the Assembly Location is stamped in the package bottom (molding ejector pin), the front side assembly code may be blank.

### ORDERING INFORMATION

Device	Package	Shipping†
MURD620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel
SRVUD620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel
SNRVUD620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel

### DISCONTINUED (Note 1)

MURD620CTG	DPAK (Pb-Free)	75 Units / Rail
NRVUD620CTG	DPAK (Pb-Free)	75 Units / Rail
NRVUD620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel
NRVUD620CTG-VF01	DPAK (Pb-Free)	2,500 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

1. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on [www.onsemi.com](http://www.onsemi.com).

# MURD620CT, NRVUD620CT, SRVUD620CT, SNRVUD620CT

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	200	V
Average Rectified Forward Current ( $T_C = 140\text{ }^\circ\text{C}$ ) Per Diode Per Device	$I_{F(AV)}$	3.0 6.0	A
Peak Repetitive Forward Current (Square Wave, Duty = 0.5, $T_C = 145\text{ }^\circ\text{C}$ ) Per Diode	$I_F$	6.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, 60 Hz)	$I_{FSM}$	50	A
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-65 to +175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS (Per Diode)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	9	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	80	$^\circ\text{C/W}$

1. Rating applies when surface mounted on the minimum pad sizes recommended.

## ELECTRICAL CHARACTERISTICS (Per Diode)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage Drop (Note 2) ( $i_F = 3\text{ Amps}$ , $T_C = 25\text{ }^\circ\text{C}$ ) ( $i_F = 3\text{ Amps}$ , $T_C = 125\text{ }^\circ\text{C}$ ) ( $i_F = 6\text{ Amps}$ , $T_C = 25\text{ }^\circ\text{C}$ ) ( $i_F = 6\text{ Amps}$ , $T_C = 125\text{ }^\circ\text{C}$ )	$V_F$	1 0.96 1.2 1.13	V
Maximum Instantaneous Reverse Current (Note 2) ( $T_J = 25\text{ }^\circ\text{C}$ , Rated dc Voltage) ( $T_J = 125\text{ }^\circ\text{C}$ , Rated dc Voltage)	$i_R$	5 250	$\mu\text{A}$
Maximum Reverse Recovery Time ( $I_F = 1\text{ Amp}$ , $di/dt = 50\text{ Amps}/\mu\text{s}$ , $V_R = 30\text{ V}$ , $T_J = 25\text{ }^\circ\text{C}$ ) ( $I_F = 0.5\text{ Amp}$ , $i_R = 1\text{ Amp}$ , $I_{REC} = 0.25\text{ A}$ , $V_R = 30\text{ V}$ , $T_J = 25\text{ }^\circ\text{C}$ )	$t_{rr}$	35 25	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

TYPICAL CHARACTERISTICS

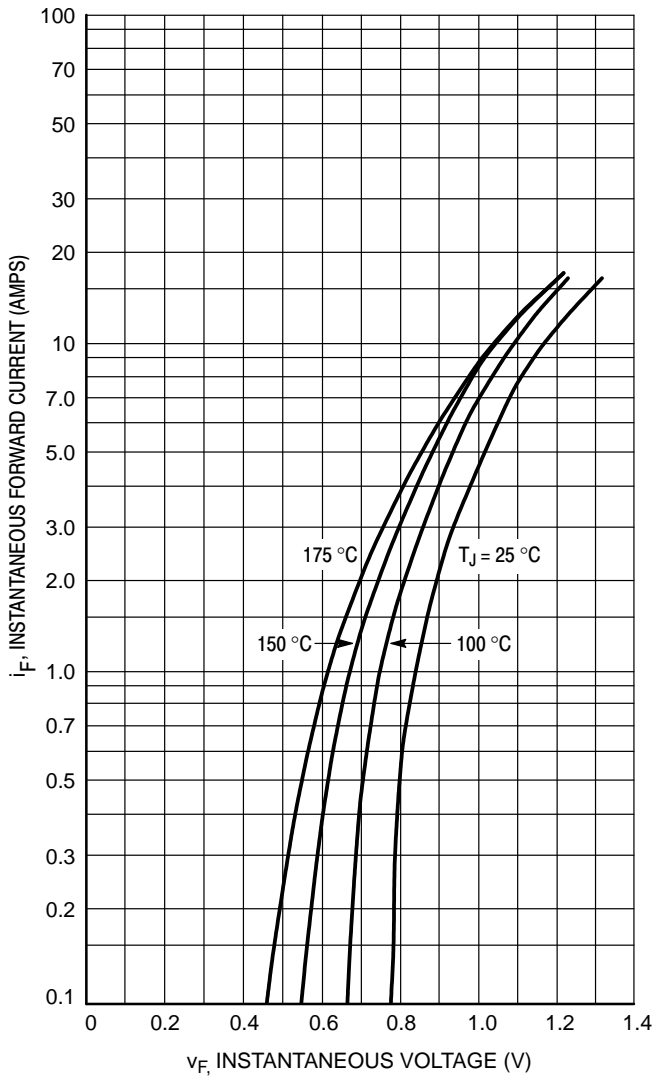


Figure 1. Typical Forward Voltage (Per Leg)

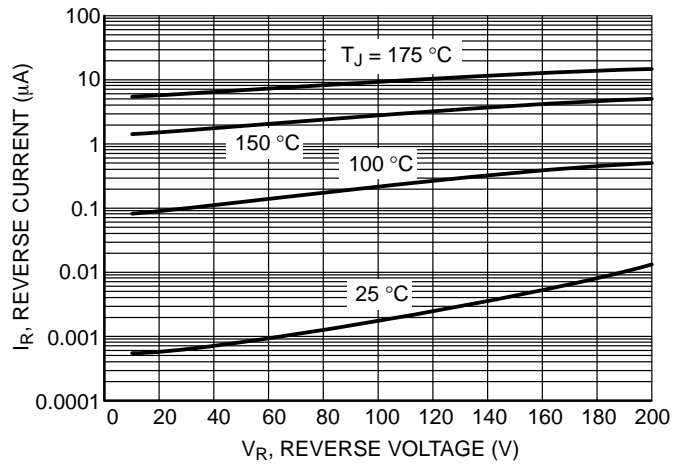


Figure 2. Typical Leakage Current\* (Per Leg)

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these curves if  $V_R$  is sufficiently below rated  $V_R$ .

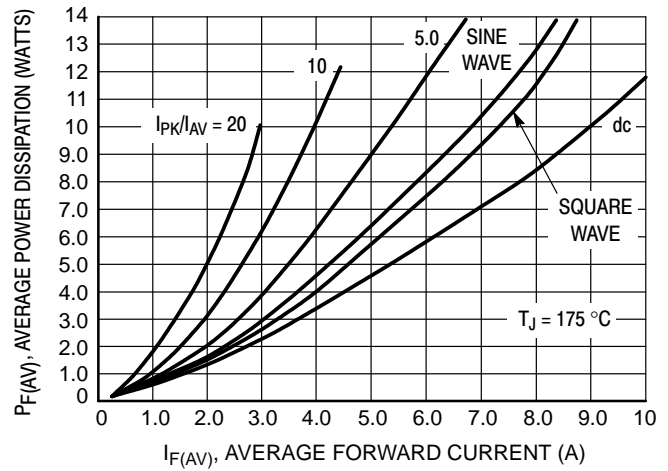


Figure 3. Average Power Dissipation (Per Leg)

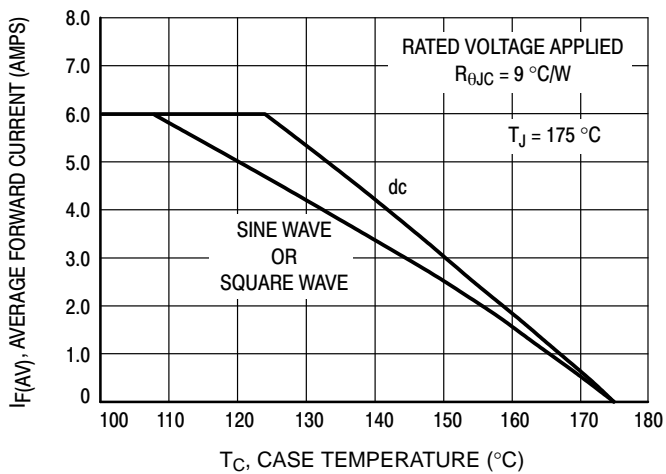


Figure 4. Current Derating, Case (Per Leg)

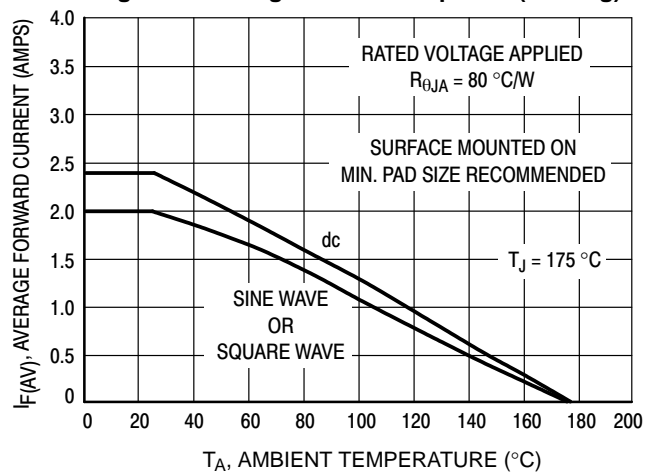


Figure 5. Current Derating, Ambient (Per Leg)

TYPICAL CHARACTERISTICS

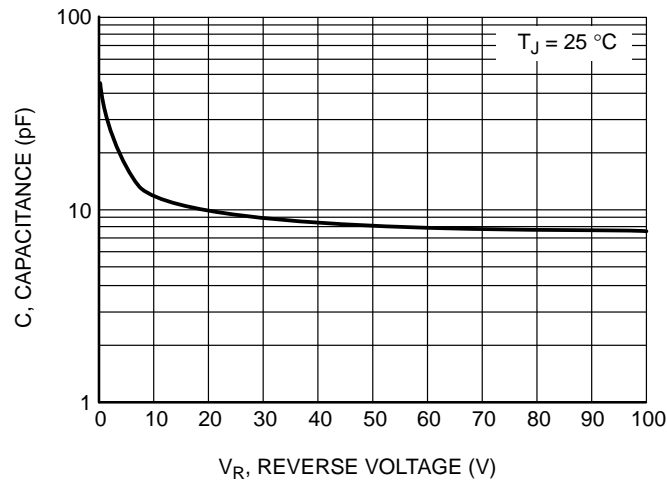


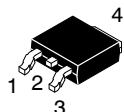
Figure 6. Typical Capacitance (Per Leg)

# MURD620CT, NRVUD620CT, SRVUD620CT, SNRVUD620CT

## REVISION HISTORY

Revision	Description of Changes	Date
16	Rebranded the Data Sheet to <b>onsemi</b> format. MURD620CTG, NRVUD620CTG, NRVUD620CTT4G, NRVUD620CTG-VF01 OPN marked as Discontinued.	10/15/2025

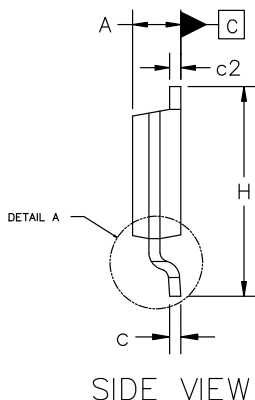
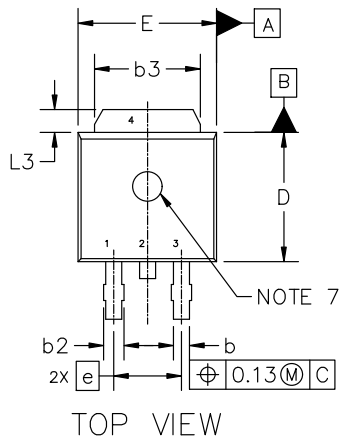
This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.



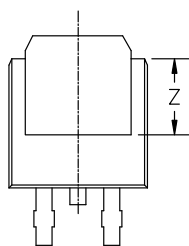
DPAK-3 6.10x6.54x2.28, 2.29P  
CASE 369C  
ISSUE K

DATE 14 MAY 2026

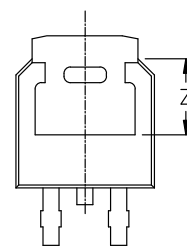
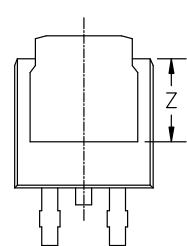
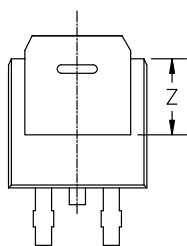
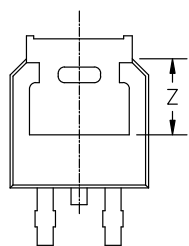
SCALE 1:1



MILLIMETERS			
DIM	MIN	NOM	MAX
A	2.18	2.28	2.38
A1	0.00	---	0.13
b	0.63	0.76	0.89
b2	0.72	0.93	1.14
b3	4.57	5.02	5.46
c	0.46	0.54	0.61
c2	0.46	0.54	0.61
D	5.97	6.10	6.22
E	6.35	6.54	6.73
e	2.29 BSC		
H	9.40	9.91	10.41
L	1.40	1.59	1.78
L1	2.90 REF		
L2	0.51 BSC		
L3	0.89	---	1.27
L4	---	---	1.01
Z	3.93	---	---



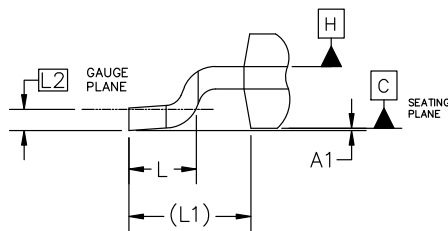
BOTTOM VIEW



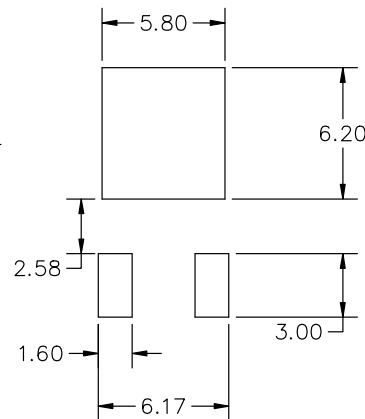
ALTERNATE CONSTRUCTIONS

NOTES:

1. DIMENSIONING AND TOLERANCING ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3, AND Z.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS.
5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.
7. OPTIONAL MOLD FEATURE.



DETAIL A  
ROTATED 90° CW



RECOMMENDED MOUNTING FOOTPRINT\*

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

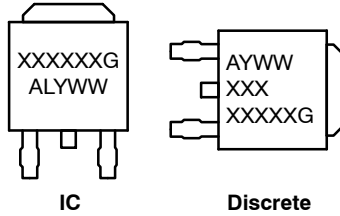
DOCUMENT NUMBER:	98AON10527D	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	DPAK-3 6.10x6.54x2.28, 2.29P	PAGE 1 OF 2

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**DPAK-3 6.10x6.54x2.28, 2.29P**  
**CASE 369C**  
**ISSUE K**

DATE 13 MAY 2026

**GENERIC  
MARKING DIAGRAM\***



- XXXXXX = Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

- |  |  |   |   |  |
|--|--|---|---|--|
| <p>STYLE 1:<br/> PIN 1. BASE<br/> 2. COLLECTOR<br/> 3. EMITTER<br/> 4. COLLECTOR</p> | <p>STYLE 2:<br/> PIN 1. GATE<br/> 2. DRAIN<br/> 3. SOURCE<br/> 4. DRAIN</p>          | <p>STYLE 3:<br/> PIN 1. ANODE<br/> 2. CATHODE<br/> 3. ANODE<br/> 4. CATHODE</p> | <p>STYLE 4:<br/> PIN 1. CATHODE<br/> 2. ANODE<br/> 3. GATE<br/> 4. ANODE</p>              | <p>STYLE 5:<br/> PIN 1. GATE<br/> 2. ANODE<br/> 3. CATHODE<br/> 4. ANODE</p>     |
| <p>STYLE 6:<br/> PIN 1. MT1<br/> 2. MT2<br/> 3. GATE<br/> 4. MT2</p>                 | <p>STYLE 7:<br/> PIN 1. GATE<br/> 2. COLLECTOR<br/> 3. EMITTER<br/> 4. COLLECTOR</p> | <p>STYLE 8:<br/> PIN 1. N/C<br/> 2. CATHODE<br/> 3. ANODE<br/> 4. CATHODE</p>   | <p>STYLE 9:<br/> PIN 1. ANODE<br/> 2. CATHODE<br/> 3. RESISTOR ADJUST<br/> 4. CATHODE</p> | <p>STYLE 10:<br/> PIN 1. CATHODE<br/> 2. ANODE<br/> 3. CATHODE<br/> 4. ANODE</p> |

<b>DOCUMENT NUMBER:</b>	<b>98AON10527D</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>DPAK-3 6.10x6.54x2.28, 2.29P</b>	<b>PAGE 2 OF 2</b>

**onsemi** and **Onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)

