

Product Summary

BV_{DS}	$R_{DS(ON)}$ max	I_D max $T_A = +25^\circ\text{C}$
60V	$2\Omega @ V_{GS} = 4.5\text{V}$	380mA
	$2.5\Omega @ V_{GS} = 2.5\text{V}$	340mA

Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

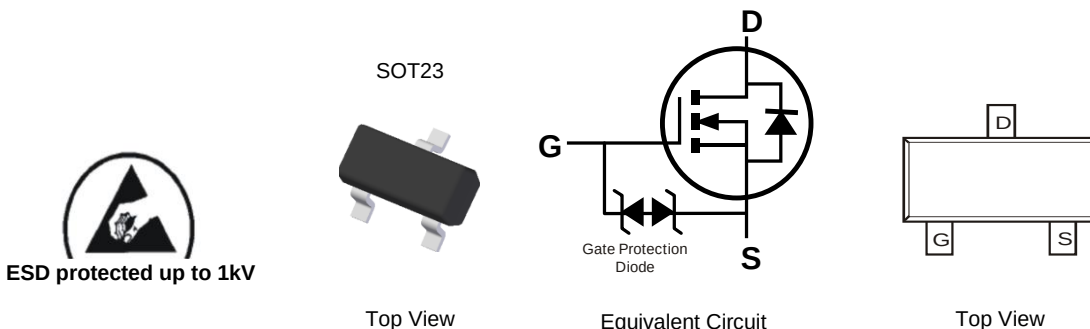
- Motor Control
- Power Management Functions
- Backlighting

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 1kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 Ⓔ3
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN62D0U-7	SOT23	3000/Tape & Reel
DMN62D0U-13	SOT23	10000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



3D2 = Product Type Marking Code
 YM or YM = Date Code Marking
 Y or Y = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	D	E	F	G	H	I	J	K	L	M	N	O
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	T _A = +25°C	I _D	380	mA
		T _A = +70°C	I _D	300	mA
	t < 5s	T _A = +25°C T _A = +70°C	I _D	430 340	mA
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	0.4	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)			I _{DM}	1.2	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P _D	380	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R _{θJA}	338	°C/W
	t < 5s		R _{θJA}	292	°C/W
Total Power Dissipation (Note 6)			P _D	590	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R _{θJA}	216	°C/W
	t < 5s		R _{θJA}	177	°C/W
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 10µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0	µA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	V _{DS} = 10V, I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.2	2.0	Ω	V _{GS} = 4.5V, I _D = 0.1A
			1.4	2.5		V _{GS} = 2.5V, I _D = 0.05A
			1.8	3.0		V _{GS} = 1.8V, I _D = 0.05A
Forward Transconductance	Y _{fs}	—	1.8	—	S	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	—	0.8	1.3	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	32	—	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	3.9	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.4	—	pF	
Gate Resistance	R _g	—	101	—	Ω	f = 1MHz, V _{GS} = 0V, V _{DS} = 0V
Total Gate Charge	Q _g	—	0.5	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	0.09	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.09	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	2.4	—	ns	V _{DD} = 30V, V _{GS} = 10V, R _G = 25Ω, I _D = 200mA
Turn-On Rise Time	t _R	—	2.5	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	22.6	—	ns	
Turn-Off Fall Time	t _F	—	12.5	—	ns	

- Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

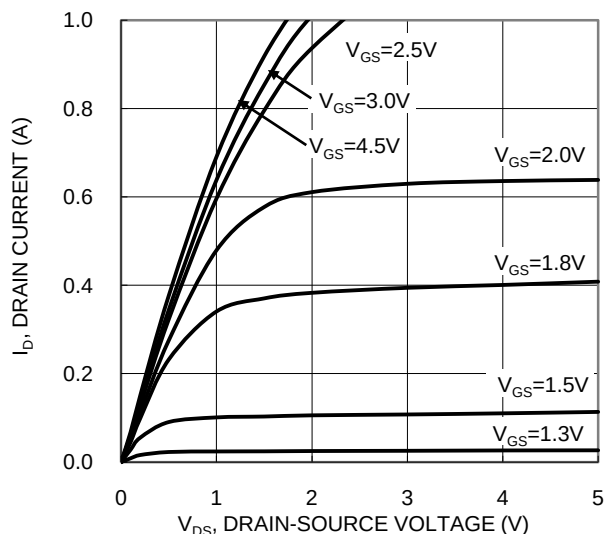


Figure 1. Typical Output Characteristic

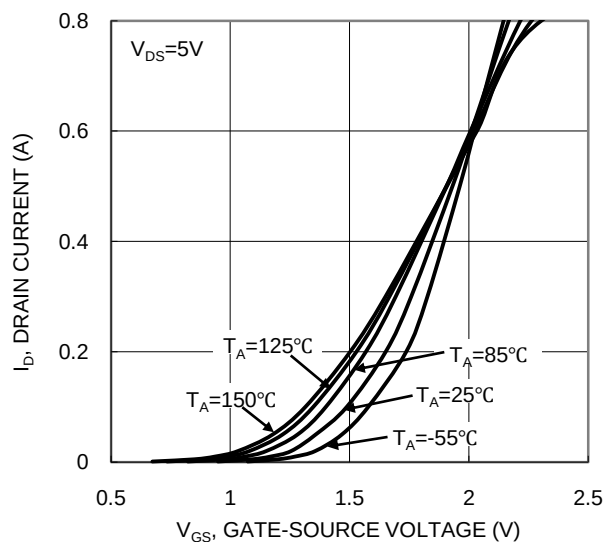


Figure 2. Typical Transfer Characteristic

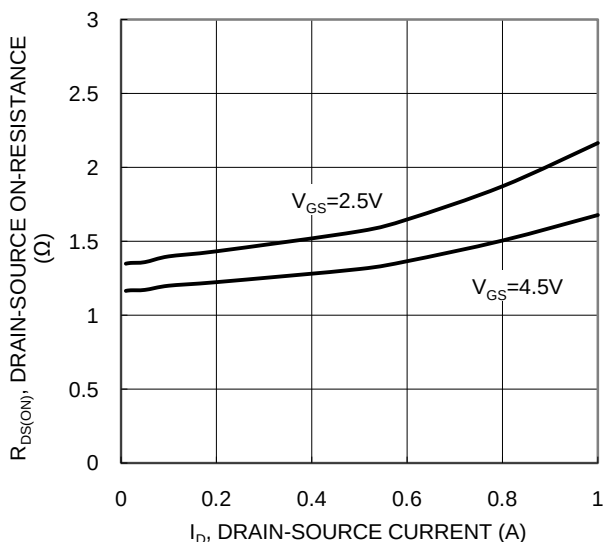


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

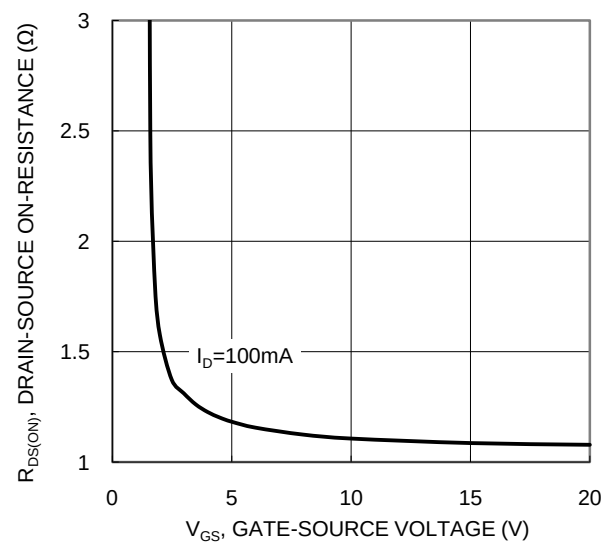


Figure 4. Typical Transfer Characteristic

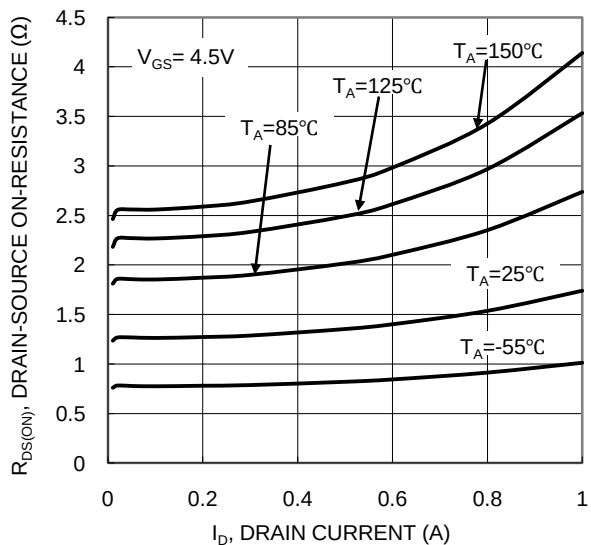


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

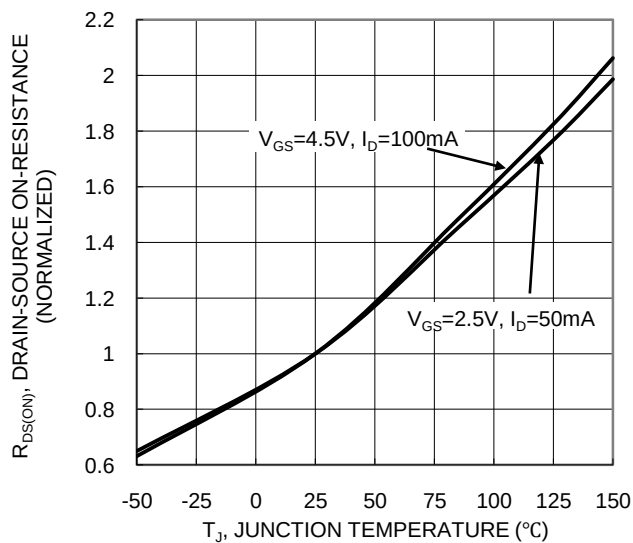
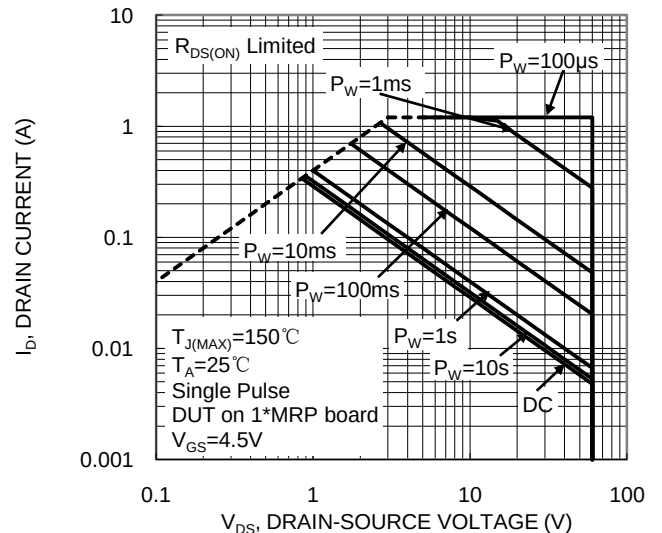
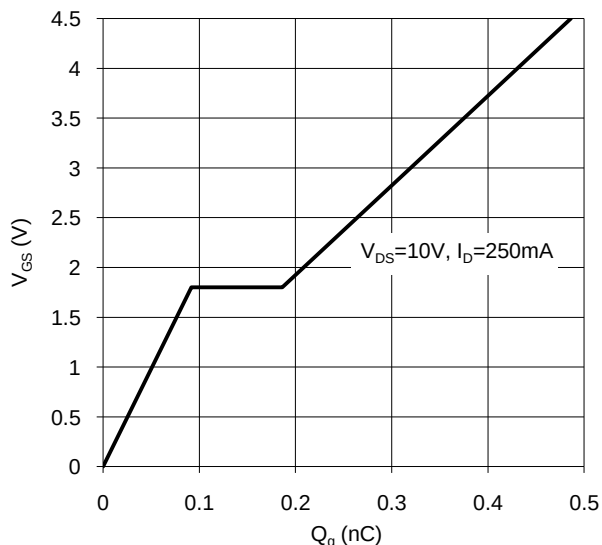
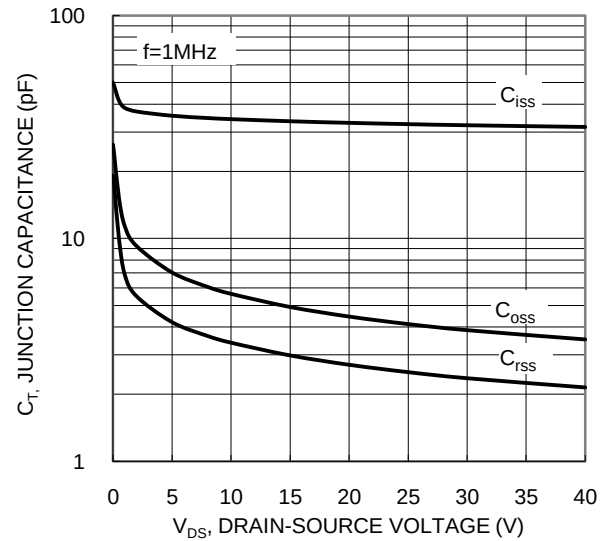
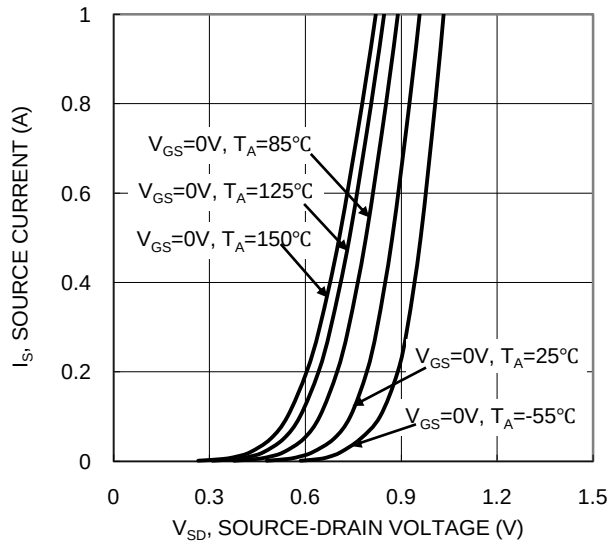
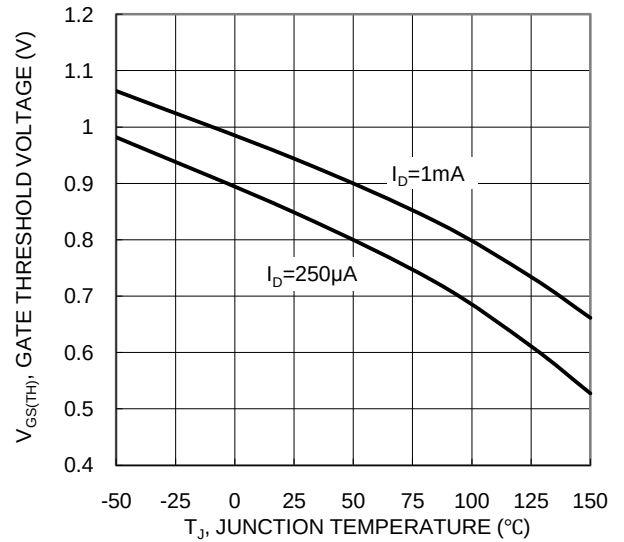
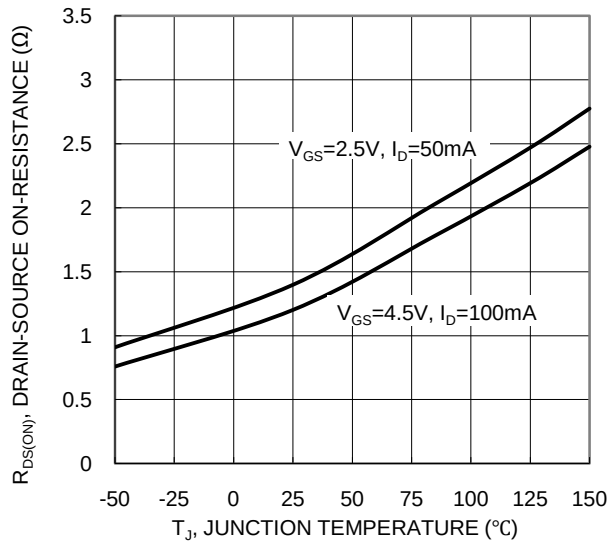


Figure 6. On-Resistance Variation with Junction Temperature



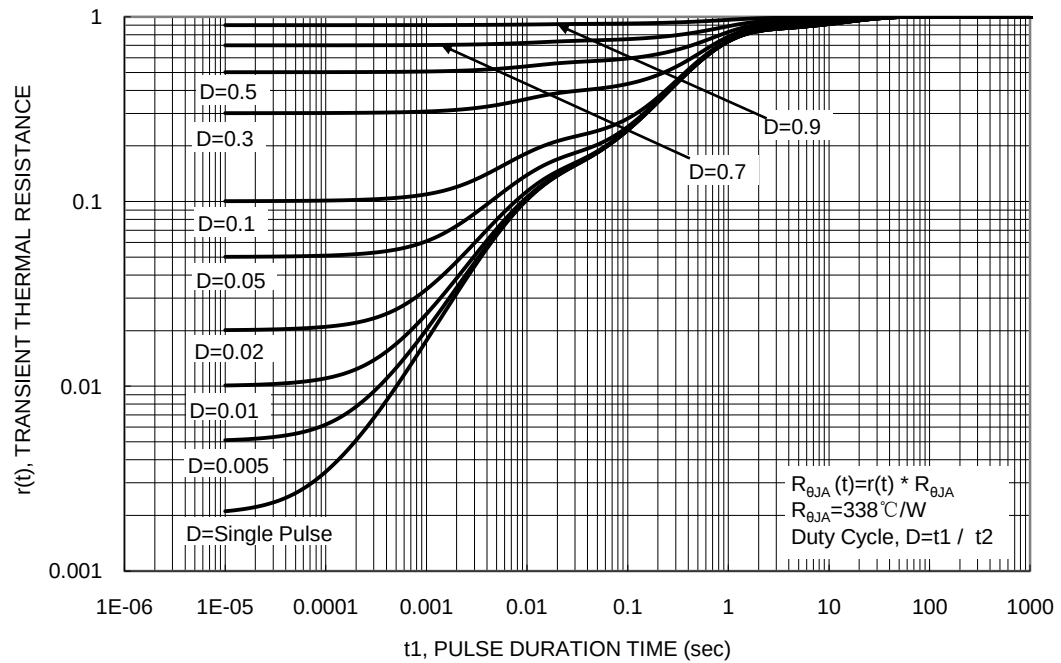
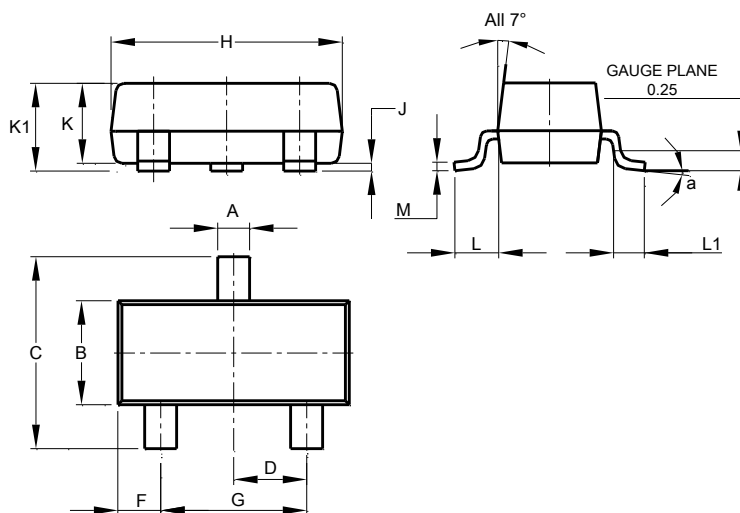


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

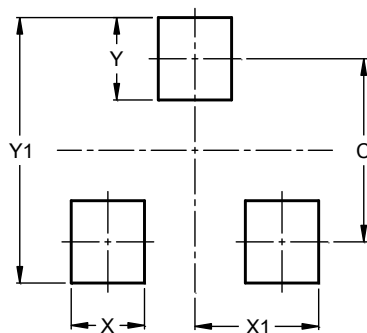


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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