

DATA SHEET

PEMB11; PUMB11

**PNP/PNP resistor-equipped
transistors; R1 = 10 k Ω , R2 = 10 k Ω**

Product specification
Supersedes data of 2001 Sep 13

2003 Oct 03

PNP/PNP resistor-equipped transistors; R1 = 10 k Ω , R2 = 10 k Ω

PEMB11; PUMB11

FEATURES

- Built-in bias resistors
- Simplified circuit design
- Reduction of component count
- Reduced pick and place costs.

APPLICATIONS

- Low current peripheral drivers
- Replacement of general purpose transistors in digital applications
- Control of IC inputs.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V _{CEO}	collector-emitter voltage	–	–50	V
I _O	output current (DC)	–	–100	mA
TR1	PNP	–	–	–
TR2	PNP	–	–	–
R1	bias resistor	10	–	k Ω
R2	bias resistor	10	–	k Ω

DESCRIPTION

PNP/PNP resistor-equipped transistors (see “Simplified outline, symbol and pinning” for package details).

PRODUCT OVERVIEW

TYPE NUMBER	PACKAGE		MARKING CODE ⁽¹⁾	NPN/PNP COMPLEMENT	NPN/PNP COMPLEMENT
	PHILIPS	EIAJ			
PEMB11	SOT666	–	B1	PEMD3	PEMH11
PUMB11	SOT363	SC-88	B*1	PUMD3	PUMH11

Note

1. * = p: Made in Hong Kong.
* = t: Made in Malaysia.
* = W: Made in China.

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PINNING	
		PIN	DESCRIPTION
PEMB11 PUMB11	Top view MAM477	1 2 3 4 5 6	emitter TR1 base TR1 collector TR2 emitter TR2 base TR2 collector TR1

PNP/PNP resistor-equipped transistors;
R1 = 10 k Ω , R2 = 10 k Ω

PEMB11; PUMB11

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PEMB11	–	plastic surface mounted package; 6 leads	SOT666
PUMB11	–	plastic surface mounted package; 6 leads	SOT363

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor					
V _{CBO}	collector-base voltage	open emitter	–	–50	V
V _{CEO}	collector-emitter voltage	open base	–	–50	V
V _{EBO}	emitter-base voltage	open collector	–	–10	V
V _I	input voltage				
	positive		–	+10	V
	negative		–	–40	V
I _O	output current (DC)		–	–100	mA
I _{CM}	peak collector current		–	–100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	SOT363	note 1	–	200	mW
	SOT666	notes 1 and 2	–	200	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C
Per device					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	SOT363	note 1	–	300	mW
	SOT666	notes 1 and 2	–	300	mW

Notes

- Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
- Reflow soldering is the only recommended soldering method.

PNP/PNP resistor-equipped transistors;
R1 = 10 k Ω , R2 = 10 k Ω

PEMB11; PUMB11

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per transistor				
R _{th j-a}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C		
	SOT363	note 1	625	K/W
	SOT666	notes 1 and 2	625	K/W
Per device				
R _{th j-a}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C		
	SOT363	note 1	416	K/W
	SOT666	note 1	416	K/W

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
2. Reflow soldering is the only recommended soldering method.

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0	–	–	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -30 V; I _B = 0	–	–	-1	μA
		V _{CE} = -30 V; I _B = 0; T _J = 150 °C	–	–	-50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0	–	–	-400	μA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -5 mA	30	–	–	
V _{CEsat}	saturation voltage	I _C = -10 mA; I _B = -0.5 mA	–	–	-150	mV
V _{i(off)}	input-off voltage	V _{CE} = -5 V; I _C = -100 μA	–	-1.1	-0.8	V
V _{i(on)}	input-on voltage	V _{CE} = -0.3 V; I _C = -10 mA	-2.5	-1.8	–	V
R1	input resistor		7	10	13	k Ω
$\frac{R2}{R1}$	resistor ratio		0.8	1	1.2	
C _c	collector capacitance	I _E = I _e = 0; V _{CB} = -10 V; f = 1 MHz	–	–	3	pF

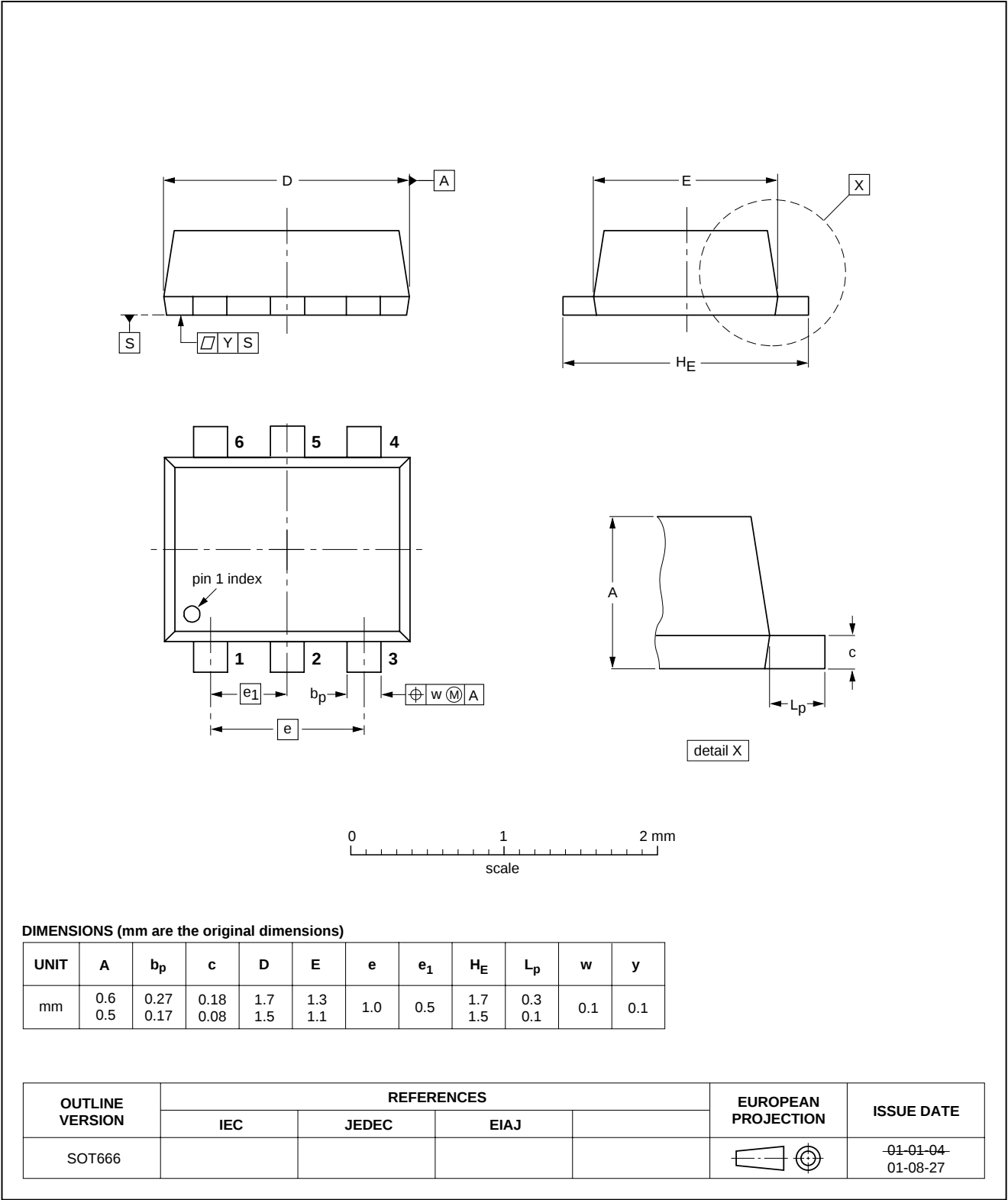
PNP/PNP resistor-equipped transistors;
R1 = 10 kΩ, R2 = 10 kΩ

PEMB11; PUMB11

PACKAGE OUTLINES

Plastic surface mounted package; 6 leads

SOT666

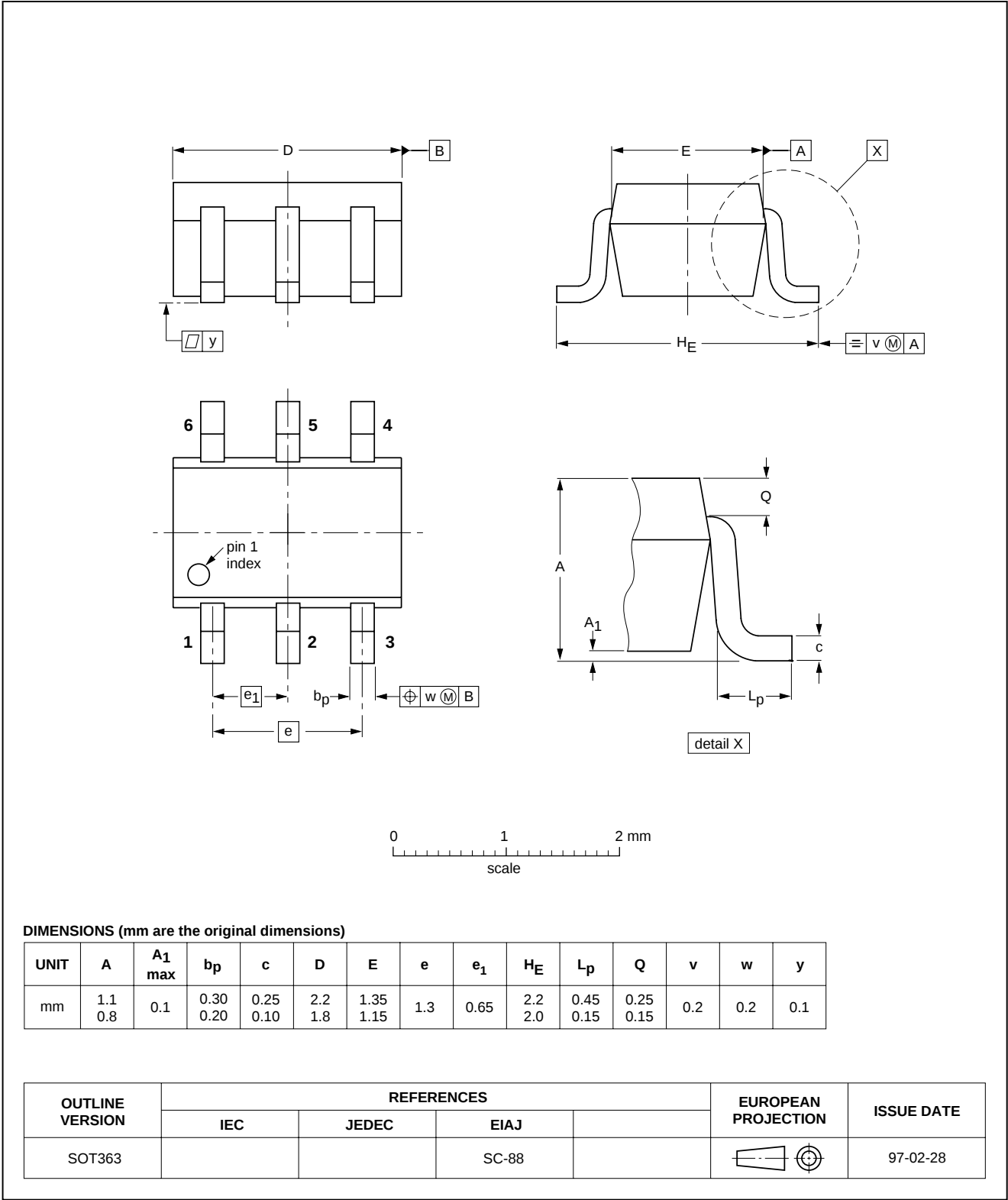


PNP/PNP resistor-equipped transistors;
R1 = 10 kΩ, R2 = 10 kΩ

PEMB11; PUMB11

Plastic surface mounted package; 6 leads

SOT363



PNP/PNP resistor-equipped transistors; R1 = 10 kΩ, R2 = 10 kΩ

PEMB11; PUMB11

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

Notes

1. Please consult the most recently issued data sheet before initiating or completing a design.
2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.
3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

DISCLAIMERS

Life support applications — These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

Right to make changes — Philips Semiconductors reserves the right to make changes in the products - including circuits, standard cells, and/or software - described or contained herein in order to improve design and/or performance. When the product is in full production (status 'Production'), relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN). Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no licence or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

Philips Semiconductors – a worldwide company

Contact information

For additional information please visit **<http://www.semiconductors.philips.com>**. Fax: **+31 40 27 24825**

For sales offices addresses send e-mail to: **sales.addresses@www.semiconductors.philips.com**.

© Koninklijke Philips Electronics N.V. 2003

SCA75

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands

R75/02/pp8

Date of release: 2003 Oct 03

Document order number: 9397 750 11805

Let's make things better.

**Philips
Semiconductors**



PHILIPS