



PBSS4140DPN

40 V low V_{CEsat} NPN/PNP transistor

9 November 2023

Product data sheet

1. General description

NPN/PNP low V_{CEsat} transistor pair in an SC-74 (SOT457) plastic package.

2. Features and benefits

- 600 mW total power dissipation
- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation
- Replaces two SOT23 packaged low V_{CEsat} transistors on same PCB area
- Reduces required PCB area
- Reduced pick and place costs
- AEC-Q101 qualified

3. Applications

- General purpose switching and muting
- LCD backlighting
- Supply line switching circuits
- Battery driven equipment (mobile phones, video cameras and hand-held devices)

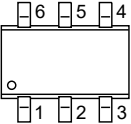
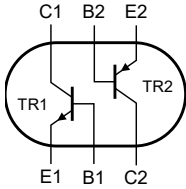
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor unless otherwise specified; for the PNP transistor with negative polarity						
V_{CEO}	collector-emitter voltage	open base	-	-	40	V
I_C	collector current		-	-	1	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	2	A
TR1 (NPN)						
R_{CEsat}	collector-emitter saturation resistance	$I_C = 500$ mA; $I_B = 50$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	260	500	m Ω
TR2 (PNP)						
R_{CEsat}	collector-emitter saturation resistance	$I_C = -500$ mA; $I_B = -50$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	300	500	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	 TSOP6 (SOT457)	 sym139
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4140DPN	TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4140DPN	M2

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor unless otherwise specified; for the PNP transistor with negative polarity						
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	A
I _{BM}	peak base current			-	1	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	370	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	600	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor unless otherwise specified; for the PNP transistor with negative polarity							
I _{CBO}	collector-base cut-off current	V _{CB} = 40 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
		V _{CB} = 40 V; I _E = 0 A; T _j = 150 °C		-	-	50	μA
I _{CEO}	collector-emitter cut-off current (base open)	I _B = 0 A; V _{CE} = 30 V		-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 1 mA; T _{amb} = 25 °C		-	-	200	mV
		I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C		-	-	250	mV
		I _C = 1 A; I _B = 100 mA; T _{amb} = 25 °C		-	-	500	mV
TR1 (NPN)							
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 1 mA; T _{amb} = 25 °C		300	-	-	
		V _{CE} = 5 V; I _C = 500 mA; T _{amb} = 25 °C		300	-	900	
		V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C		200	-	-	
R _{CEsat}	collector-emitter saturation resistance	I _C = 500 mA; I _B = 50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		-	260	500	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 1 A; I _B = 100 mA; T _{amb} = 25 °C		-	-	1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C		-	-	1.1	V
f _T	transition frequency	V _{CE} = 10 V; I _C = 50 mA; f = 100 MHz; T _{amb} = 25 °C		150	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	10	pF
TR2 (PNP)							
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -1 mA; T _{amb} = 25 °C		300	-	-	
		V _{CE} = -5 V; I _C = -100 mA; T _{amb} = 25 °C		300	-	800	
		V _{CE} = -5 V; I _C = -500 mA; T _{amb} = 25 °C		250	-	-	
		V _{CE} = -5 V; I _C = -1 A; T _{amb} = 25 °C		160	-	-	
R _{CEsat}	collector-emitter saturation resistance	I _C = -500 mA; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		-	300	500	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; T _{amb} = 25 °C		-	-	-1.1	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -5 V; I _C = -1 A; T _{amb} = 25 °C		-	-	-1	V

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
f_T	transition frequency	$V_{CE} = -10\text{ V}$; $I_C = -50\text{ mA}$; $f = 100\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	150	-	-	MHz
C_c	collector capacitance	$V_{CB} = -10\text{ V}$; $I_E = 0\text{ A}$; $i_e = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	12	pF

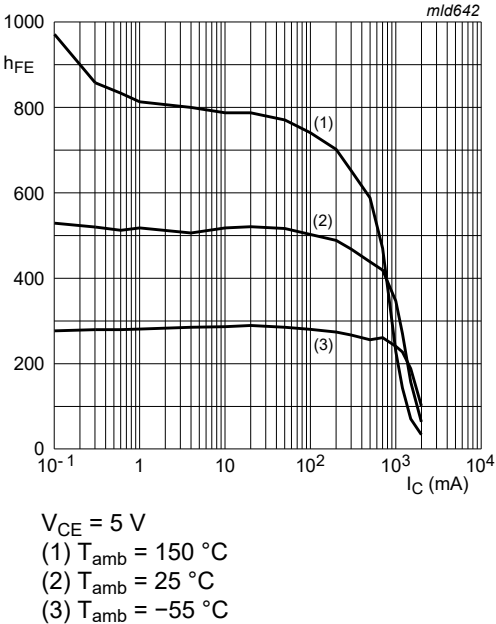


Fig. 1. TR1 (NPN): DC current gain as a function of collector current; typical values

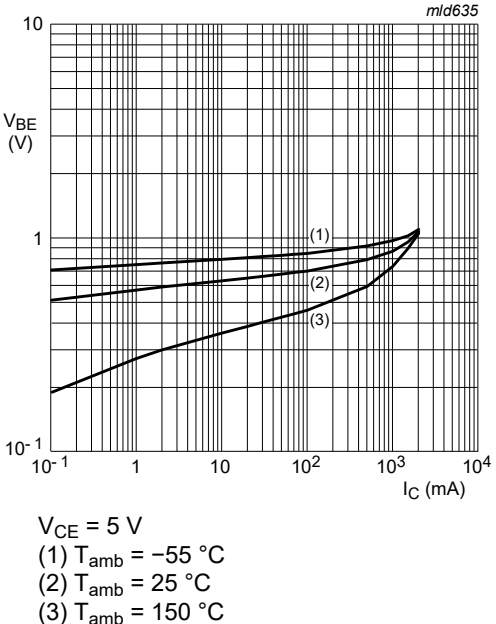


Fig. 2. TR1 (NPN): Base-emitter voltage as a function of collector current; typical values

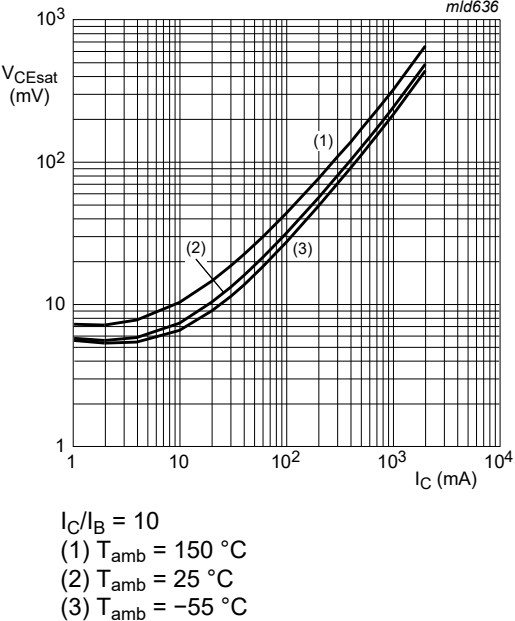


Fig. 3. TR1 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values

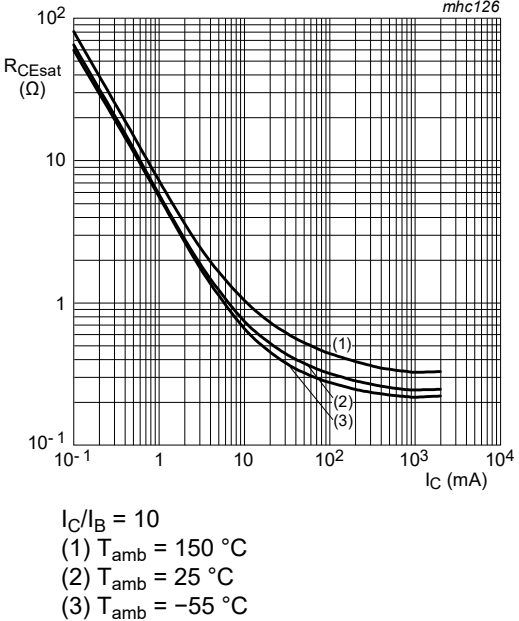


Fig. 4. TR1 (NPN): Equivalent on-resistance as a function of collector current; typical values

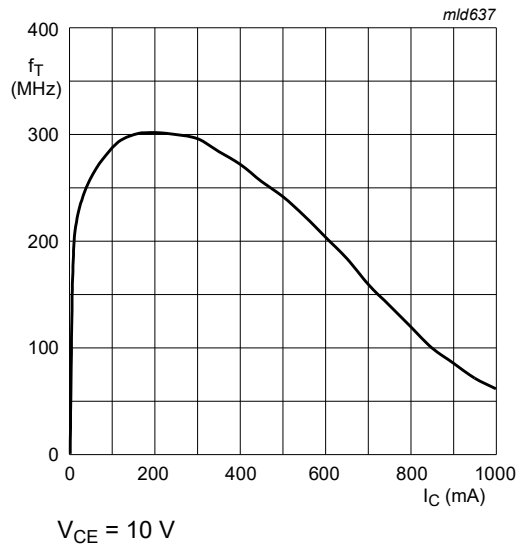


Fig. 5. TR1 (NPN): Transition frequency as a function of collector current; typical values

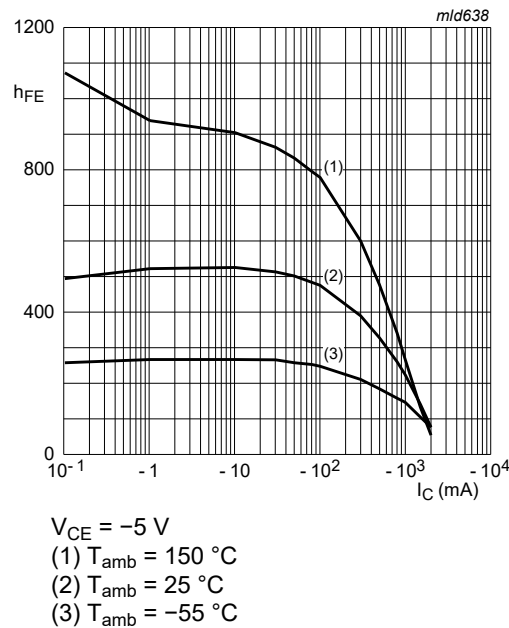


Fig. 6. TR2 (PNP): DC current gain as a function of collector current; typical values

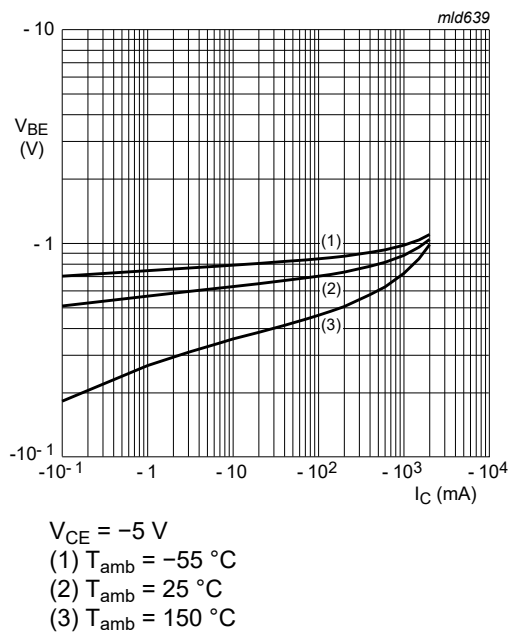


Fig. 7. TR2 (PNP): Base-emitter voltage as a function of collector current; typical values

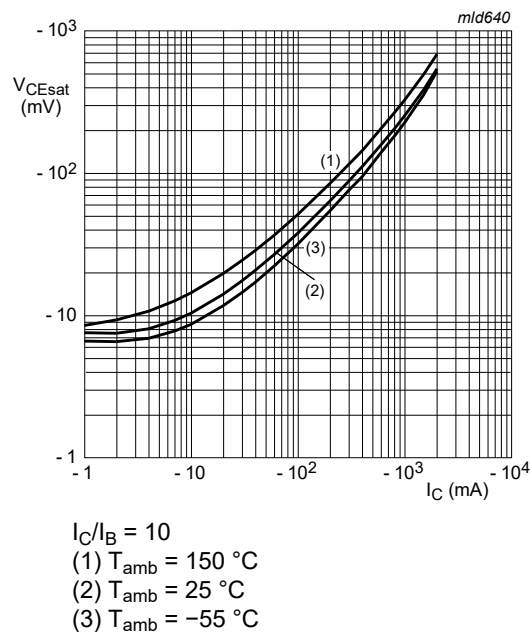


Fig. 8. TR2 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values

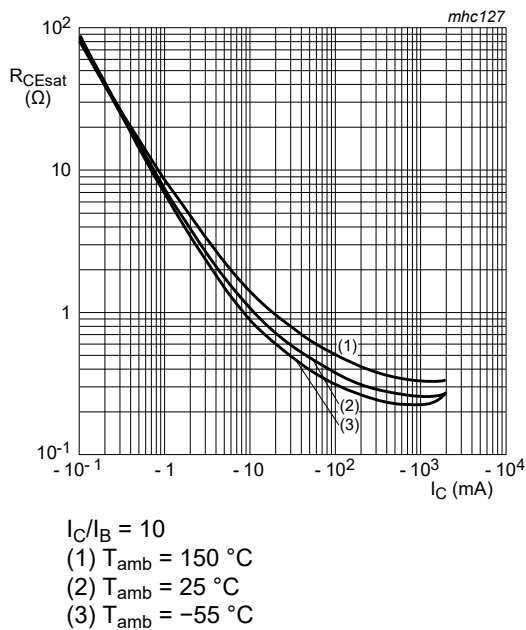


Fig. 9. TR2 (PNP): Equivalent on-resistance as a function of collector current; typical values

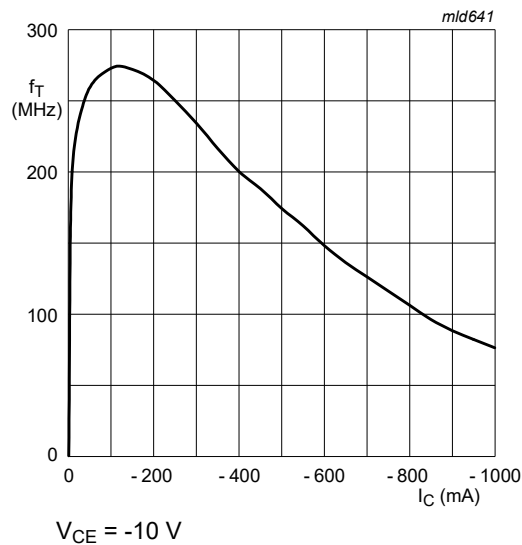


Fig. 10. TR2 (PNP): Transition frequency as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

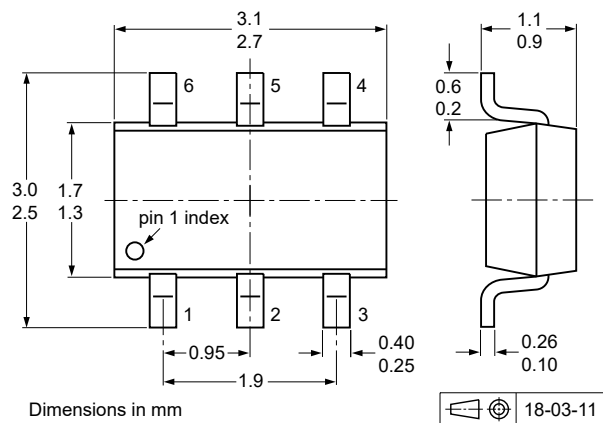
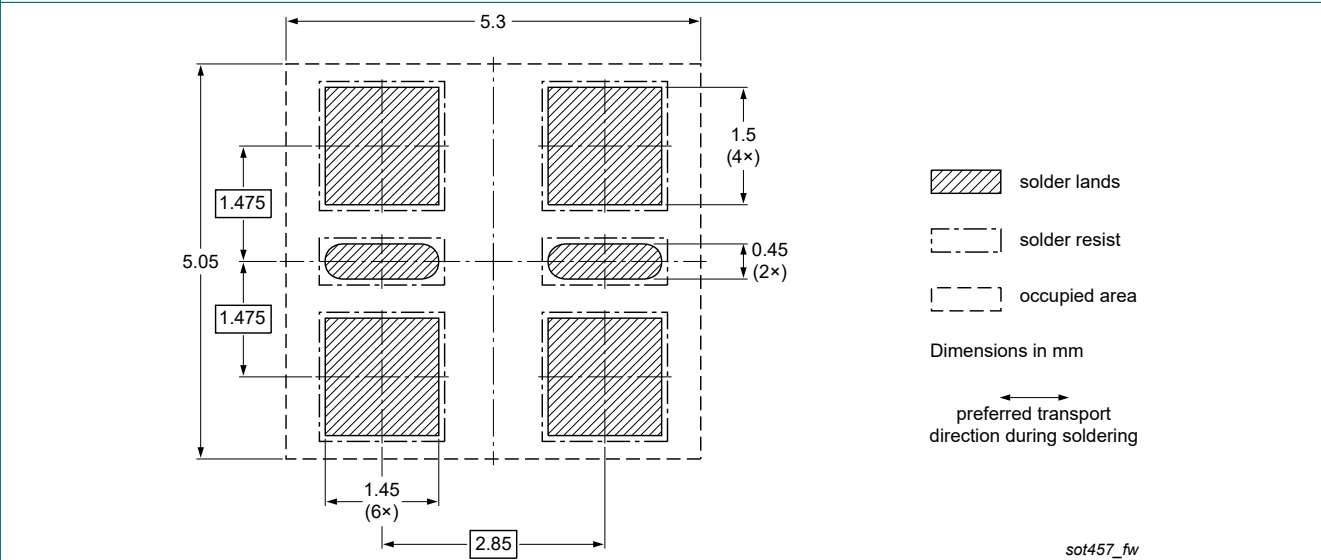
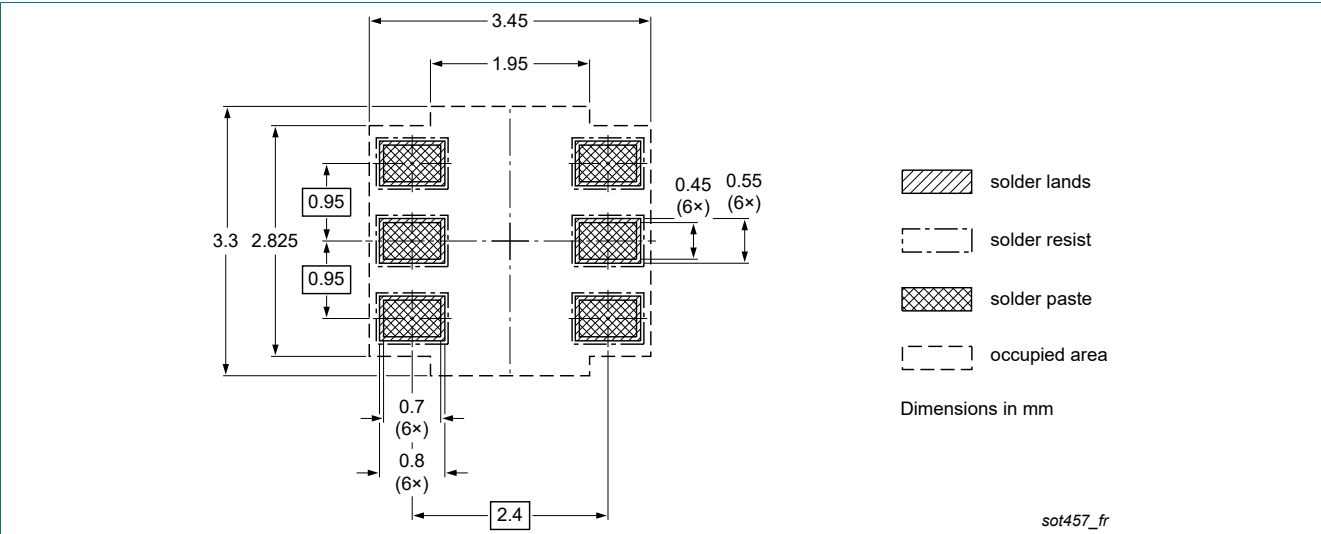


Fig. 11. Package outline TSOP6 (SOT457)

13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4140DPN v.2	20231109	Product data sheet	-	PBSS4140DPN v.1
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. - Legal texts have been adapted to the new company name where appropriate.			
PBSS4140DPN v.1	20011213	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 2

9. Thermal characteristics..... 3

10. Characteristics..... 3

11. Test information..... 6

12. Package outline..... 6

13. Soldering..... 7

14. Revision history.....8

15. Legal information.....9

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