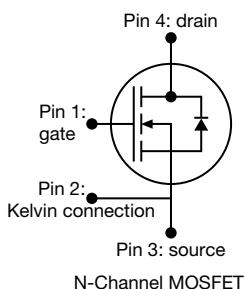
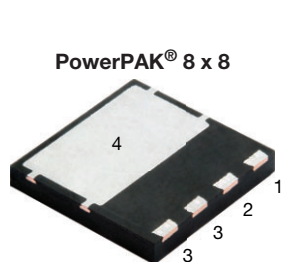


## EF Series Power MOSFET With Fast Body Diode



### FEATURES

- 4<sup>th</sup> generation E series technology
- Low figure-of-merit (FOM)  $R_{on} \times Q_g$
- Low effective capacitance ( $C_{o(er)}$ )
- Reduced switching and conduction losses
- Avalanche energy rated (UIS)
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

### APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
  - High-intensity discharge (HID)
  - Fluorescent ballast lighting
- Industrial
  - Welding
  - Induction heating
  - Motor drives
  - Battery chargers
  - Solar (PV inverters)

### PRODUCT SUMMARY

|                                         |                 |       |
|-----------------------------------------|-----------------|-------|
| $V_{DS}$ (V) at $T_J$ max.              | 650             |       |
| $R_{DS(on)}$ typ. ( $\Omega$ ) at 25 °C | $V_{GS} = 10$ V | 0.109 |
| $Q_g$ max. (nC)                         | 47              |       |
| $Q_{gs}$ (nC)                           | 12              |       |
| $Q_{gd}$ (nC)                           | 11              |       |
| Configuration                           | Single          |       |

### ORDERING INFORMATION

|                                 |                    |
|---------------------------------|--------------------|
| Package                         | PowerPAK 8 x 8     |
| Lead (Pb)-free and halogen-free | SiHH125N60EF-T1GE3 |

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

| PARAMETER                                        | SYMBOL           | LIMIT          | UNIT |
|--------------------------------------------------|------------------|----------------|------|
| Drain-source voltage                             | $V_{DS}$         | 600            | V    |
| Gate-source voltage                              | $V_{GS}$         | $\pm 30$       |      |
| Continuous drain current ( $T_J = 150$ °C)       | $V_{GS}$ at 10 V | $T_C = 25$ °C  | A    |
|                                                  |                  | $T_C = 100$ °C |      |
| Pulsed drain current <sup>a</sup>                | $I_{DM}$         | 66             |      |
| Linear derating factor                           |                  | 1.25           | W/°C |
| Single pulse avalanche energy <sup>b</sup>       | $E_{AS}$         | 88             | mJ   |
| Maximum power dissipation                        | $P_D$            | 156            | W    |
| Operating junction and storage temperature range | $T_J, T_{stg}$   | -55 to +150    | °C   |
| Drain-source voltage slope                       | $dv/dt$          | $T_J = 125$ °C | V/ns |
| Reverse diode $dv/dt$ <sup>c</sup>               |                  |                |      |

#### Notes

- Repetitive rating; pulse width limited by maximum junction temperature
- $V_{DD} = 120$  V, starting  $T_J = 25$  °C,  $L = 28.2$  mH,  $R_g = 25$   $\Omega$ ,  $I_{AS} = 2.5$  A
- $I_{SD} \leq I_D$ ,  $di/dt = 500$  A/ $\mu$ s, starting  $T_J = 25$  °C

**THERMAL RESISTANCE RATINGS**

| PARAMETER                        | SYMBOL     | TYP. | MAX. | UNIT |
|----------------------------------|------------|------|------|------|
| Maximum junction-to-ambient      | $R_{thJA}$ | 42   | 55   | °C/W |
| Maximum junction-to-case (drain) | $R_{thJC}$ | 0.57 | 0.80 |      |

**SPECIFICATIONS** ( $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

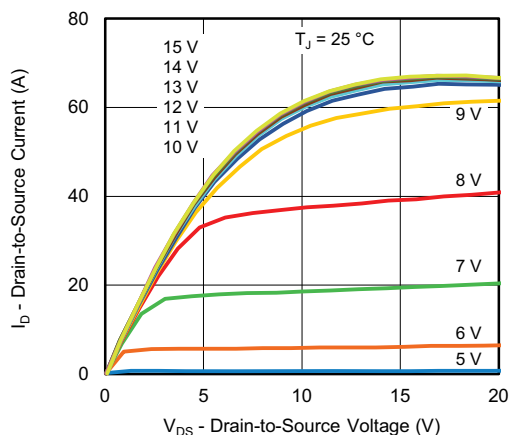
| PARAMETER                                                 | SYMBOL              | TEST CONDITIONS                                                                                                                                          |                                               | MIN. | TYP.  | MAX.      | UNIT                |
|-----------------------------------------------------------|---------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------|------|-------|-----------|---------------------|
| Static                                                    |                     |                                                                                                                                                          |                                               |      |       |           |                     |
| Drain-source breakdown voltage                            | $V_{DS}$            | $V_{GS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$                                                                                                   |                                               | 600  | -     | -         | V                   |
| $V_{DS}$ temperature coefficient                          | $\Delta V_{DS}/T_J$ | Reference to $25\text{ }^\circ\text{C}$ , $I_D = 1\text{ mA}$                                                                                            |                                               | -    | 0.67  | -         | V/ $^\circ\text{C}$ |
| Gate-source threshold voltage (N)                         | $V_{GS(th)}$        | $V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$                                                                                                       |                                               | 3    | -     | 5         | V                   |
| Gate-source leakage                                       | $I_{GSS}$           | $V_{GS} = \pm 20\text{ V}$                                                                                                                               |                                               | -    | -     | $\pm 100$ | nA                  |
|                                                           |                     | $V_{GS} = \pm 30\text{ V}$                                                                                                                               |                                               | -    | -     | $\pm 1$   | $\mu\text{A}$       |
| Zero gate voltage drain current                           | $I_{DSS}$           | $V_{DS} = 480\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                                          |                                               | -    | -     | 1         | $\mu\text{A}$       |
|                                                           |                     | $V_{DS} = 480\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 125\text{ }^\circ\text{C}$                                                                      |                                               | -    | -     | 2         | mA                  |
| Drain-source on-state resistance                          | $R_{DS(on)}$        | $V_{GS} = 10\text{ V}$                                                                                                                                   | $I_D = 12\text{ A}$                           | -    | 0.109 | 0.125     | $\Omega$            |
| Forward transconductance <sup>a</sup>                     | $g_{fs}$            | $V_{DS} = 20\text{ V}$ , $I_D = 12\text{ A}$                                                                                                             |                                               | -    | 6     | -         | S                   |
| Dynamic                                                   |                     |                                                                                                                                                          |                                               |      |       |           |                     |
| Input capacitance                                         | $C_{iss}$           | $V_{GS} = 0\text{ V}$ ,<br>$V_{DS} = 100\text{ V}$ ,<br>$f = 1\text{ MHz}$                                                                               |                                               | -    | 1533  | -         | pF                  |
| Output capacitance                                        | $C_{oss}$           |                                                                                                                                                          |                                               | -    | 68    | -         |                     |
| Reverse transfer capacitance                              | $C_{rss}$           |                                                                                                                                                          |                                               | -    | 6     | -         |                     |
| Effective output capacitance, energy related <sup>a</sup> | $C_{o(er)}$         | $V_{DS} = 0\text{ V to } 480\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                           |                                               | -    | 54    | -         |                     |
| Effective output capacitance, time related <sup>b</sup>   | $C_{o(tr)}$         |                                                                                                                                                          |                                               | -    | 351   | -         |                     |
| Total gate charge                                         | $Q_g$               | $V_{GS} = 10\text{ V}$                                                                                                                                   | $I_D = 12\text{ A}$ , $V_{DS} = 480\text{ V}$ | -    | 31    | 47        | nC                  |
| Gate-source charge                                        | $Q_{gs}$            |                                                                                                                                                          |                                               | -    | 12    | -         |                     |
| Gate-drain charge                                         | $Q_{gd}$            |                                                                                                                                                          |                                               | -    | 11    | -         |                     |
| Turn-on delay time                                        | $t_{d(on)}$         | $V_{DD} = 480\text{ V}$ , $I_D = 12\text{ A}$ ,<br>$V_{GS} = 10\text{ V}$ , $R_g = 9.1\text{ }\Omega$                                                    |                                               | -    | 19    | 38        | ns                  |
| Rise time                                                 | $t_r$               |                                                                                                                                                          |                                               | -    | 33    | 66        |                     |
| Turn-off delay time                                       | $t_{d(off)}$        |                                                                                                                                                          |                                               | -    | 33    | 66        |                     |
| Fall time                                                 | $t_f$               |                                                                                                                                                          |                                               | -    | 20    | 40        |                     |
| Gate input resistance                                     | $R_g$               | $f = 1\text{ MHz}$ , open drain                                                                                                                          |                                               | 0.3  | 0.65  | 1.3       | $\Omega$            |
| Drain-Source Body Diode Characteristics                   |                     |                                                                                                                                                          |                                               |      |       |           |                     |
| Continuous source-drain diode current                     | $I_S$               | MOSFET symbol showing the integral reverse p - n junction diode<br> |                                               | -    | -     | 23        | A                   |
| Pulsed diode forward current                              | $I_{SM}$            |                                                                                                                                                          |                                               | -    | -     | 66        |                     |
| Diode forward voltage                                     | $V_{SD}$            | $T_J = 25\text{ }^\circ\text{C}$ , $I_S = 12\text{ A}$ , $V_{GS} = 0\text{ V}$                                                                           |                                               | -    | -     | 1.2       | V                   |
| Reverse recovery time                                     | $t_{rr}$            | $T_J = 25\text{ }^\circ\text{C}$ , $I_F = I_S = 12\text{ A}$ ,<br>$di/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 400\text{ V}$                              |                                               | -    | 117   | 234       | ns                  |
| Reverse recovery charge                                   | $Q_{rr}$            |                                                                                                                                                          |                                               | -    | 0.7   | 1.4       | $\mu\text{C}$       |
| Reverse recovery current                                  | $I_{RRM}$           |                                                                                                                                                          |                                               | -    | 11    | -         | A                   |

**Notes**

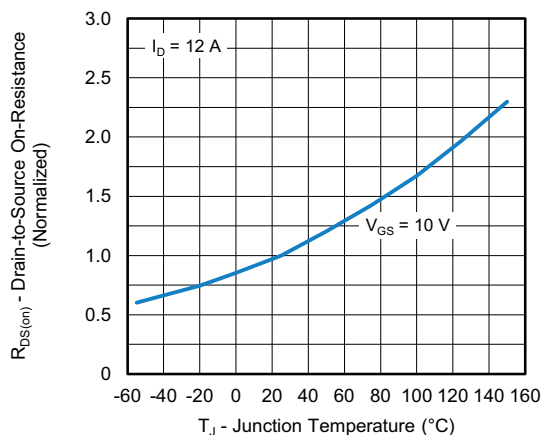
- a.  $C_{oss(er)}$  is a fixed capacitance that gives the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$   
b.  $C_{oss(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$



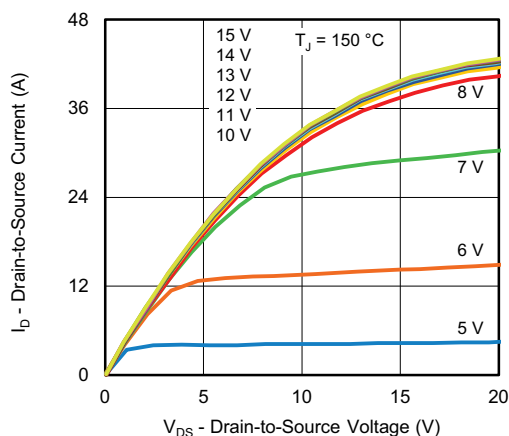
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



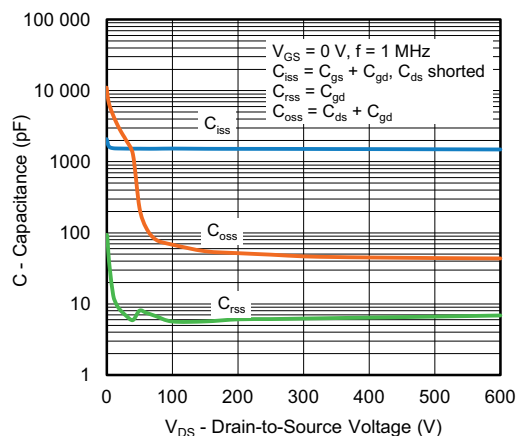
**Fig. 1 - Typical Output Characteristics**



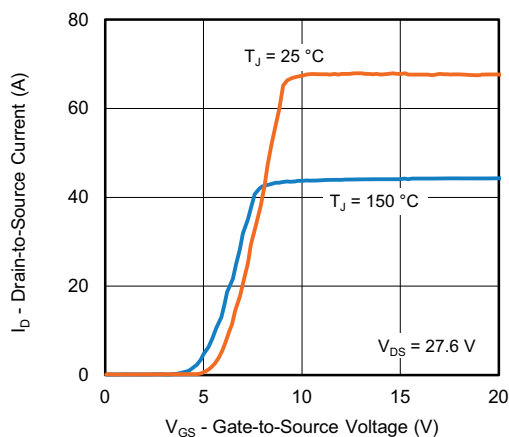
**Fig. 4 - Normalized On-Resistance vs. Temperature**



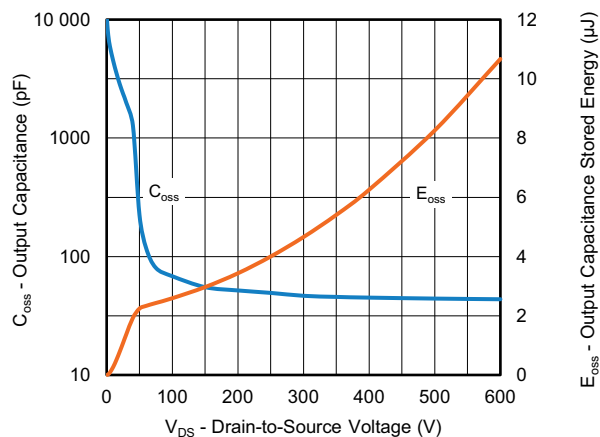
**Fig. 2 - Typical Output Characteristics**



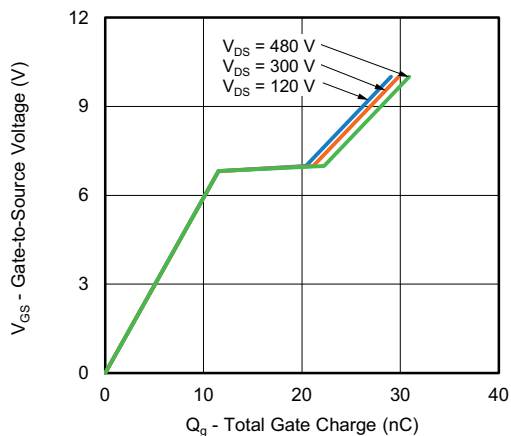
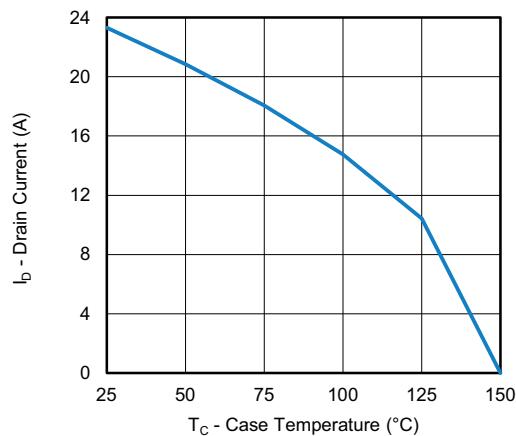
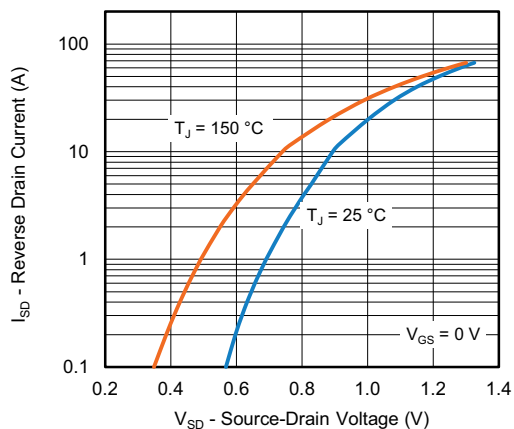
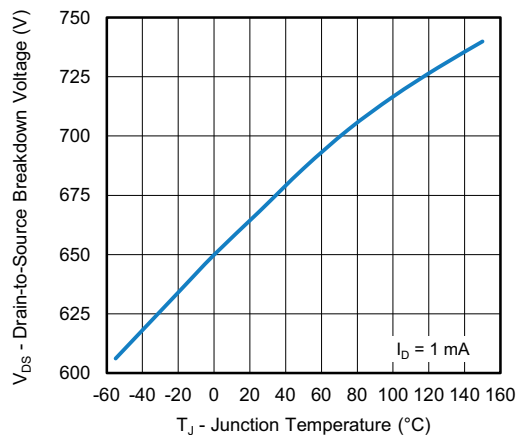
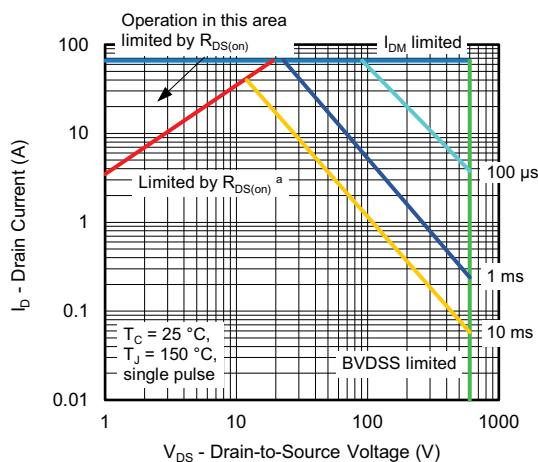
**Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage**



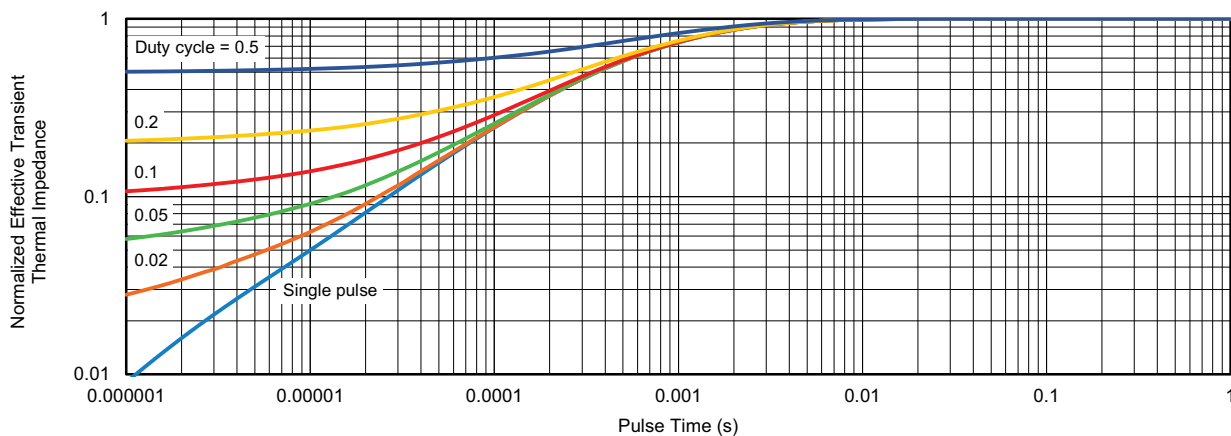
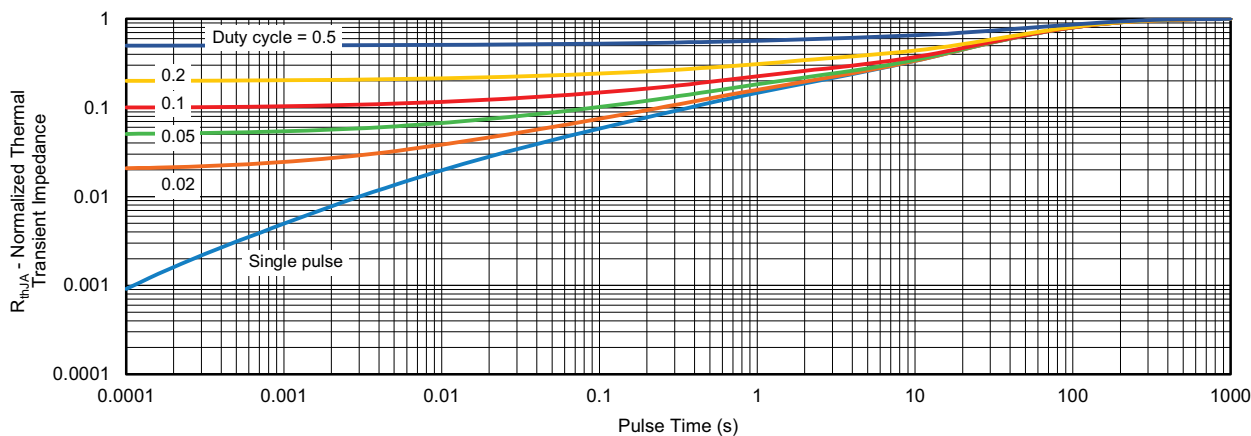
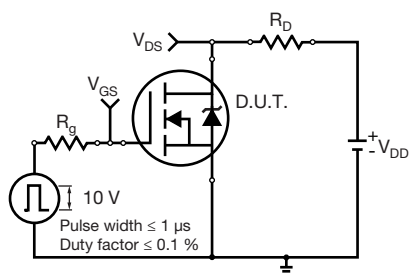
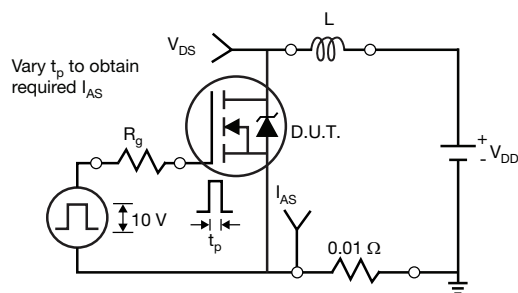
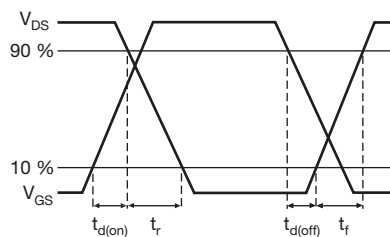
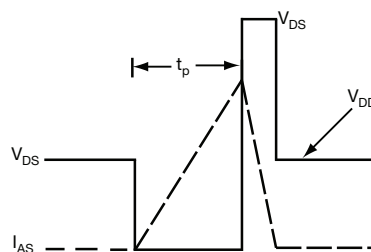
**Fig. 3 - Typical Transfer Characteristics**

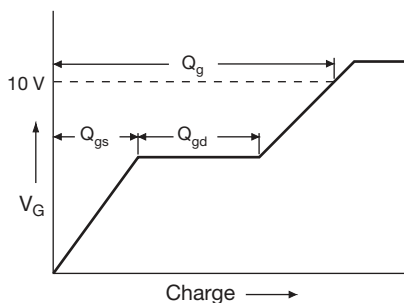
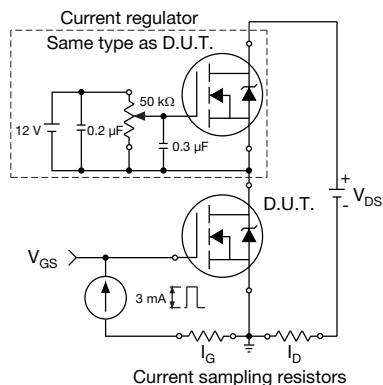
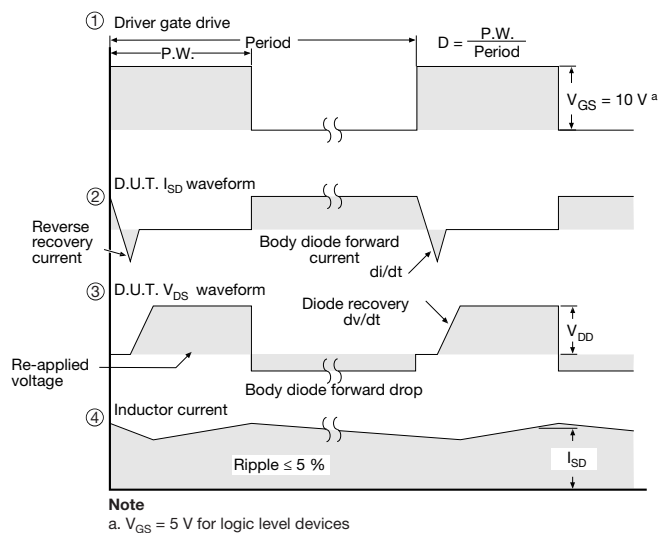
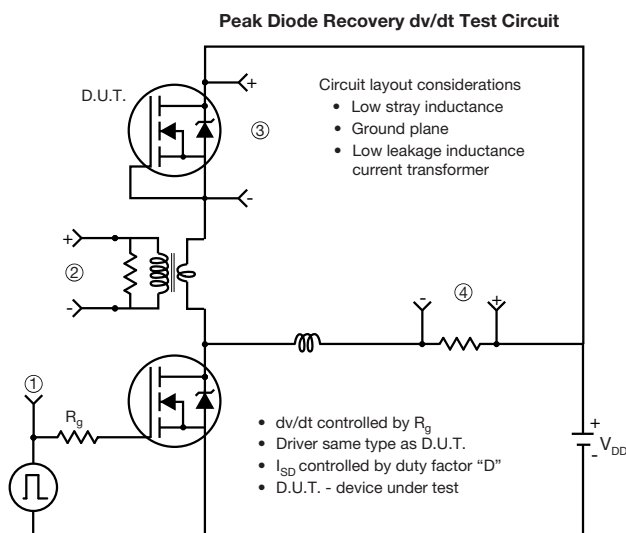


**Fig. 6 -  $C_{oss}$  and  $E_{oss}$  vs.  $V_{DS}$**


**Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage**

**Fig. 10 - Maximum Drain Current vs. Case Temperature**

**Fig. 8 - Typical Source-Drain Diode Forward Voltage**

**Fig. 11 - Temperature vs. Drain-to-Source Voltage**

**Fig. 9 - Maximum Safe Operating Area**
**Note**

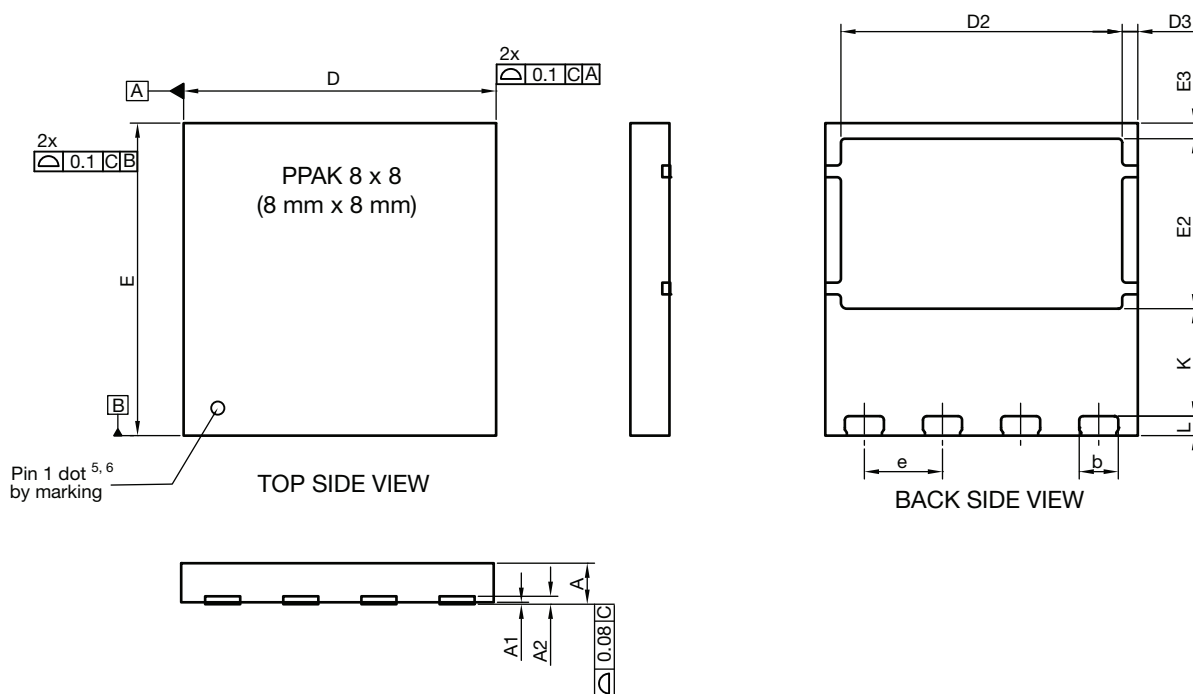
a.  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified


**Fig. 12 - Normalized Transient Thermal Impedance, Junction-to-Case**

**Fig. 13 - Normalized Transient Thermal Impedance, Junction-to-Ambient**

**Fig. 14 - Switching Time Test Circuit**

**Fig. 16 - Unclamped Inductive Test Circuit**

**Fig. 15 - Switching Time Waveforms**

**Fig. 17 - Unclamped Inductive Waveforms**


**Fig. 18 - Basic Gate Charge Waveform**

**Fig. 19 - Gate Charge Test Circuit**

**Fig. 20 - For N-Channel**

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## PowerPAK® 8 x 8 Case Outline



| DIM.             | MILLIMETERS |      |      | INCHES     |       |       |
|------------------|-------------|------|------|------------|-------|-------|
|                  | MIN.        | NOM. | MAX. | MIN.       | NOM.  | MAX.  |
| A                | 0.95        | 1.00 | 1.05 | 0.037      | 0.039 | 0.041 |
| A1               | 0.00        | -    | 0.05 | 0.000      | -     | 0.002 |
| A2               | 020 ref.    |      |      | 0.008 ref. |       |       |
| b                | 0.95        | 1.00 | 1.05 | 0.037      | 0.039 | 0.041 |
| D                | 7.90        | 8.00 | 8.10 | 0.311      | 0.315 | 0.319 |
| D2               | 7.10        | 7.20 | 7.30 | 0.280      | 0.283 | 0.287 |
| D3               | 0.40 BSC    |      |      | 0.016 BSC  |       |       |
| e                | 2.00 BSC    |      |      | 0.079 BSC  |       |       |
| E                | 7.90        | 8.00 | 8.10 | 0.311      | 0.315 | 0.319 |
| E2               | 4.30        | 4.35 | 4.40 | 0.169      | 0.171 | 0.173 |
| E3               | 0.40 BSC    |      |      | 0.016 BSC  |       |       |
| K                | 2.75 BSC    |      |      | 0.108 BSC  |       |       |
| L                | 0.45        | 0.50 | 0.55 | 0.018      | 0.020 | 0.022 |
| N <sup>(3)</sup> | 8           |      |      | 8          |       |       |

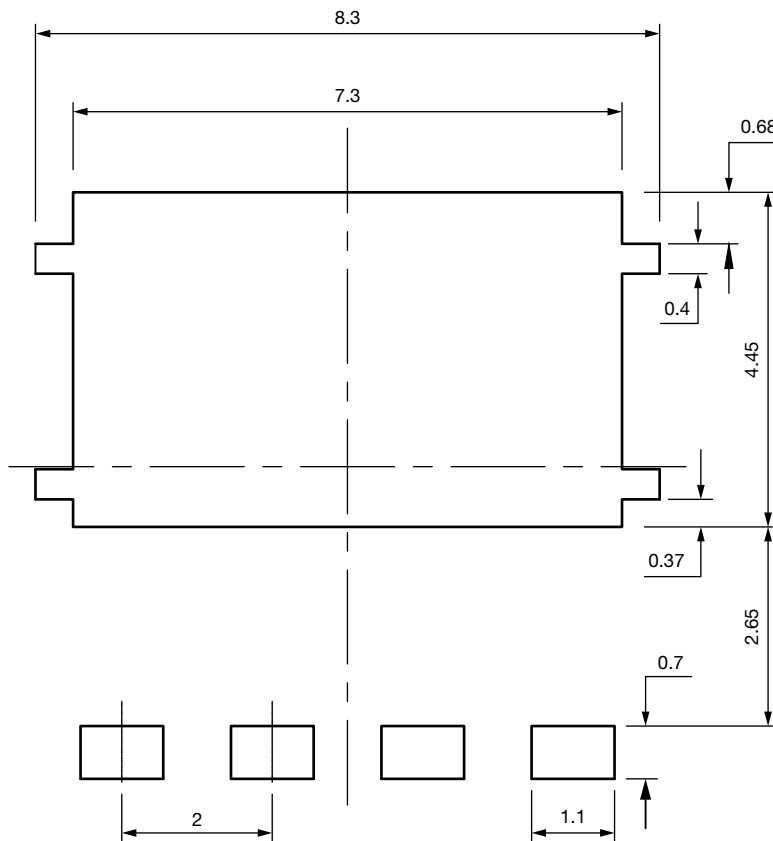
### Notes

- (1) Use millimeters as the primary measurement
- (2) Dimensioning and tolerances conform to ASME Y14.5 M - 1994
- (3) N is the number of terminals
- (4) The pin 1 identifier must be existed on the top surface of the package by using indentation mark or other feature of package body
- (5) Exact shape and size of this feature is optional

ECN: E20-0518-Rev. B, 28-Sep-2020  
DWG: 6041



## Recommended Minimum PADs for PowerPAK® 8 mm x 8 mm



Dimensions in millimeters



## Disclaimer

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