

LYT1402-1604

LYTSwitch-1 Family

Single-Stage LED Driver IC with Combined PFC and Constant Current Output for Buck Topology

Product Highlights

Single-Stage PFC + Accurate CC Output

- +/- 3% CC regulation in single line input voltage applications
- Power factor > 0.9
- High efficiency > 93%
- Robust 725 V MOSFET for increased line voltage surge performance
- Critical Conduction Mode (CrM) buck
- Low EMI
- Excellent line noise and transient rejection

Design Flexibility

- Supports high- and low-side buck topologies
- Wide input (90 VAC – 308 VAC) and output voltage range operation
- 3 family members cover power range for optimum device selection
- Requires no inductor bias winding

Highest Reliability

- Lowest component count
- Comprehensive protection features with auto-restart
 - Input and output overvoltage protection (OVP)
 - Output short-circuit protection
 - Open-loop protection
- Advanced thermal control
 - Thermal foldback ensures that light continues to be delivered at elevated temperatures
 - Over-temperature shutdown provides protection during fault conditions

Description

The LYTSwitch™-1 family is ideal for single-stage, high PF, constant current LED bulbs and tubes.

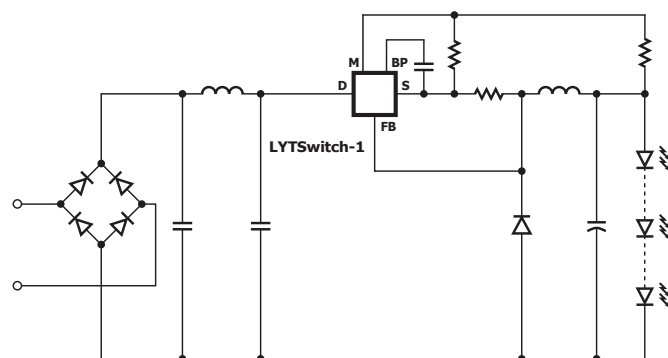
The family incorporates a high-voltage MOSFET with a variable on-time CrM controller. Extensive protection features with minimum external components provide industry leading power density and functionality. The devices can be used in high-side or low-side non-isolated buck topology.

The CrM operation results in low turn-on losses and reduces cost of output diode (slower reverse recovery).

LYTSwitch-1 devices are suitable for applications from 2 W to 22 W. See Table 1 for selection guidance.

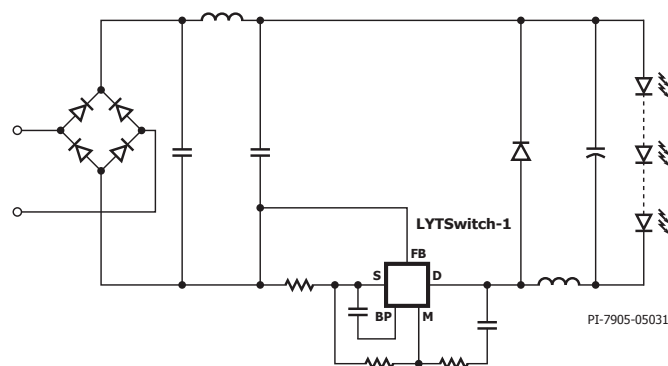


Figure 2. SO-8 D Package.



PI-7906-050316

Figure 1a. High-Side Buck – Typical Application Schematic.



PI-7905-050316

Figure 1b. Low-Side Buck – Typical Application Schematic.

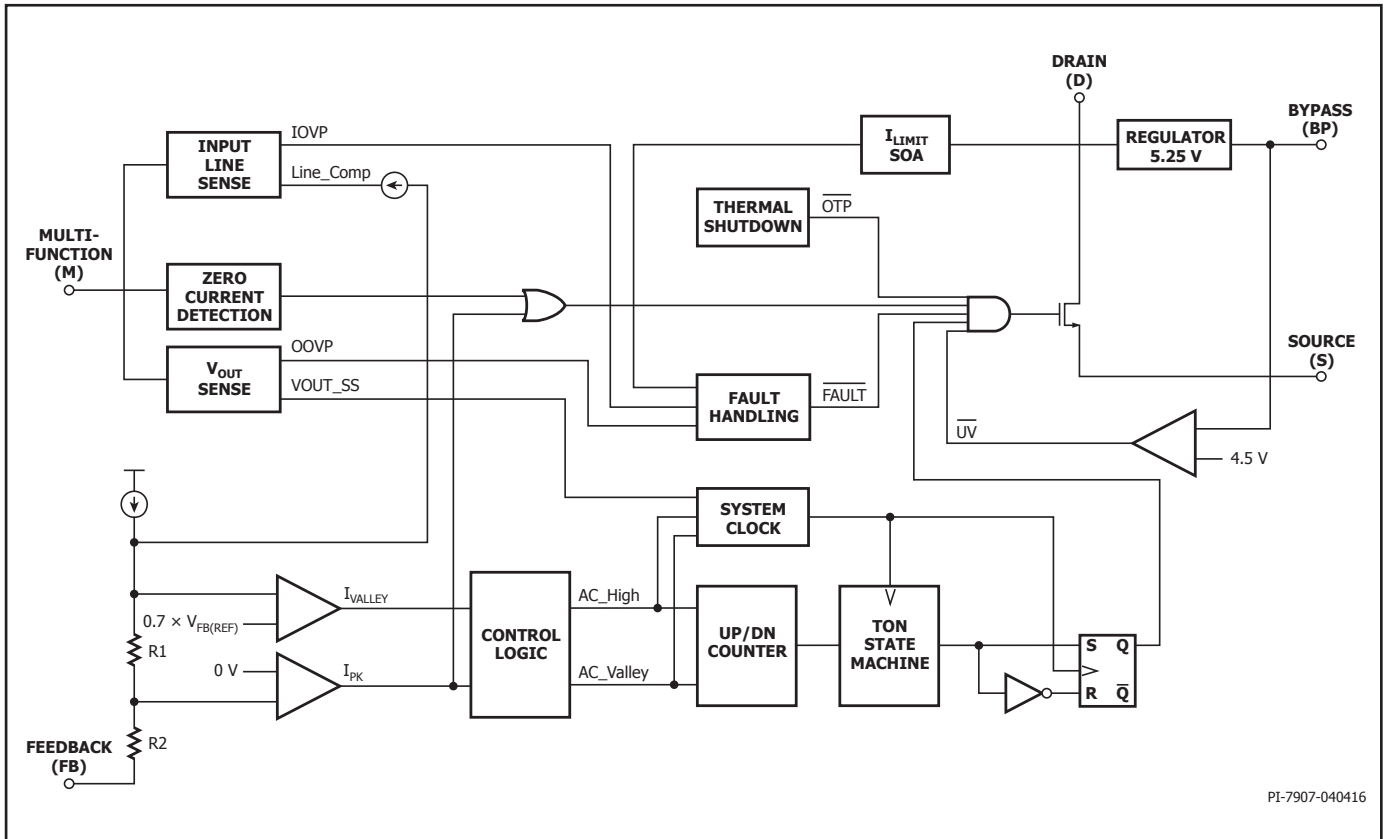
Output Power Table¹

Product ³	Optimized for Smallest Components	
	$V_{OUT} \leq 30 V^2$	$45 V \leq V_{OUT} \leq 55 V^2$
LYT1402D	4.0 W	8.0 W
LYT1403D	7.5 W	15 W
LYT1404D	11 W	22 W
Product ³	Optimized for Lowest THD	
	$V_{OUT} \leq 30 V^2$	$V_{OUT} \geq 55 V^2$
LYT1602D	4.0 W	8.0 W
LYT1603D	7.5 W	15 W
LYT1604D	11 W	22 W

Table 1. Output Power Table (Buck Topology).

Notes:

1. Maximum practical continuous power in an open frame design with adequate heat sinking, measured at 50°C ambient.
2. Output power scales linearly if VOUT falls in between the specified voltages.
3. Package: SO-8 (D Package).



PI-7907-040416

Figure 2. Block Diagram.

Pin Functional Description

BYPASS (BP) Pin:

5.25 V supply rail.

MULTIFUNCTION (M) Pin:

Mode 1: FET OFF

- Detection of inductor de-magnetization (ZCD) to ensure CrM.
- Output OVP Sensing (120 % of V_{OUT} nominal).
- Steady-state operation voltage range is [1 V – 2.4 V].

Mode 2: FET ON

- Line OVP.

FEEDBACK (FB) Pin:

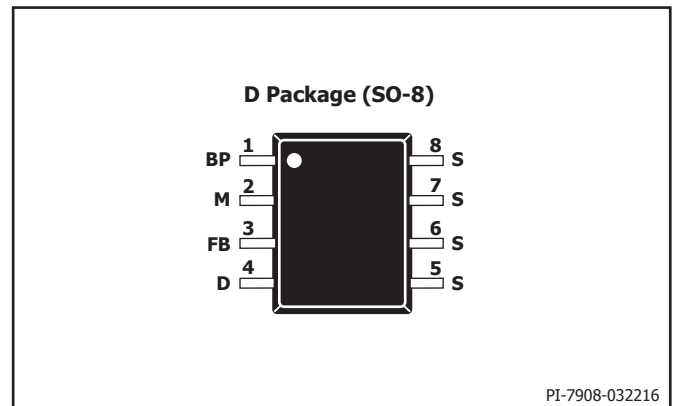
- FET current sensing using external current sense resistor.
- Normal operation voltage range is [$V_{FB(REF)} - 0$ V].

DRAIN (D) Pin:

High-voltage internal MOSFET.

SOURCE (S) Pin:

Power and signal ground.



PI-7908-032216

Figure 3. Pin Configuration.

Absolute Maximum Ratings^(1,3)

DRAIN Pin Voltage:	LYT1x0x.....	-0.3 V to 725 V
DRAIN Pin Peak Current:	LYT1x02	1.05 A (1.3 A) ⁽¹⁾
	LYT1x03	2.1 A (2.6 A) ⁽¹⁾
	LYT1x04	2.8 A (3.5 A) ⁽¹⁾
BYPASS Pin Voltage		-0.3 V to 6.0 V
MULTIFUNCTION, FEEDBACK Pin Voltage.....		-0.45 V to 7.0 V ⁽²⁾
Lead Temperature		260 °C
Storage Temperature		-65 to 150 °C
Operating Junction Temperature.....		-40 to 150 °C ⁽⁴⁾

Notes:

1. The higher peak Drain current (in parentheses) is allowed while the Drain voltage is simultaneously less than 400 V for 725 V integrated MOSFET.
2. In case SOURCE pin is open, -0.7 V between FEEDBACK pin and SOURCE pin is observed with no degradation in performance.
3. The Absolute Maximum Ratings specified may be applied, one at a time without causing permanent damage to the product. Exposure to Absolute Maximum Ratings for extended periods of time may affect product reliability.
4. Normally limited by internal circuitry.

Thermal Resistance

Thermal Resistance: SO-8 Package:

(θ_{JA})	100 °C/W ⁽²⁾ , 80 °C/W ⁽³⁾
$(\theta_{JC})^{(1)}$	30 °C/W

Notes:

1. Measured on the SOURCE pin close to plastic interface.
2. Soldered to 0.36 sq. inch (232 mm²) 2 oz. (610 g/m²) copper clad, with no external heat sink attached.
3. Soldered to 1 sq. in. (645 mm²), 2 oz, (610 g/m²) copper clad.

Parameter	Symbol	Conditions SOURCE = 0 V T _J = -40 °C to 125 °C (Unless Otherwise Specified)	Min	Typ	Max	Units
Control Functions						
Minimum Switching Frequency	f _{MIN}		18	20	22	kHz
Maximum Switch ON-Time	T _{ON(MAX)}		37.5	40	45	μs
Minimum Switch ON-Time	T _{ON(MIN)}		1.012	1.1	1.25	μs
FEEDBACK Pin Reference Voltage	V _{FB(REF)}	T _J = 25 °C	-283	-280	-277	mV
Dead Zone Detect Threshold	V _{TH(DZ)}			0.3 × V _{FB(REF)}		V
Maximum Constant Current Zone	T _{CC(MAX)}			6		ms
Forced Minimum Constant Current Zone	T _{CC(MIN)}			1.2		ms
BYPASS Pin Supply Current	I _{SBY}	Standby (MOSFET not switching)		180		μA
	I _{DSS}	MOSFET Switching	LYT1x02	680		μA
			LYT1x03	785		
			LYT1x04	850		
BYPASS Pin Charge Current	I _{CH1}	V _{BP} = 0.0 V, V _{DS} ≥ 36 V	-10	-4.5		mA
	I _{CH2}	V _{BP} = 5.0 V, V _{DS} ≥ 36 V	-6	-2		mA
BYPASS Pin Voltage	V _{BP}		5.075	5.22	5.35	V

Parameter	Symbol	Conditions SOURCE = 0 V T _j = -40 °C to 125 °C (Unless Otherwise Specified)		Min	Typ	Max	Units
Control Functions (cont.)							
BYPASS Pin Shunt Voltage	V _{BP(SHUNT)}			5.2	5.39	5.55	V
BYPASS Pin Power-Up Reset Threshold Voltage	V _{BP(RESET)}			4.35	4.5	4.65	V
Circuit Protection							
Current Limit for Auto-Restart	I _{LIMIT(AR)}	di/dt = 277 mA/μs T _j = 25 °C	LYT1x02	0.59	0.65	0.70	A
		di/dt = 446 mA/μs T _j = 25 °C	LYT1x03	1.06	1.15	1.24	
		di/dt = 662 mA/μs T _j = 25 °C	LYT1x04	1.61	1.75	1.88	
Fault Minimum Switch ON-Time	T _{FAULT(MIN)}				250	400	ns
Auto-Restart	T _{AR(OFF)1}	T _j = 25 °C			100		ms
	T _{AR(OFF)2}				1000		
Input Overvoltage Threshold	I _{IOV}	T _j = 25 °C		0.9	1.0	1.1	mA
MULTIFUNCTIONAL Pin Auto-Restart Threshold Voltage (Output OVP)	V _{OOV}	T _j = 25 °C		2.3	2.4	2.48	V
MULTIFUNCTIONAL Pin Undervoltage Threshold (Output Short)	V _{OUV}	T _j = 25 °C See Note B		0.91	0.95	0.99	V
Junction Temperature at Fold-Back	T _{FB}	See Note B		138	145	152	°C
Thermal Shutdown Temperature	T _{SD}	See Note A			160		°C
Thermal Shutdown Hysteresis	T _{SD(H)}	See Note A			75		°C

Parameter	Symbol	Conditions SOURCE = 0 V T _J = -40 °C to 125 °C (Unless Otherwise Specified)		Min	Typ	Max	Units
Output							
ON-State Resistance	R _{DS(ON)}	LYT1x02 I _D = 91 mA	T _J = 25 °C		9.2	10.6	Ω
			T _J = 100 °C		14.0	16.1	
		LYT1x03 I _D = 139 mA	T _J = 25 °C		4.5	5.2	
			T _J = 100 °C		6.8	7.8	
		LYT1x04 I _D = 182 mA	T _J = 25 °C		3.4	3.9	
			T _J = 100 °C		5.1	5.8	
OFF-State Leakage	I _{DSS1}	V _{BP} = 5.25 V, V _{DS} = 580 V T _J = 125 °C	LYT1x02			40	μA
			LYT1x03			55	
			LYT1x04			70	
Breakdown Voltage	BV _{DSS}	LYT1x0x		725			V

NOTES:

- A. Guaranteed by design.
 B. This parameter is derived from characterization. Non-production test.

Typical Performance Characteristics

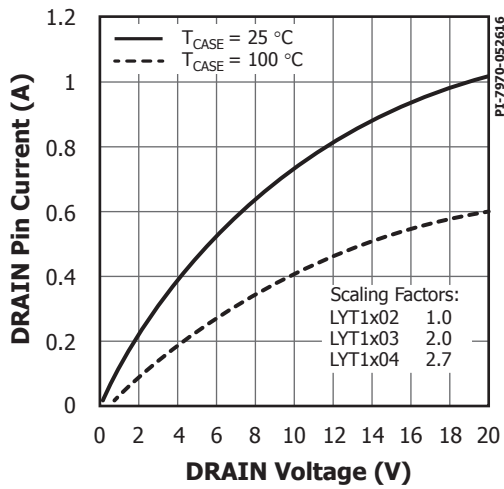


Figure 1. DRAIN Pin Current vs. Drain Pin Voltage.

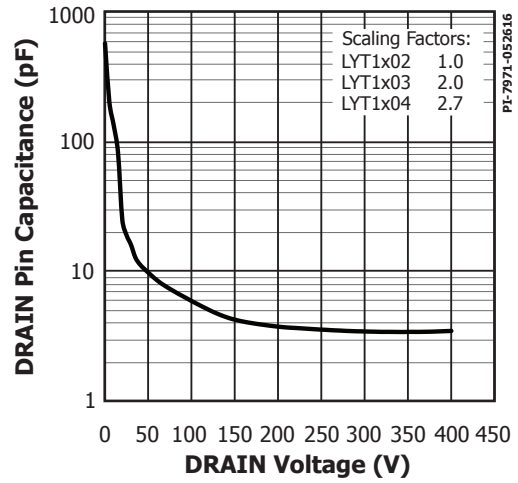


Figure 2. DRAIN Pin Capacitance vs. DRAIN Pin Voltage.

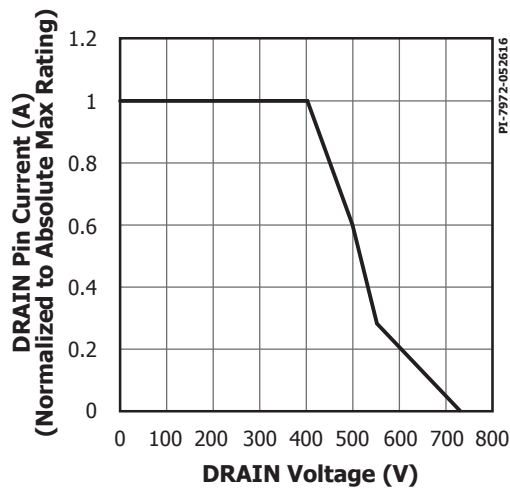
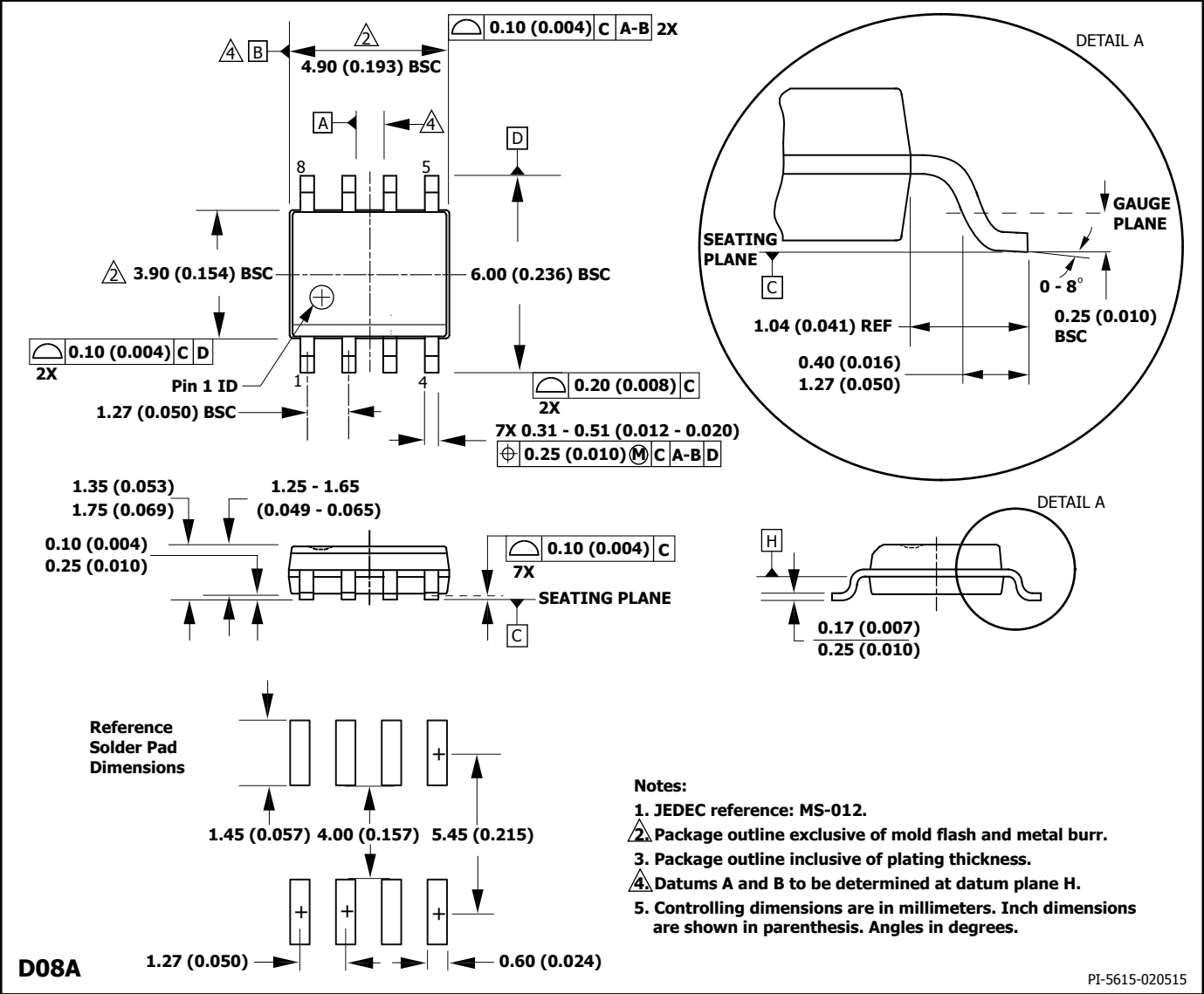


Figure 3. Maximum Allowable DRAIN Pin Current vs. DRAIN Pin Voltage.

SO-8 (D Package)



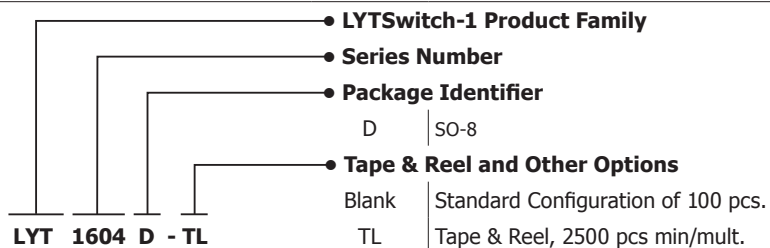
MSL Table

Part Number	MSL Rating
LYT1402D	1
LYT1403D	1
LYT1404D	1
LYT1602D	1
LYT1603D	1
LYT1604D	1

ESD and Latch-Up Table

Test	Conditions	Results
Latch-up at 125 °C	JESD78D	> ±100 mA or > 1.5 × V(max) on all pins
Human Body Model ESD	ANSI/ESDA/JEDEC JS-001-2012	> ±2000 V on all pins
Machine Model ESD	JESD22-A115CA	> ±200 V on all pins
Charged Device Model ESD	JESD22-C101	> ±500 V on all pins

Part Ordering Information



Notes

Revision	Notes	Date
A	Code A.	06/16

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Power Integrations Worldwide Sales Support Locations

World Headquarters

5245 Hellyer Avenue
San Jose, CA 95138, USA.
Main: +1-408-414-9200
Customer Service:
Phone: +1-408-414-9665
Fax: +1-408-414-9765
e-mail: usasales@power.com

China (Shanghai)

Rm 2410, Charity Plaza, No. 88
North Caoxi Road
Shanghai, PRC 200030
Phone: +86-21-6354-6323
Fax: +86-21-6354-6325
e-mail: chinasales@power.com

China (Shenzhen)

17/F, Hivac Building, No. 2, Keji Nan
8th Road, Nanshan District,
Shenzhen, China, 518057
Phone: +86-755-8672-8689
Fax: +86-755-8672-8690
e-mail: chinasales@power.com

Germany

Lindwurmstrasse 114
80337 Munich
Germany
Phone: +49-895-527-39110
Fax: +49-895-527-39200
e-mail: eurosales@power.com

Germany

HellwegForum 1
59469 Ense
Germany
Tel: +49-2938-64-39990
e-mail: igbt-driver.sales@power.com

India

#1, 14th Main Road
Vasanthanagar
Bangalore-560052 India
Phone: +91-80-4113-8020
Fax: +91-80-4113-8023
e-mail: indiasales@power.com

Italy

Via Milanese 20, 3rd. Fl.
20099 Sesto San Giovanni (MI) Italy
Phone: +39-024-550-8701
Fax: +39-028-928-6009
e-mail: eurosales@power.com

Japan

Kosei Dai-3 Bldg.
2-12-11, Shin-Yokohama,
Kohoku-ku
Yokohama-shi, Kanagawa
222-0033 Japan
Phone: +81-45-471-1021
Fax: +81-45-471-3717
e-mail: japansales@power.com

Korea

RM 602, 6FL
Korea City Air Terminal B/D, 159-6
Samsung-Dong, Kangnam-Gu,
Seoul, 135-728, Korea
Phone: +82-2-2016-6610
Fax: +82-2-2016-6630
e-mail: koreasales@power.com

Singapore

51 Newton Road
#19-01/05 Goldhill Plaza
Singapore, 308900
Phone: +65-6358-2160
Fax: +65-6358-2015
e-mail: singaporesales@power.com

Taiwan

5F, No. 318, Nei Hu Rd., Sec. 1
Nei Hu Dist.
Taipei 11493, Taiwan R.O.C.
Phone: +886-2-2659-4570
Fax: +886-2-2659-4550
e-mail: taiwansales@power.com

UK

Cambridge Semiconductor,
a Power Integrations company
Westbrook Centre, Block 5, 2nd Floor
Milton Road
Cambridge CB4 1YG
Phone: +44 (0) 1223-446483
e-mail: eurosales@power.com