1200 V SiC MPS™ Diode



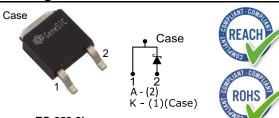
# $V_{RRM}$ = 1200 V $I_{F (Tc = 135^{\circ}C)}$ = 20 A $Q_{C}$ = 33 nC

# Silicon Carbide Schottky Diode

#### **Features**

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q<sub>C</sub>/I<sub>F</sub>
- Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient of V<sub>F</sub>
- Extremely Fast Switching Speeds

# <u>Package</u>



TO-252-2L

## **Advantages**

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current

### **Applications**

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters
- LED and HID Lighting
- AC-DC Converters & Auxiliary Power Supplies

### Absolute Maximum Ratings (At T<sub>C</sub> = 25 °C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit V	
Repetitive Peak Reverse Voltage	$V_{RRM}$		1200		
		T <sub>C</sub> = 25 °C, D = 1	40		
Continuous Forward Current	I <sub>F</sub>	$T_C = 135  ^{\circ}C, D = 1$	20	А	
		$T_C = 166  ^{\circ}C, D = 1$	8		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,SM}$	$T_C = 25 ^{\circ}\text{C},  t_P = 10 \text{ms}$	65	۸	
		$T_C$ = 150 °C, $t_P$ = 10 ms	52	А	
Repetitive Peak Forward Surge Current, Half Sine Wave	I <sub>F,RM</sub>	$T_C = 25 ^{\circ}\text{C},  t_P = 10 \text{ms}$	s 38		
		$T_C$ = 150 °C, $t_P$ = 10 ms	26	Α	
Non-Repetitive Peak Forward Surge Current	I <sub>F,max</sub>	T <sub>C</sub> = 25 °C, t <sub>P</sub> = 10 μs	610	А	
i <sup>2</sup> t Value	∫i² dt	$T_C$ = 25 °C, $t_P$ = 10 ms	28	$A^2s$	
Non-Repetitive Avalanche Energy	E <sub>AS</sub>	$L = 3 \text{ mH}, I_{AS} = 8 \text{ A}$	90	mJ	
Diode Ruggedness	dV/dt	V <sub>R</sub> = 0 ~ 960 V	100	V/ns	
Power Dissipation	P <sub>tot</sub>	T <sub>C</sub> = 25 °C	272	W	
Operating and Storage Temperature	$T_j$ , $T_stg$		-55 to 175	°C	

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### **Electrical Characteristics**

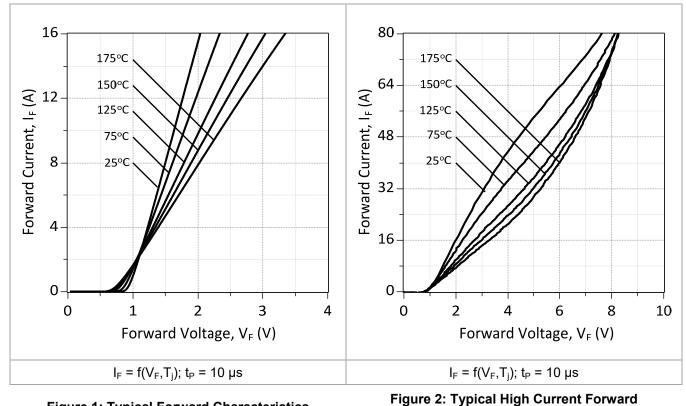
Danamatan	Comple al	Conditions		Values			11!4
Parameter	Symbol			Min.	Тур.	Max.	Unit
Diode Forward Voltage		I <sub>F</sub> = 8 A, T <sub>j</sub> = 25 °C			1.5	1.8	V
	$V_{F}$	$I_F = 8 \text{ A}, T_j = 175 ^{\circ}\text{C}$			2	2.4	
Reverse Current	1	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 25 °C			0.7	7	μΑ
	I <sub>R</sub>	$V_R$ = 1200 V, $T_j$ = 175 °C			2.1	25.2	
Total Capacitive Charge	0		V <sub>R</sub> = 400 V		22		nC
	$Q_{C}$	$I_F \le I_{F,MAX}$	V <sub>R</sub> = 800 V		33		
Switching Time	4	- $dI_F/dt = 200 \text{ A/µs}$ $T_i = 175 \text{ °C}$ $V_R = 400$			- 10		
	$t_s$		V <sub>R</sub> = 800 V		< 10		ns
Total Capacitance	-	V <sub>R</sub> = 1 V, f = 1 MHz, T <sub>j</sub> = 25 °C			545		
	С	$V_R$ = 800 V, f = 1 MHz, $T_j$ = 25 °C			41		pF

### **Thermal / Mechanical Characteristics**

Thermal Resistance, Junction - Case	R <sub>thJC</sub>	0.5	°C/W
Weight	$W_{T}$	0.3	g

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**Figure 1: Typical Forward Characteristics** 

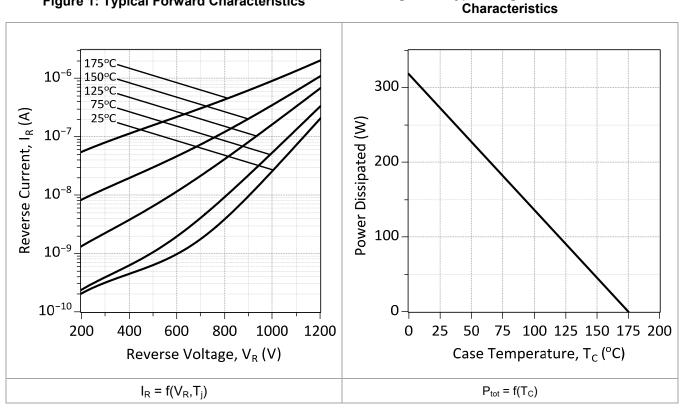
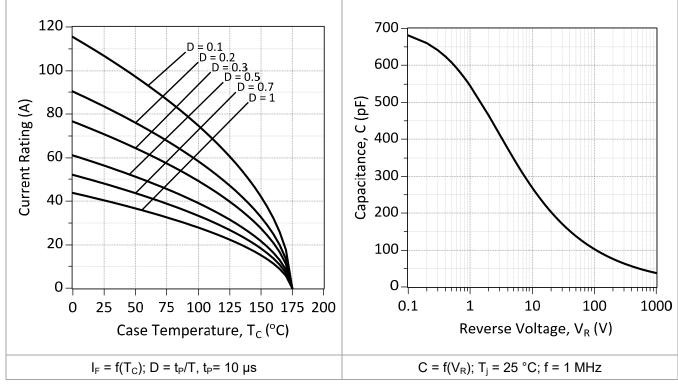


Figure 3: Typical Reverse Characteristics

**Figure 4: Power Derating Curve** 

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**Figure 5: Current Derating Curves** 

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Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics

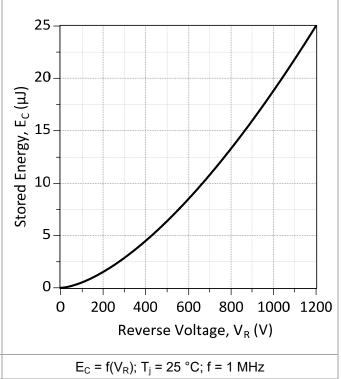
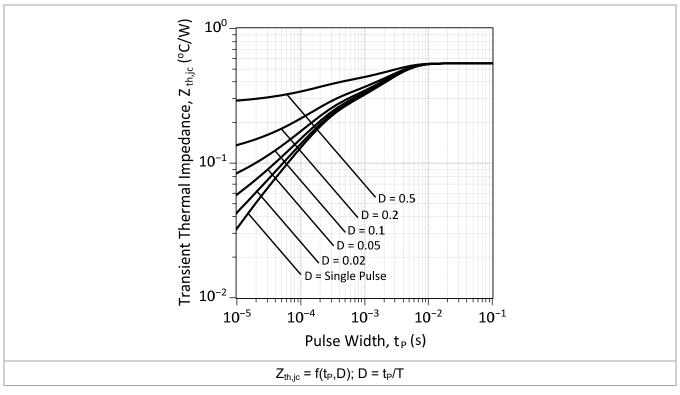


Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics

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**Figure 9: Transient Thermal Impedance** 

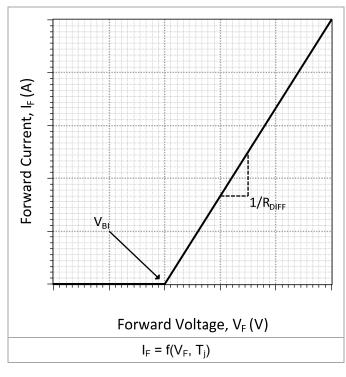


Figure 10: Forward Curve Model

$$I_F = (V_F - V_{BI})/R_{DIFF}(A)$$

#### Built-In Voltage (V<sub>BI</sub>):

$$V_{BI}(T_j) = m^*T_j + n (V)$$
  
 $m = -1.55e-03, n = 1.01$ 

#### Differential Resistance (RDIFF):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c (\Omega);$$
  
  $a = 1.57e-06, b = 2.62e-04, c = 0.0554$ 

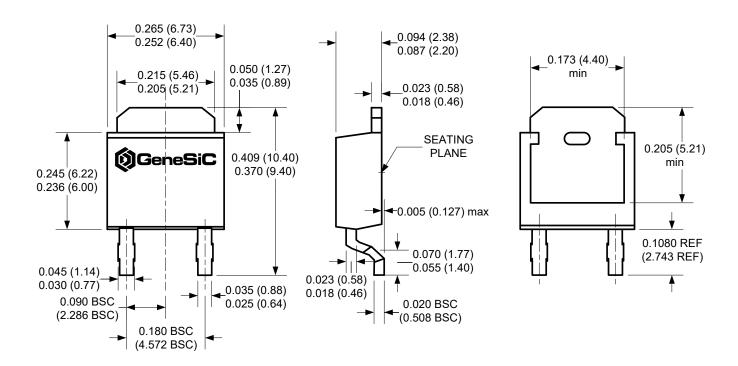
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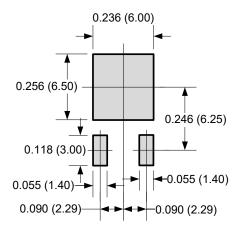
### **Package Dimensions**

#### TO-252-2L

#### **Package Outline**



#### **Recommended Solder Pad Layout**



#### NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

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### **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS), as implemented November 15, 2017. RoHS Declarations for this product can be obtained from your GeneSiC representative.

#### **REACh Compliance**

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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#### **Related Links**

- Soldering Document: http://www.genesicsemi.com/quality/quality-manual/
- Tin-whisker Report: http://www.genesicsemi.com/guality/compliance/
- Reliability Report: http://www.genesicsemi.com/quality/reliability/