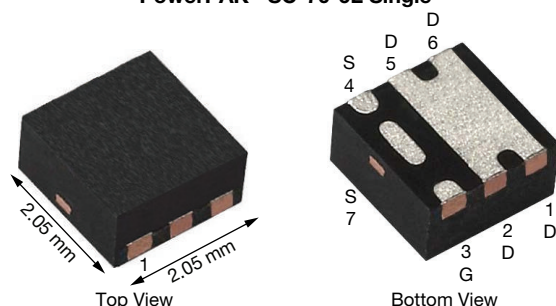


Automotive P-Channel 40 V (D-S) 175 °C MOSFET

PowerPAK® SC-70-6L Single

Marking Code: QEXXXX

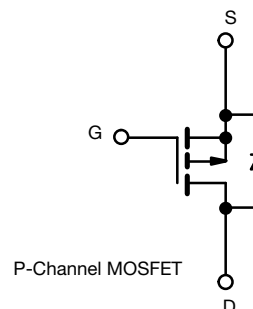
PRODUCT SUMMARY

V_{DS} (V)	-40
$R_{DS(on)}$ (Ω) at $V_{GS} = -10$ V	0.035
$R_{DS(on)}$ (Ω) at $V_{GS} = -4.5$ V	0.050
I_D (A)	-10
Configuration	Single
Package	PowerPAK SC-70

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912

AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE


P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	-40	V
Gate-source voltage	V_{GS}	± 20	
Continuous drain current	I_D	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Continuous source current (diode conduction) ^a	I_S	10	
Pulsed drain current ^b	I_{DM}	-40	
Single pulse avalanche current	I_{AS}	-15	mJ
Single pulse avalanche energy	E_{AS}	11.25	
Maximum power dissipation ^b	P_D	$T_C = 25$ °C	W
		$T_C = 125$ °C	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	R_{thJA}	90	°C/W
Junction-to-case (drain)	R_{thJF}	11	

Notes

- Package limited
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR4 material)

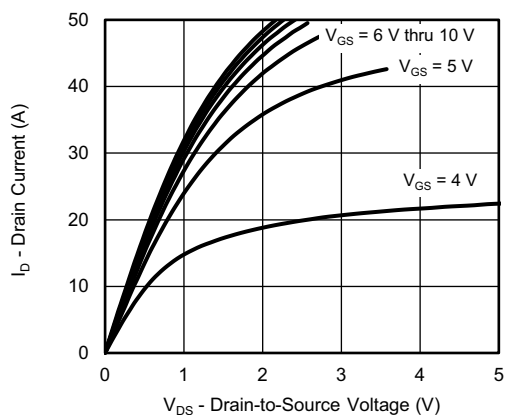
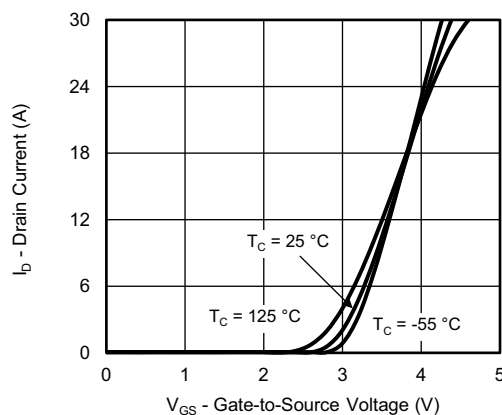
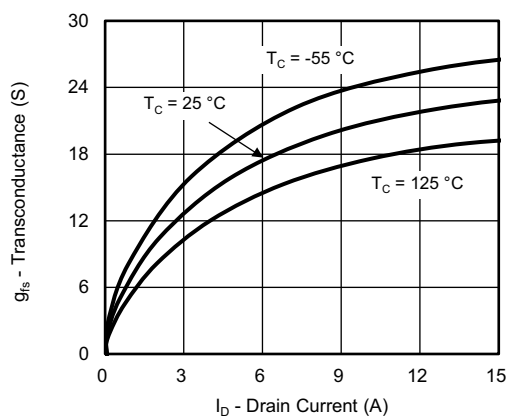
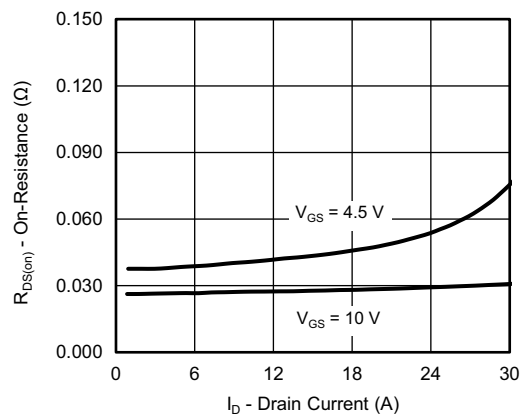
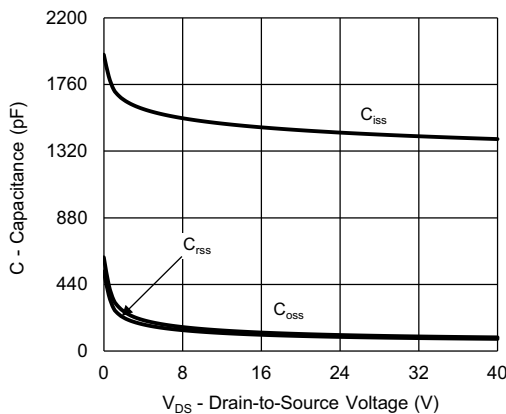
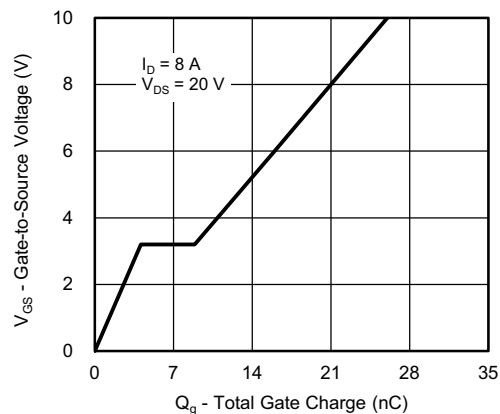


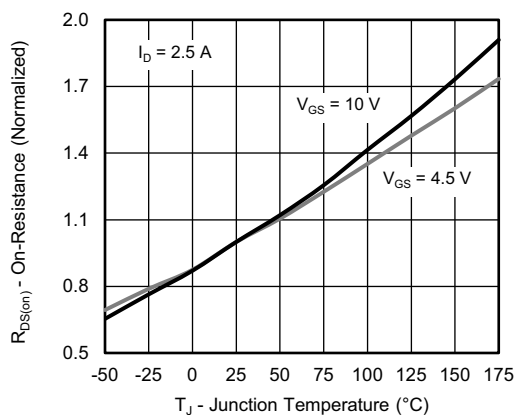
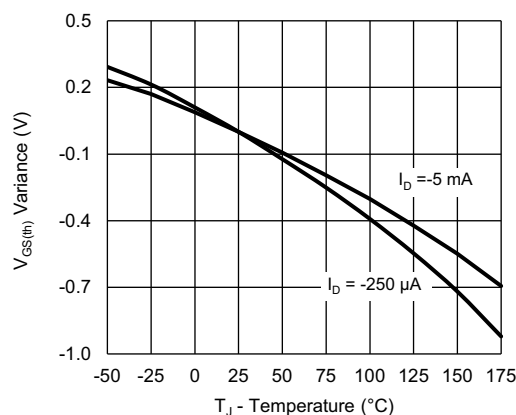
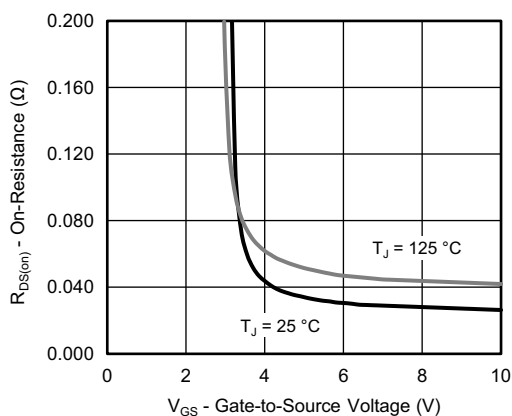
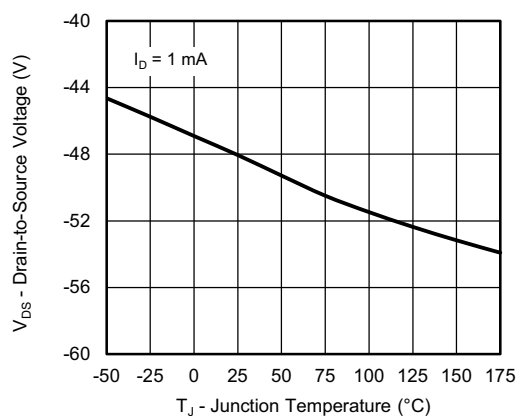
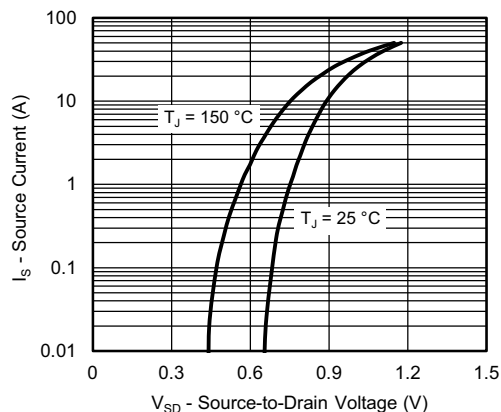
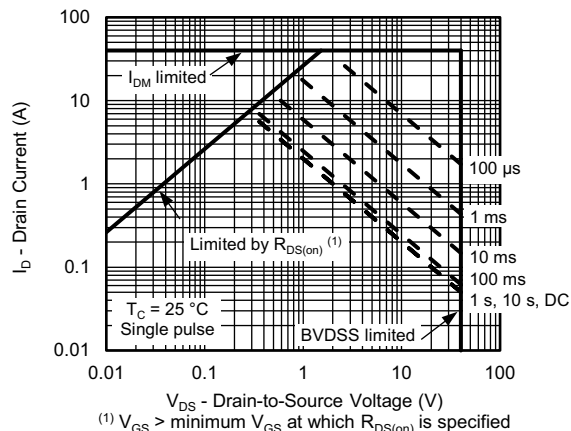
SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA		-40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA		-1.5	-2.0	-2.5	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = -30 V	-	-	-1	μA
		V _{GS} = 0 V	V _{DS} = -40 V, T _J = 125 °C	-	-	-50	
		V _{GS} = 0 V	V _{DS} = -40 V, T _J = 175 °C	-	-	-150	
On-state drain current ^a	I _{D(on)}	V _{GS} = -10 V	V _{DS} ≥ 5 V	-8	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -10 V	I _D = -5 A	-	0.027	0.035	Ω
		V _{GS} = -10 V	I _D = -5 A, T _J = 125 °C	-	-	0.067	
		V _{GS} = -10 V	I _D = -5 A, T _J = 175 °C	-	-	0.080	
		V _{GS} = -4.5 V	I _D = -4 A	-	0.038	0.050	
Forward transconductance ^b	g _{fs}	V _{DS} = -10 V, I _D = -7 A		-	18	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = -25 V, f = 1 MHz	-	1450	1815	pF
Output capacitance	C _{oss}			-	105	131	
Reverse transfer capacitance	C _{rss}			-	92	115	
Total gate charge ^c	Q _g	V _{GS} = -10 V	V _{DS} = -20 V, I _D = -8 A	-	26	33	nC
Gate-source charge ^c	Q _{gs}			-	4.1	-	
Gate-drain charge ^c	Q _{gd}			-	4.8	-	
Gate resistance	R _g	f = 1 MHz		4.4	7.4	11.8	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = -20 V, R _L = 8 Ω I _D ≅ -2.5 A, V _{GEN} = -10 V, R _g = 1 Ω		-	9	15	ns
Rise time ^c	t _r			-	4	8	
Turn-off delay time ^c	t _{d(off)}			-	36	55	
Fall time ^c	t _f			-	10	16	
Source-Drain Diode Ratings and Characteristics							
Pulsed current ^a	I _{SM}			-	-	-40	A
Forward voltage	V _{SD}	I _F = -5 A, V _{GS} = 0		-	-0.84	-1.2	V

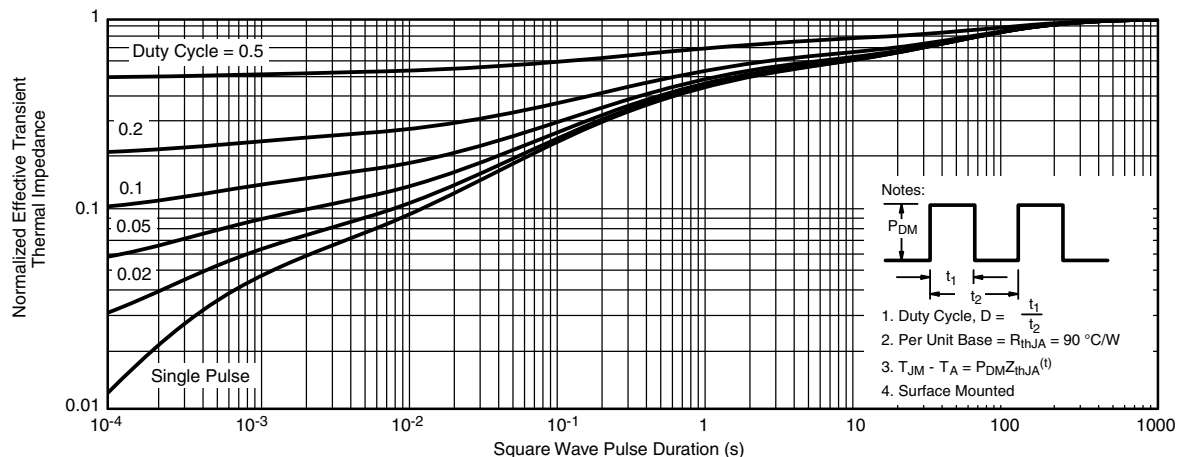
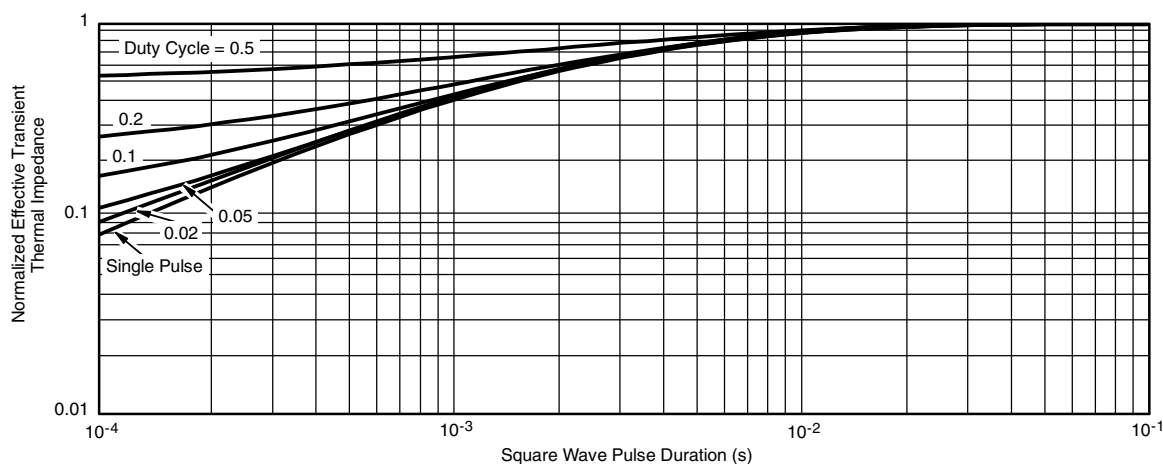
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

On-Resistance vs. Junction Temperature

Threshold Voltage

On-Resistance vs. Gate-to-Source Voltage

Drain Source Breakdown vs. Junction Temperature

Source-Drain Diode Forward Voltage

Safe Operating Area

**THERMAL RATINGS** ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Foot****Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)
- are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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