



ON Semiconductor®

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CPH3362

Power MOSFET –100V, 1.7Ω, –0.7A, Single P-Channel

Features

- On-resistance $R_{DS(on)1}=1.3\Omega$ (typ)
- 4V drive
- Halogen free compliance

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Value	Unit
Drain to Source Voltage	V_{DSS}		–100	V
Gate to Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		–0.7	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	–2.8	A
Power Dissipation	P_D	When mounted on ceramic substrate (900mm ² ×0.8mm)	1	W
Junction Temperature	T_j		150	°C
Storage Temperature	T_{stg}		–55 to +150	°C

This product is designed to “ESD immunity < 200V*”, so please take care when handling.

* Machine Model

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm ² ×0.8mm)	$R_{\theta JA}$	125	°C/W

Electrical Characteristics

 at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}$, $V_{GS} = 0\text{V}$	–100			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -100\text{V}$, $V_{GS} = 0\text{V}$			–1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16\text{V}$, $V_{DS} = 0\text{V}$			±10	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = -10\text{V}$, $I_D = -1\text{mA}$	–1.2		–2.6	V
Forward Transconductance	g_{FS}	$V_{DS} = -10\text{V}$, $I_D = -0.3\text{A}$		1.0		S
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D = -0.7\text{A}$, $V_{GS} = -10\text{V}$		1.3	1.7	Ω
	$R_{DS(on)2}$	$I_D = -0.3\text{A}$, $V_{GS} = -4.5\text{V}$		1.4	1.96	Ω
	$R_{DS(on)3}$	$I_D = -0.3\text{A}$, $V_{GS} = -4\text{V}$		1.45	2.1	Ω
Input Capacitance	C_{iss}	$V_{DS} = -20\text{V}$, $f = 1\text{MHz}$		142		pF
Output Capacitance	C_{oss}			12		pF
Reverse Transfer Capacitance	C_{rss}			7.3		pF

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ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

CPH3362

Continued from preceding page.

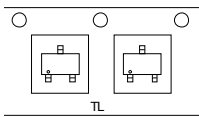
Parameter	Symbol	Conditions	Value			Unit
			min	Typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		3.9		ns
Rise Time	t_r			3.4		ns
Turn-OFF Delay Time	$t_{d(off)}$			28		ns
Fall Time	t_f			12		ns
Total Gate Charge	Q_g	$V_{DS}=-50V, V_{GS}=-10V, I_D=-0.7A$		3.7		nC
Gate to Source Charge	Q_{gs}			0.37		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.86		nC
Forward Diode Voltage	V_{SD}	$I_S=-0.7A, V_{GS}=0V$		-0.83	-1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

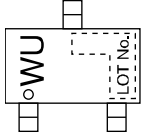
Ordering & Package Information

Device	Package	Shipping	note
CPH3362-TL-W	CPH3, SC-59 SOT-23, TO-236	3,000 pcs. / reel	Pb-Free and Halogen Free

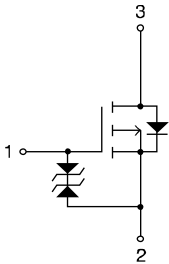
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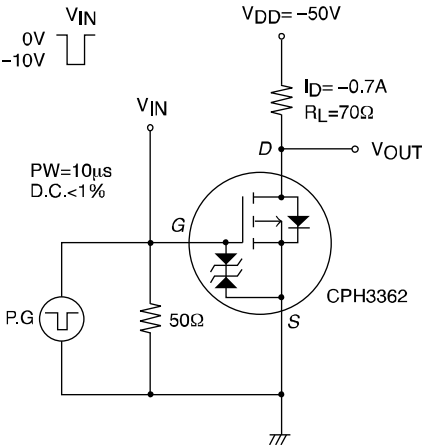
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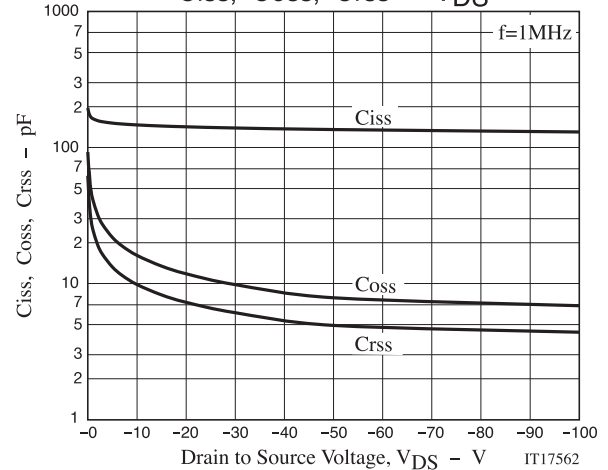
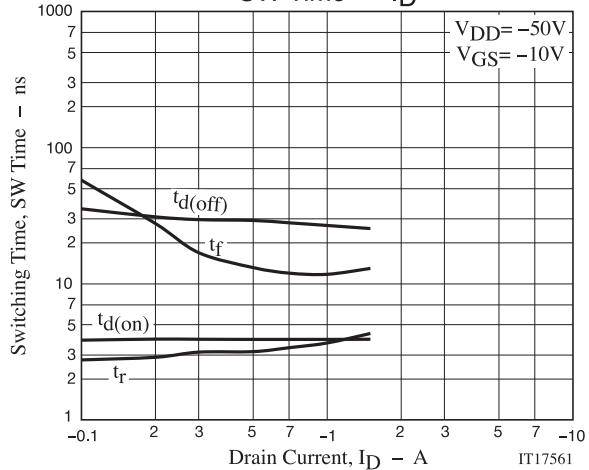
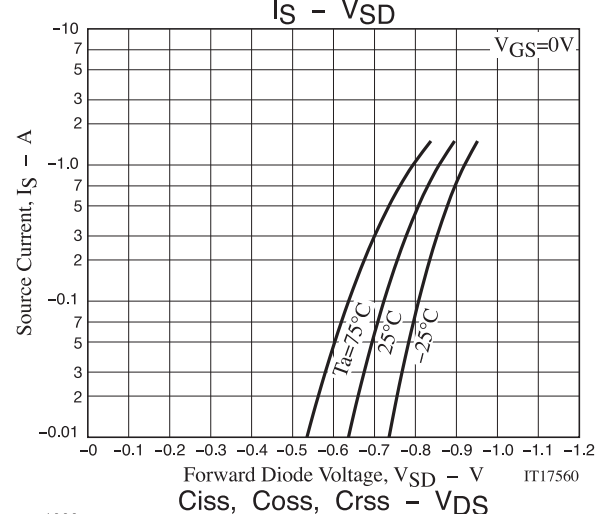
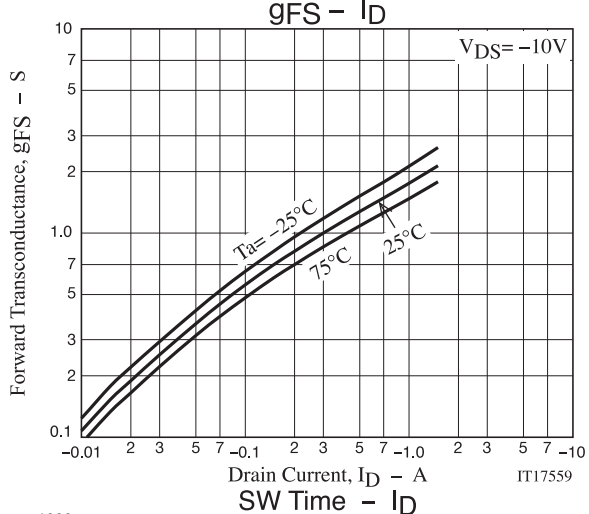
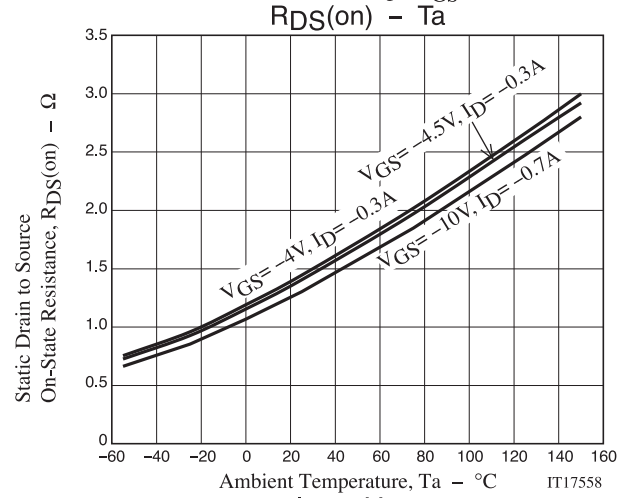
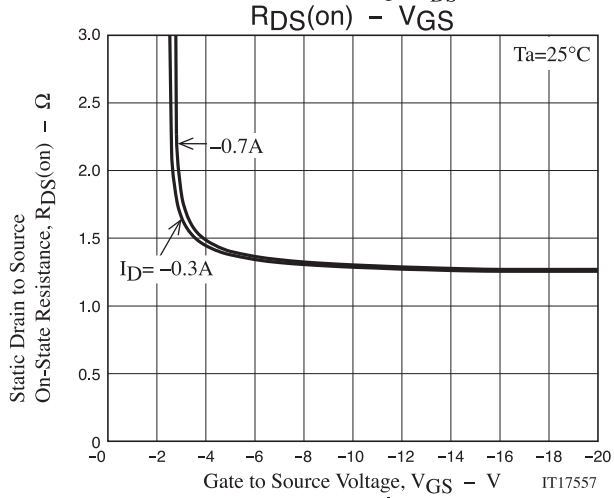
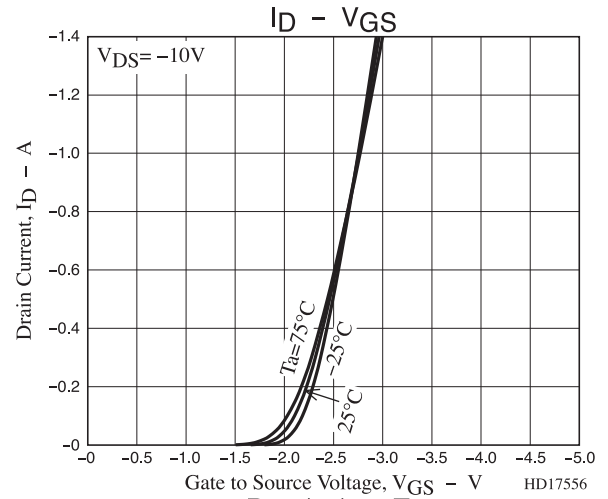
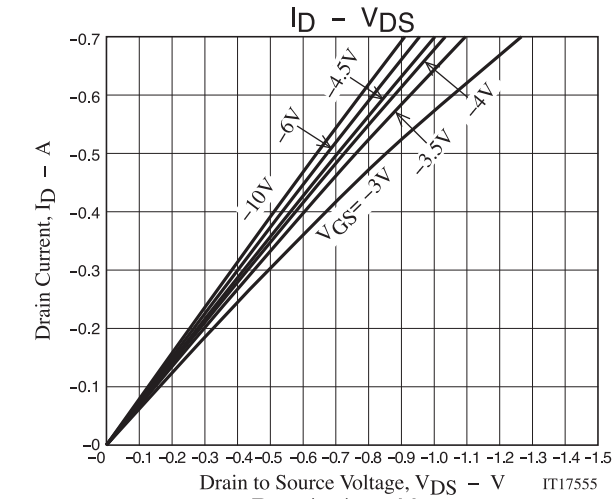


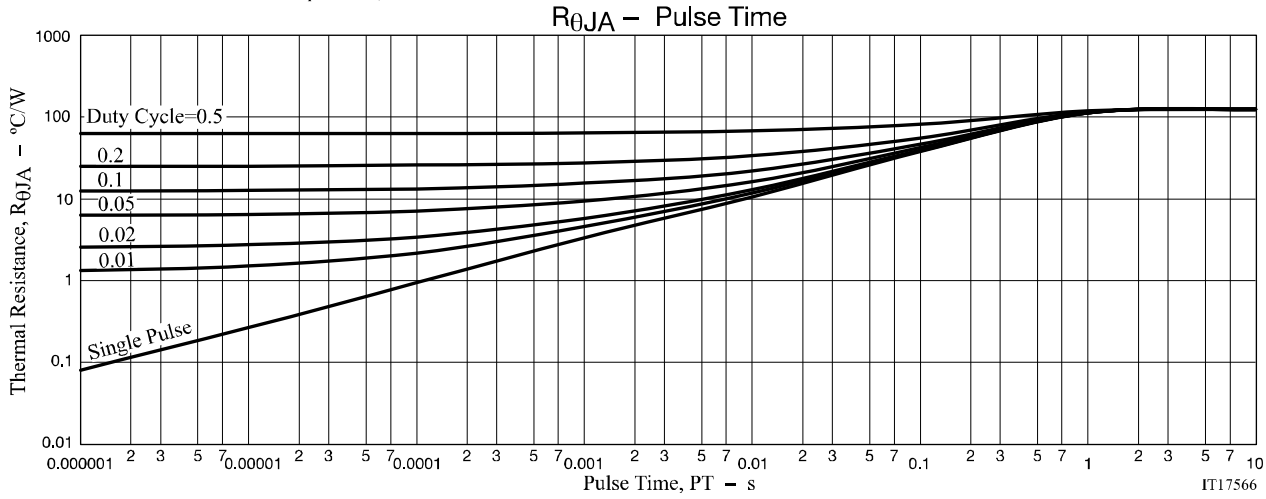
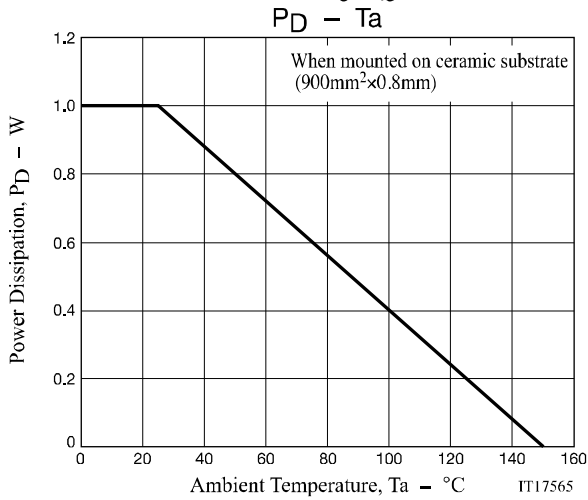
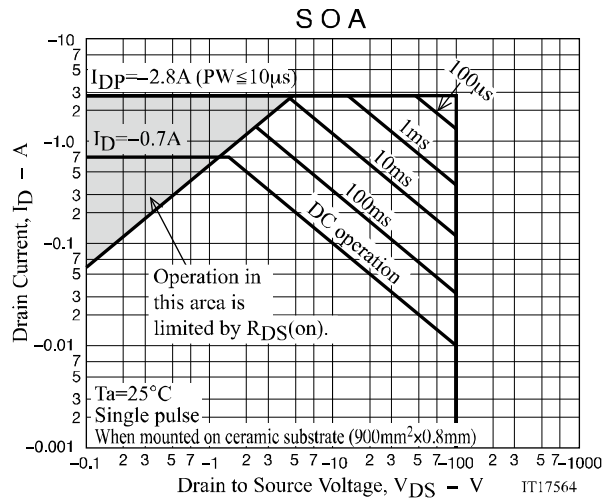
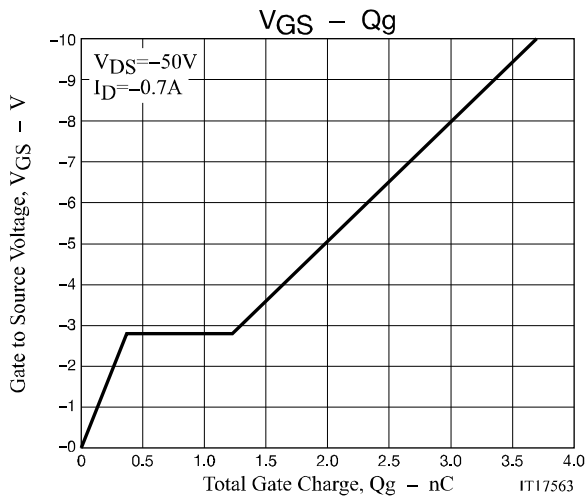
Electrical Connection



Switching Time Test Circuit







Package Dimensions

CPH3362-TL-W

CPH3

CASE 318BA

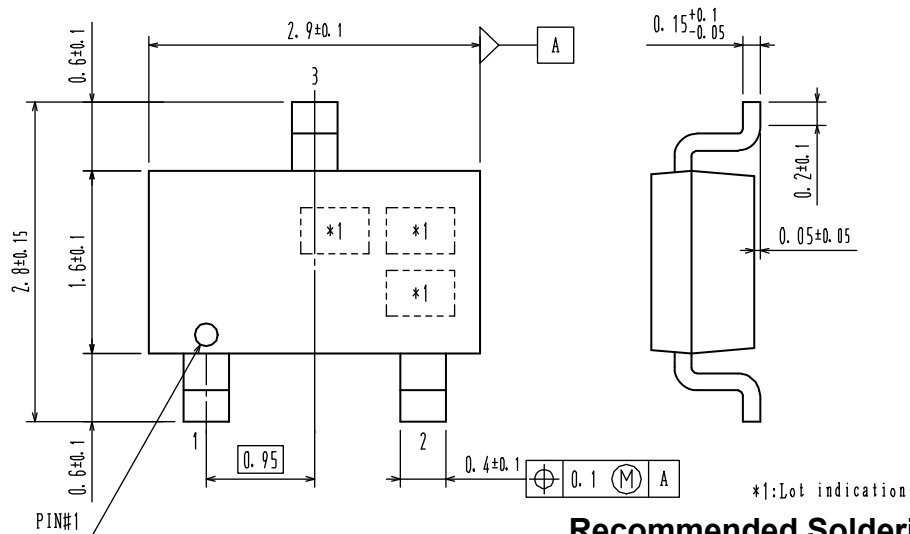
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unit : mm

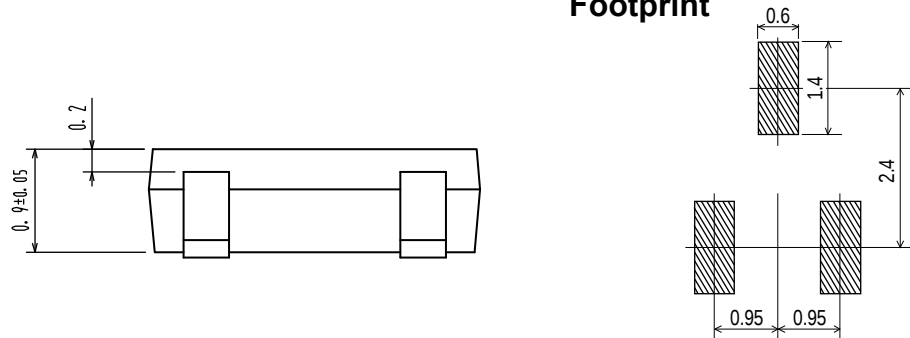
1: Gate

2: Source

3: Drain



Recommended Soldering Footprint



Note on usage : Since the CPH3362 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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