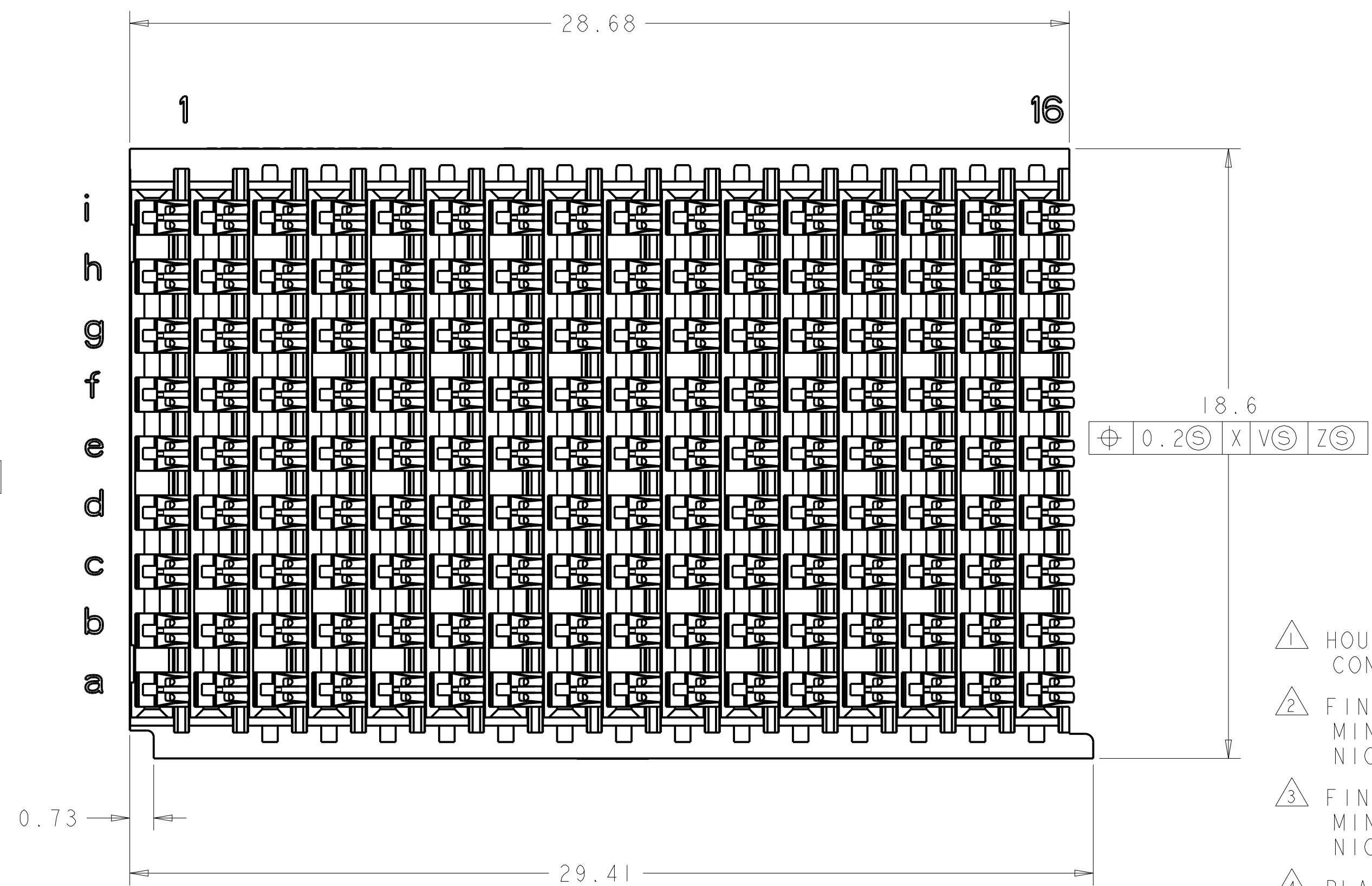
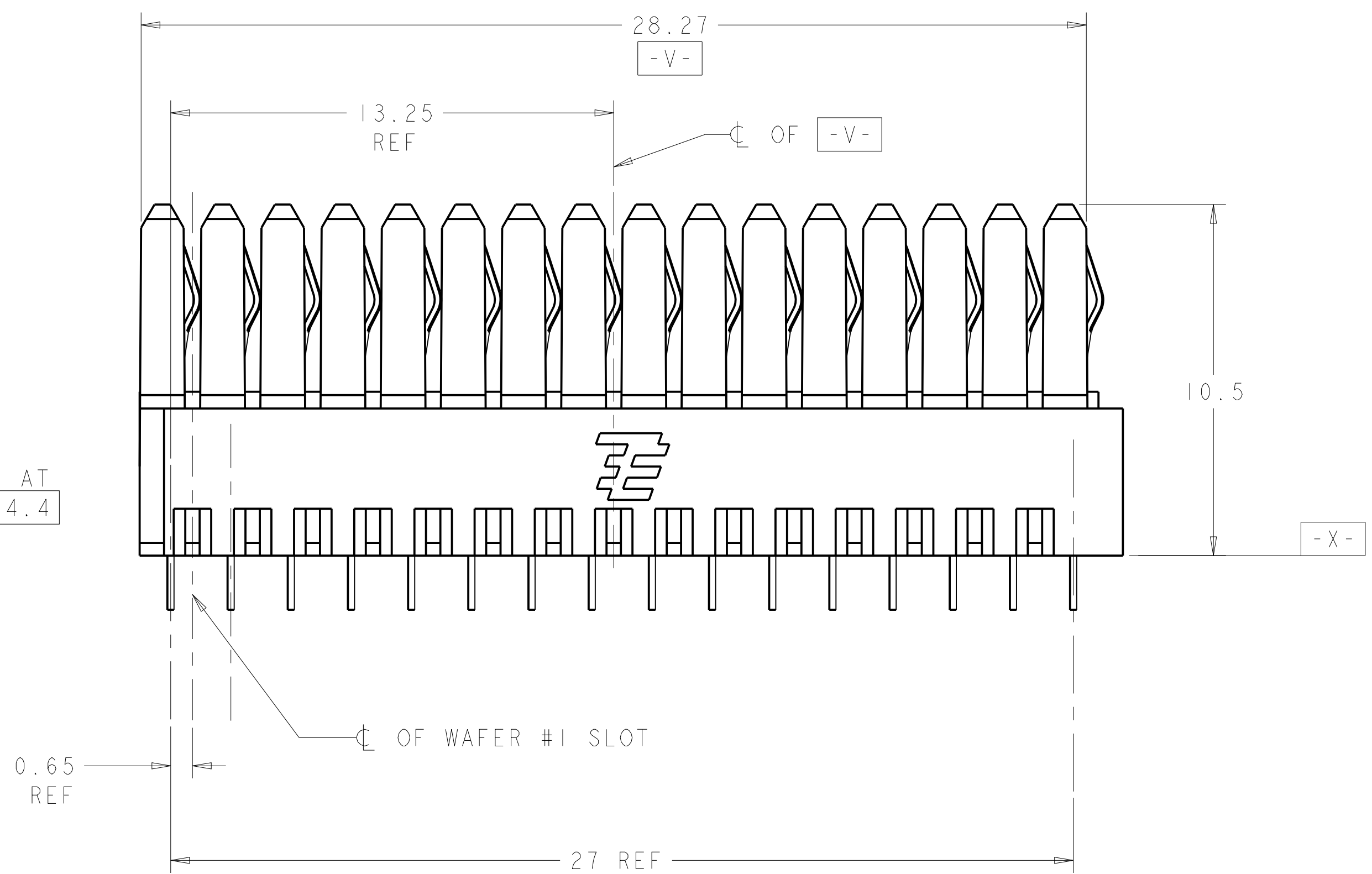




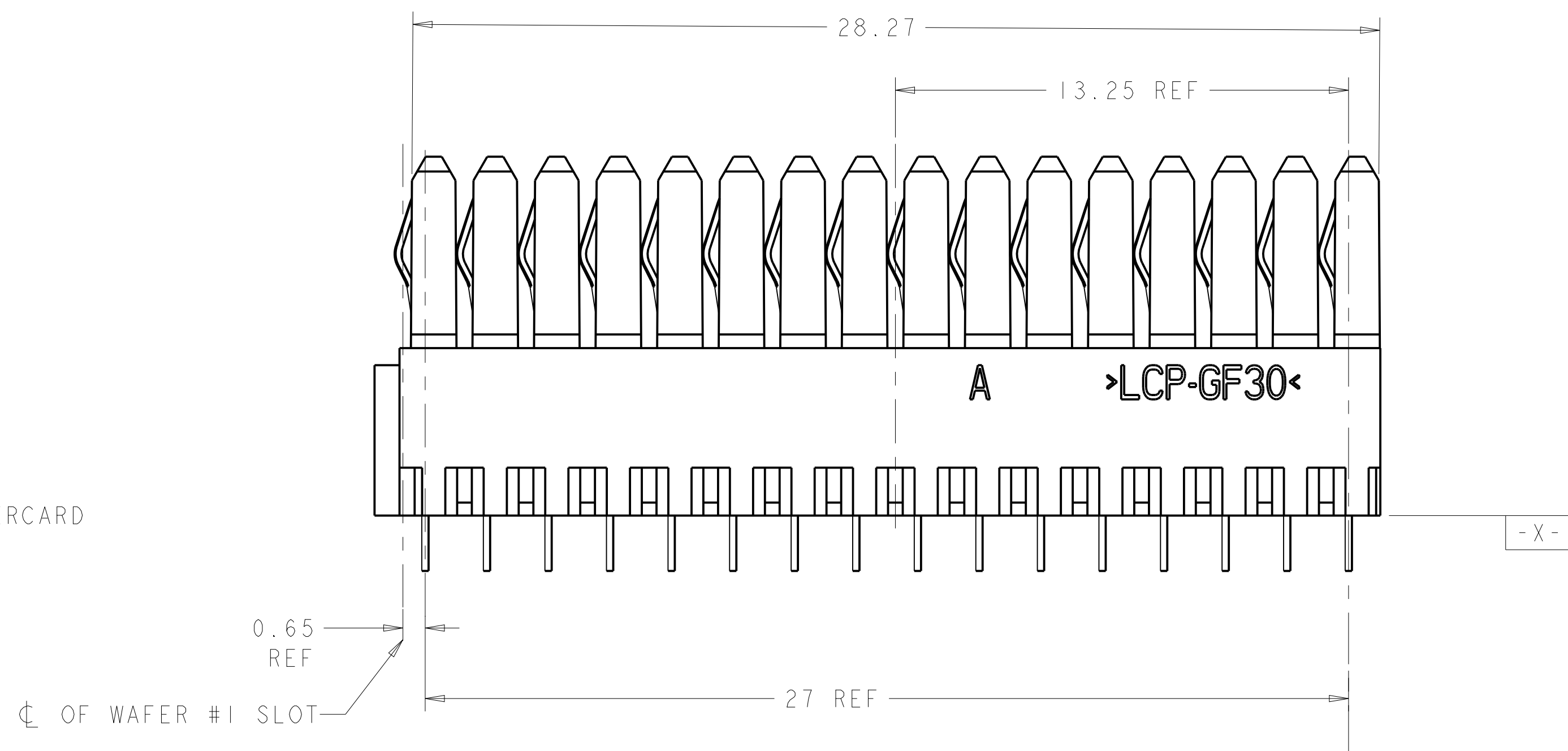
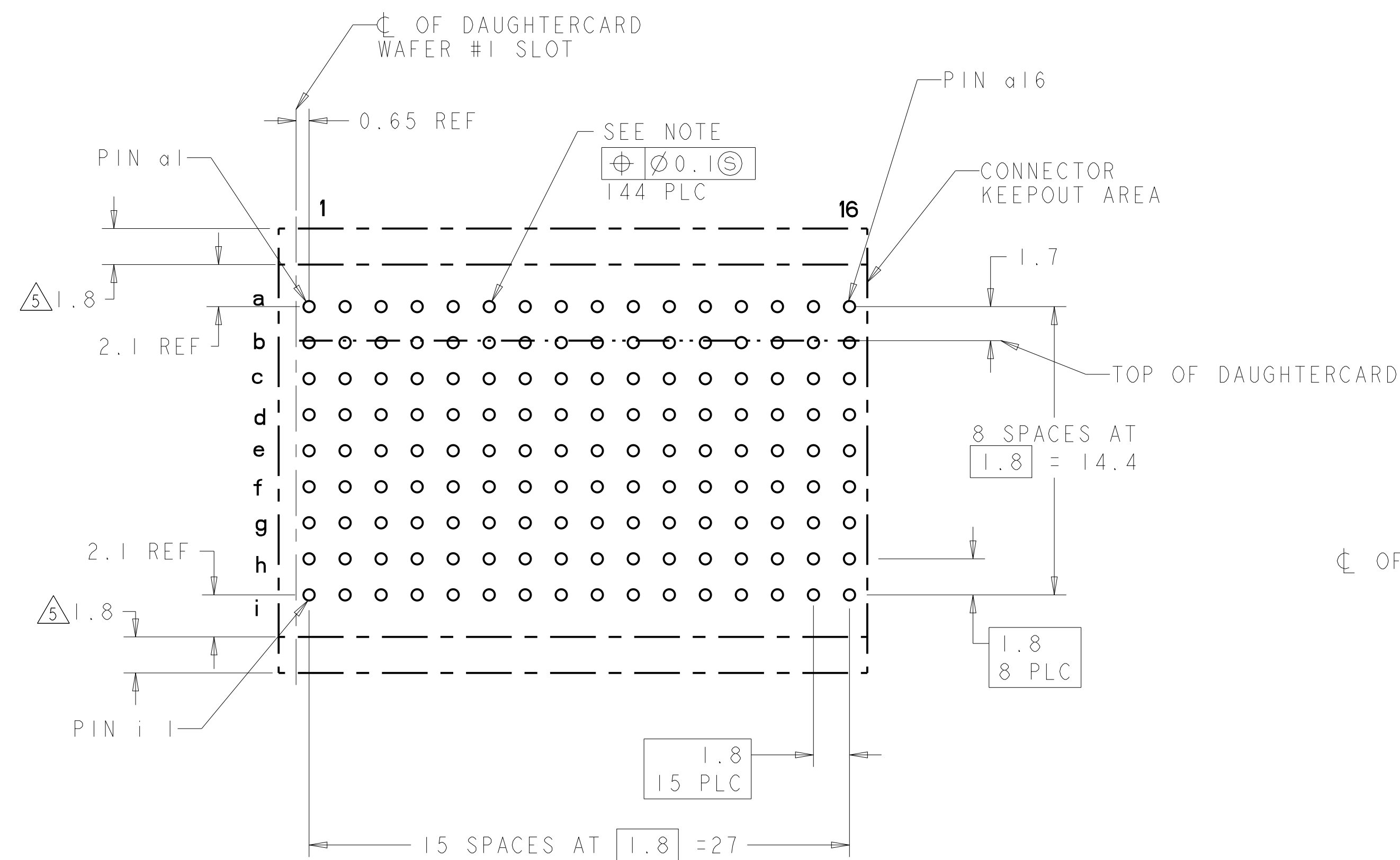
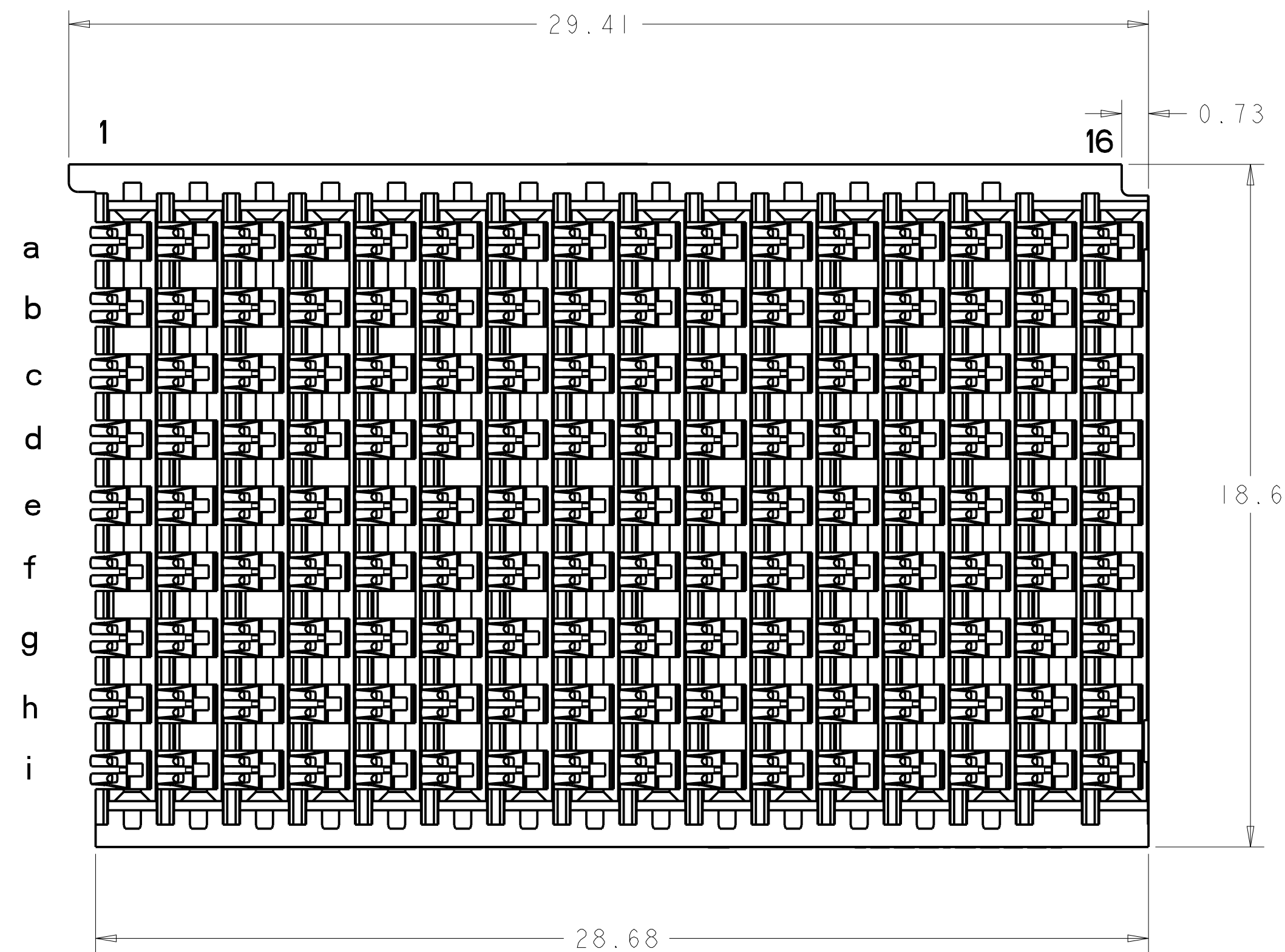
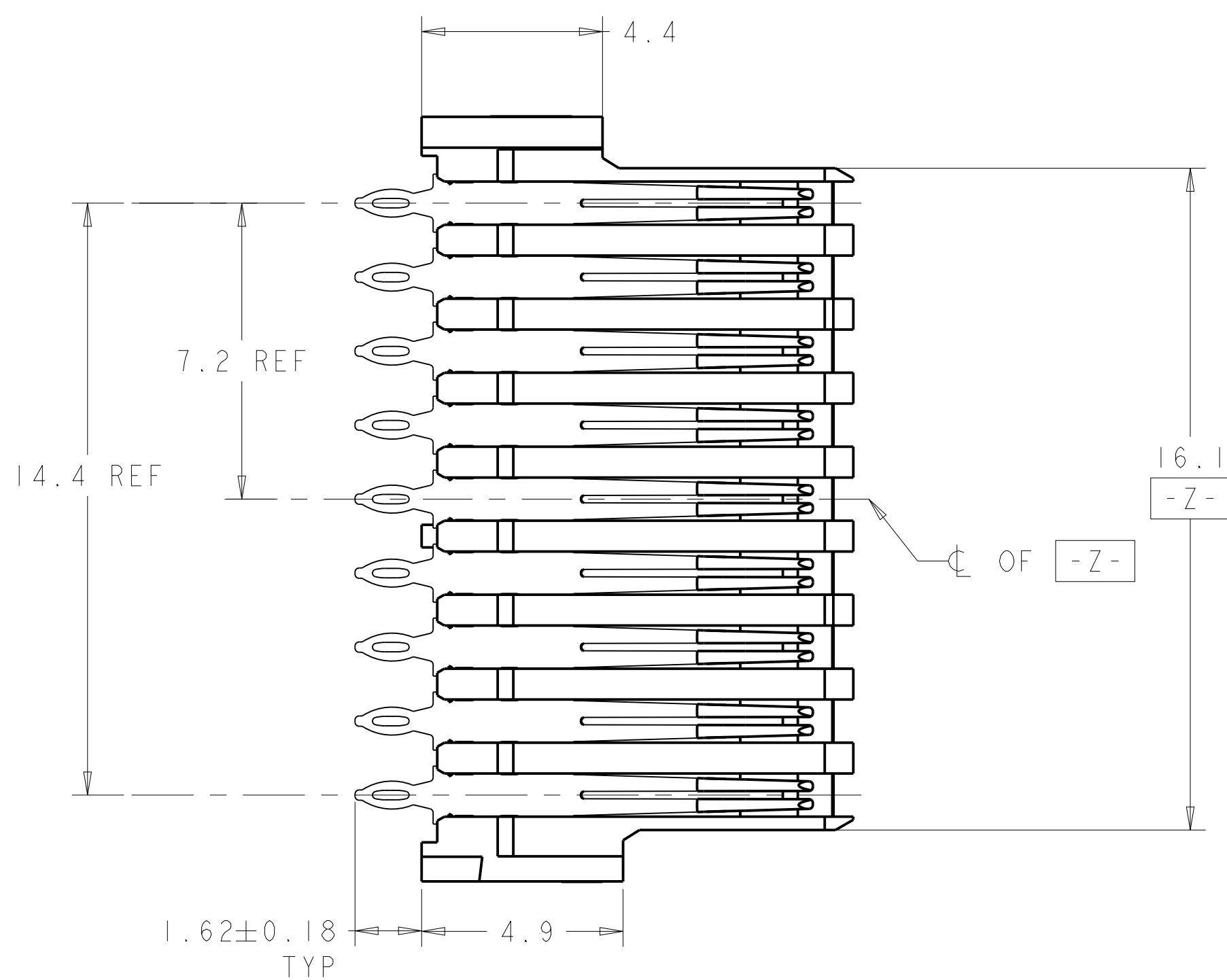
-
- Diagram illustrating the layout of a daughtercard, showing a grid of pins and dimensions.
- Key dimensions and labels:
- Grid dimensions: 16 rows (labeled 1 to 16) and 16 columns (labeled a to p).
 - Pin pitch: 1.8 mm (5 PLG).
 - Reference dimensions: 0.65 REF, 0.89 REF, 1.7 REF, 2.1 REF.
 - Labels: TOP OF DAUGHTERCARD (CONNECTOR SIDE), OF DAUGHTERCARD WAFER #1 SLOT, PIN "a1", PIN "i16", 8 SPACES AT 1.8 = 14.4, 1.8 8 PLG, 1.8 15 PLG, 1.8 144 PLG, Ø SEE NOTE 4, Ø 0.1 (S).



	1410140-2
	1410140-1
PLATING	PART NO

4805 (3/11)

REAR TRANSITION MODULE APPLICATION



RTM PC BOARD LAYOUT
(CONNECTOR SIDE)
SCALE 5:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN R. TREA	08NOV02002	 TE Connectivity
		CHK J. CONSOLI	18NOV02002	
DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME CONNECTOR ASSEMBLY, BACKPLANE VERTICAL RECEPTACLE, CENTER, 20.3mm, MultiGig RT 2
mm		0 PLC	±	
		1 PLC	±0.5	
		2 PLC	±0.13	
		3 PLC	±	PRODUCT SPEC 108-2072 APPLICATION SPEC 114-13056 WEIGHT -
		4 PLC	±	
		5 PLC	±	
		ANGLES	±1°0'	
MATERIAL	FINISH			SIZE A100779
-	-			CASE CODE C1410140
		CUSTOMER DRAWING		DRAWING NO 1410140
				RESTRICTED TO -
				SCALE 2:1
				SHEET 2 OF 2
				REV F