



2SB1124/2SD1624

Bipolar Transistor

(-50V, (-)3A, Low $V_{CE(sat)}$, (PNP)NPN Single PCP

ON Semiconductor®

<http://onsemi.com>

Applications

- Voltage regulators, relay drivers, lamp drivers, electrical equipment

Features

- Adoption of FBET, MBIT processes
- Low collector-to-emitter saturation voltage
- Fast switching speed
- Large current capacity and wide ASO

Specifications () : 2SB1124

Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

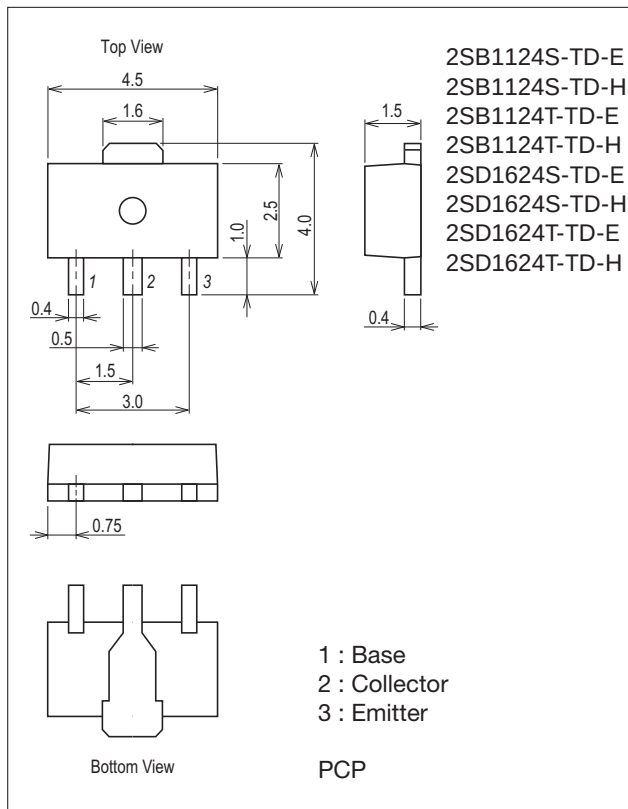
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		(-)60	V
Collector-to-Emitter Voltage	V_{CEO}		(-)50	V
Emitter-to-Base Voltage	V_{EBO}		(-)6	V
Collector Current	I_C		(-)3	A
Collector Current (Pulse)	I_{CP}		(-)6	A

Continued on next page.

Package Dimensions

unit : mm (typ)

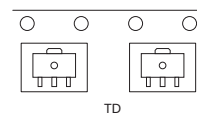
7007B-004



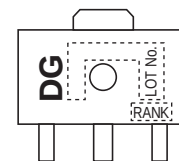
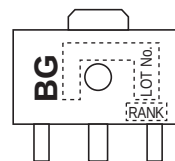
Product & Package Information

- Package : PCP
- JEITA, JEDEC : SC-62, SOT-89, TO-243
- Minimum Packing Quantity : 1,000 pcs./reel

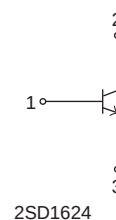
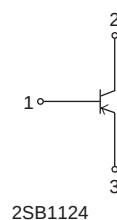
Packing Type: TD



Marking



Electrical Connection



2SB1124 / 2SD1624

Continued from preceding page.

Parameter	Symbol	Conditions	Ratings	Unit
Collector Dissipation	P _C		500	mW
		When mounted on ceramic substrate (250mm ² ×0.8mm)	1.5	W
Junction Temperature	T _j		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

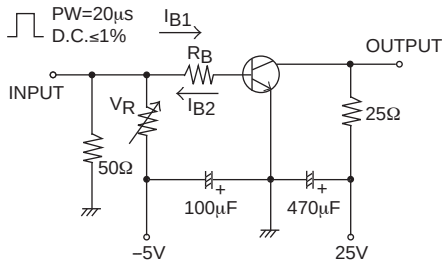
Electrical Characteristics at T_a=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0A			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0A			(-)1	μA
DC Current Gain	h _{FE1}	V _{CE} =(-)2V, I _C =(-)100mA	100*		560*	
	h _{FE2}	V _{CE} =(-)2V, I _C =(-)3A	35			
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)50mA		150		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(39)25		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)2A, I _B =(-)100mA		(-0.35)0.19	(-0.7)0.5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	V _{CE} =(-)2A, I _C =(-)100mA		(-)0.94	(-)1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C =(-)10μA, I _E =0A	(-)60			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =(-)1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μA, I _C =0A	(-)6			V
Turn-ON Time	t _{on}	See specified Test Circuit.		(70)70		ns
Storage Time	t _{stg}			(450)650		ns
Fall Time	t _f			(35)35		ns

* ; The 2SB1124/2SD1624 are classified by 100mA h_{FE} as follows :

Rank	R	S	T	U
h _{FE}	100 to 200	140 to 280	200 to 400	280 to 560

Switching Time Test Circuit

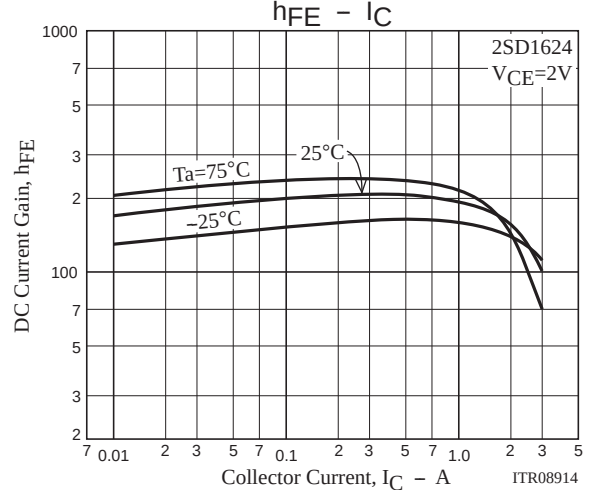
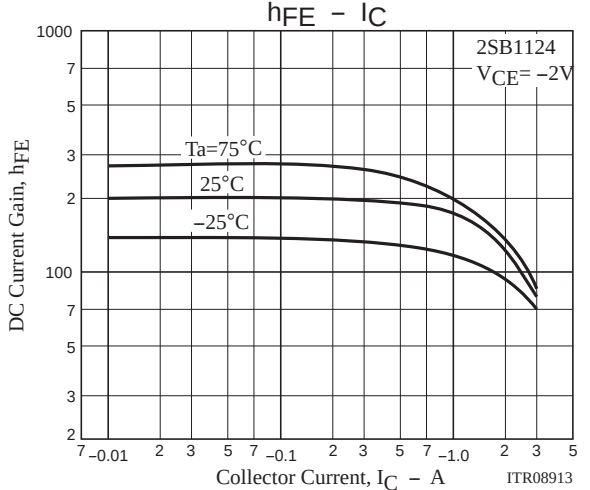
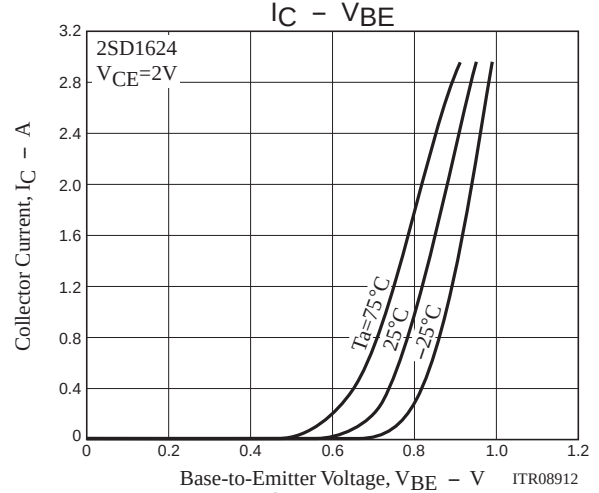
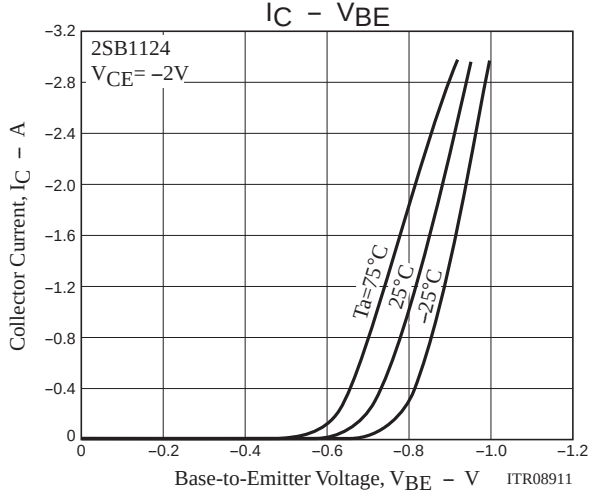
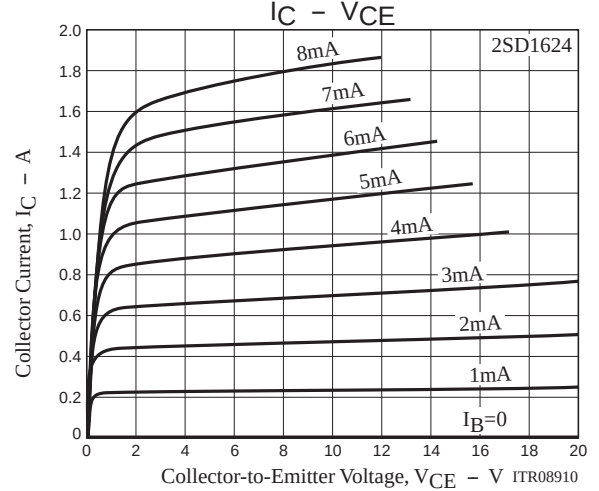
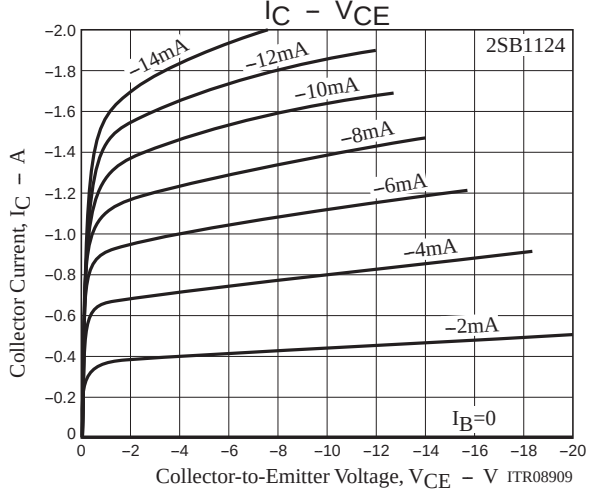
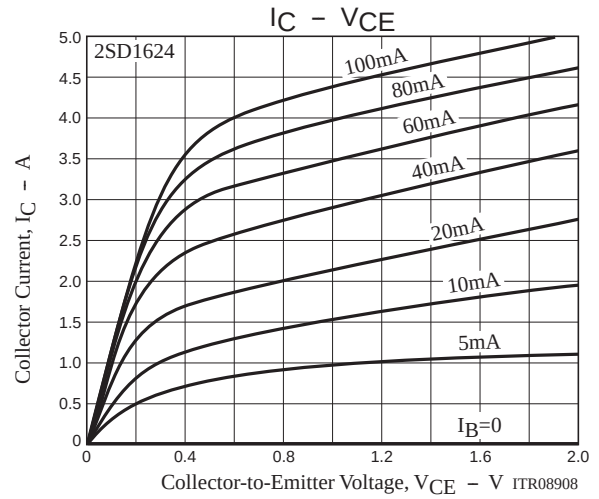
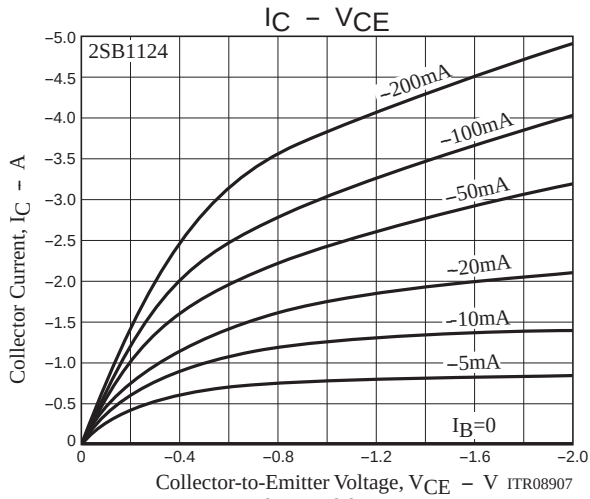


$$I_C = 10I_{B1} = -10I_{B2} = 1A$$

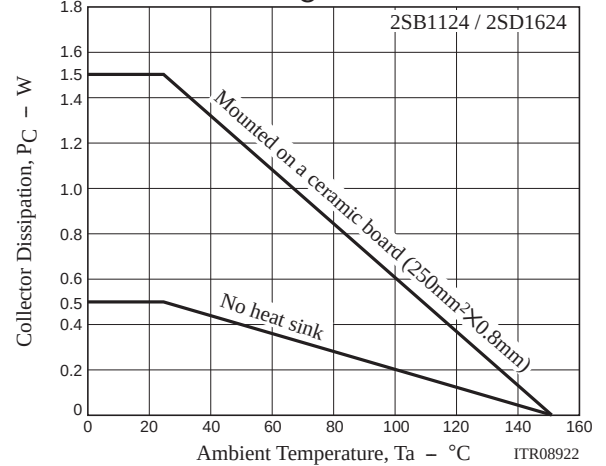
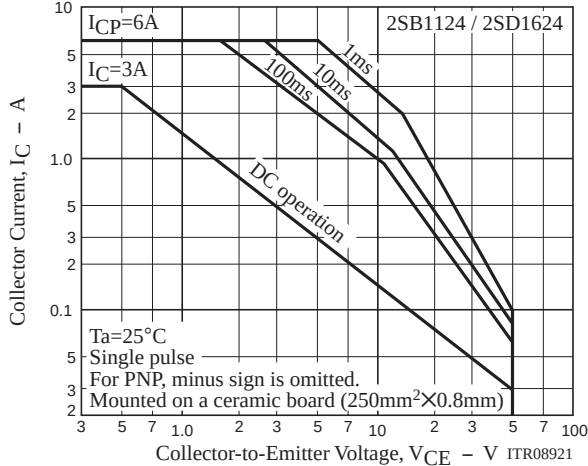
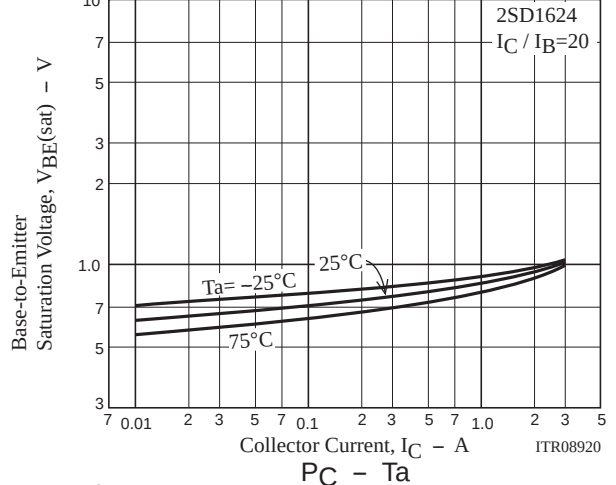
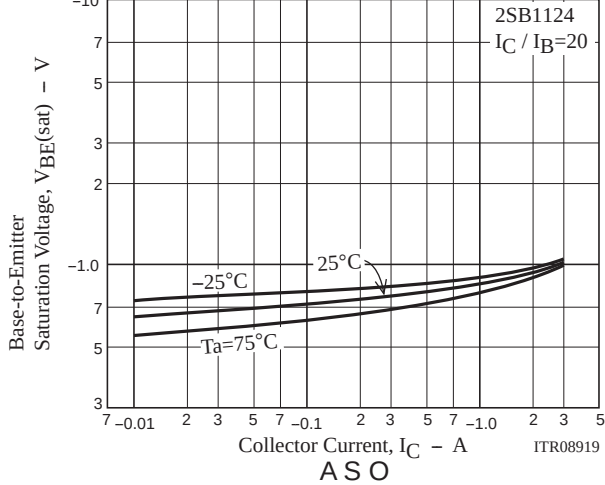
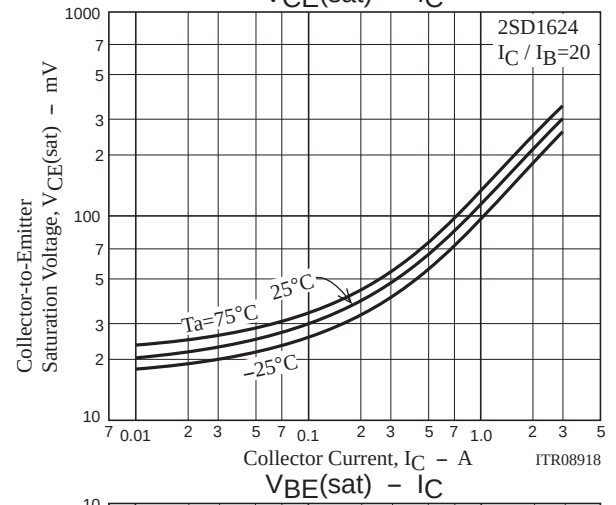
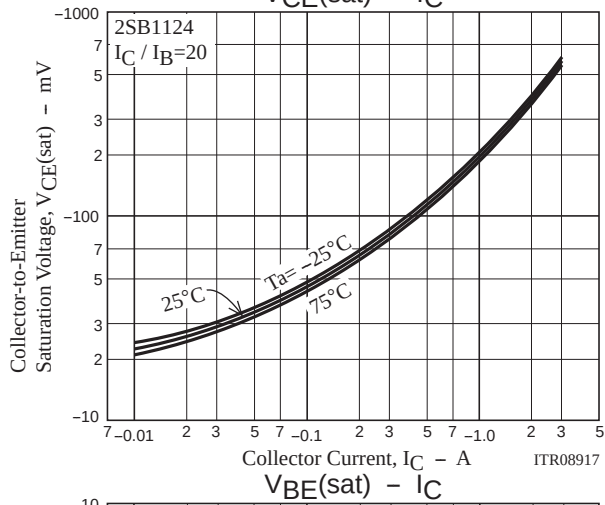
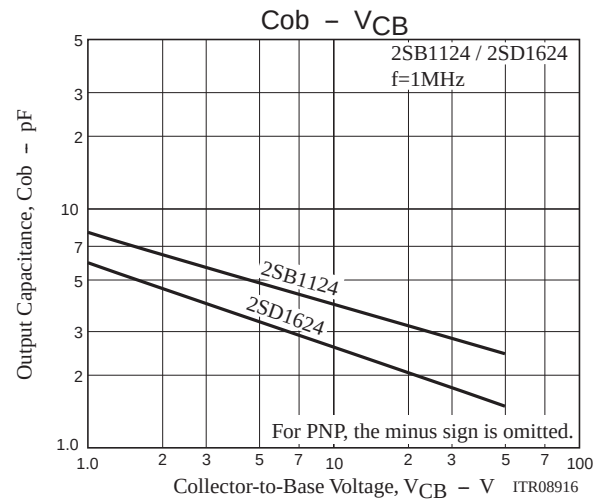
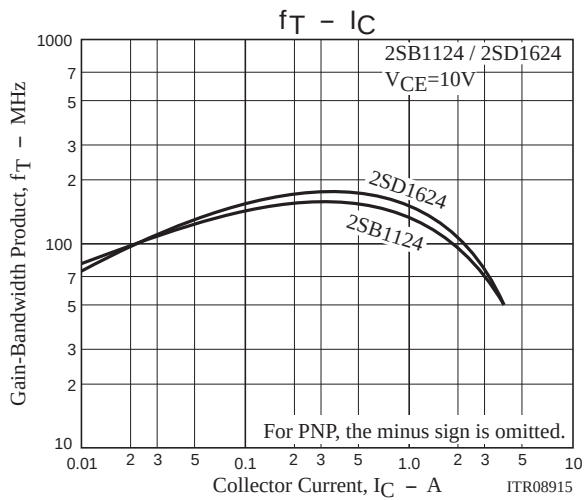
For PNP, the polarity is reversed.

Ordering Information

Device	Package	Shipping	memo
2SB1124S-TD-E	PCP	1,00pcs./reel	Pb Free
2SB1124S-TD-H	PCP	1,00pcs./reel	Pb Free and Halogen Free
2SB1124T-TD-E	PCP	1,00pcs./reel	Pb Free
2SB1124T-TD-H	PCP	1,00pcs./reel	Pb Free and Halogen Free
2SD1624S-TD-E	PCP	1,00pcs./reel	Pb Free
2SD1624S-TD-H	PCP	1,00pcs./reel	Pb Free and Halogen Free
2SD1624T-TD-E	PCP	1,00pcs./reel	Pb Free
2SD1624T-TD-H	PCP	1,00pcs./reel	Pb Free and Halogen Free



2SB1124 / 2SD1624



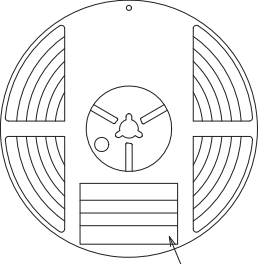
Bag Packing Specification

2SB1124S-TD-E, 2SB1124S-TD-H, 2SB1124T-TD-E, 2SB1124T-TD-H, 2SD1624S-TD-E, 2SD1624S-TD-H, 2SD1624T-TD-E, 2SD1624T-TD-H

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
PCP	PCP	1,000	4,000	24,000	4 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Reel label

Reel label, Inner box label
(unit:mm)

69

43

80

Type No. → (P) TYPE 000000000

LOT No. → (1) LOT 00

Quantity → (Q) QTY 0,000 (1) LEAD FREE *

Origin → (Z) SPECIAL
Z0722005310C
ASSEMBLY:*** (DIFFUSION:***)

Outer box label

108

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

TYPE CODE

000000000000

TYPE 00000000

QTY 0,000 PCS (1) LEAD FREE *

LOT 00000000

PACKAGE 00000000

SPECIAL

Z0722005310C
ASSEMBLY:*** (DIFFUSION:***)

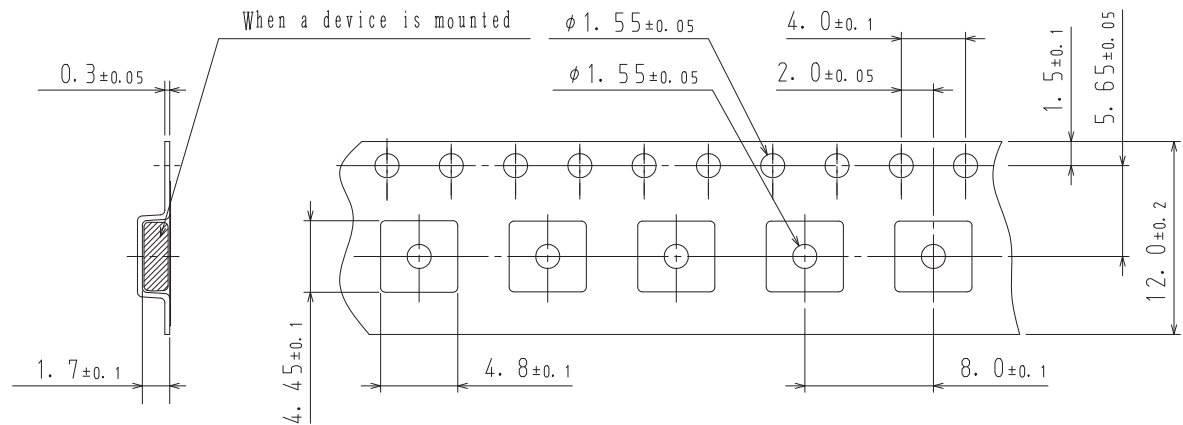
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

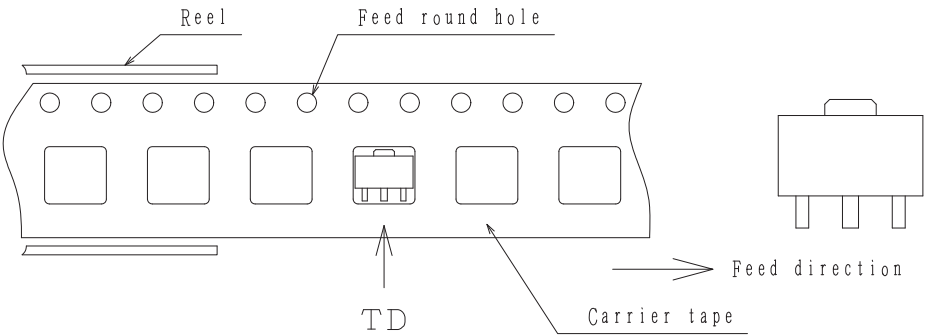
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



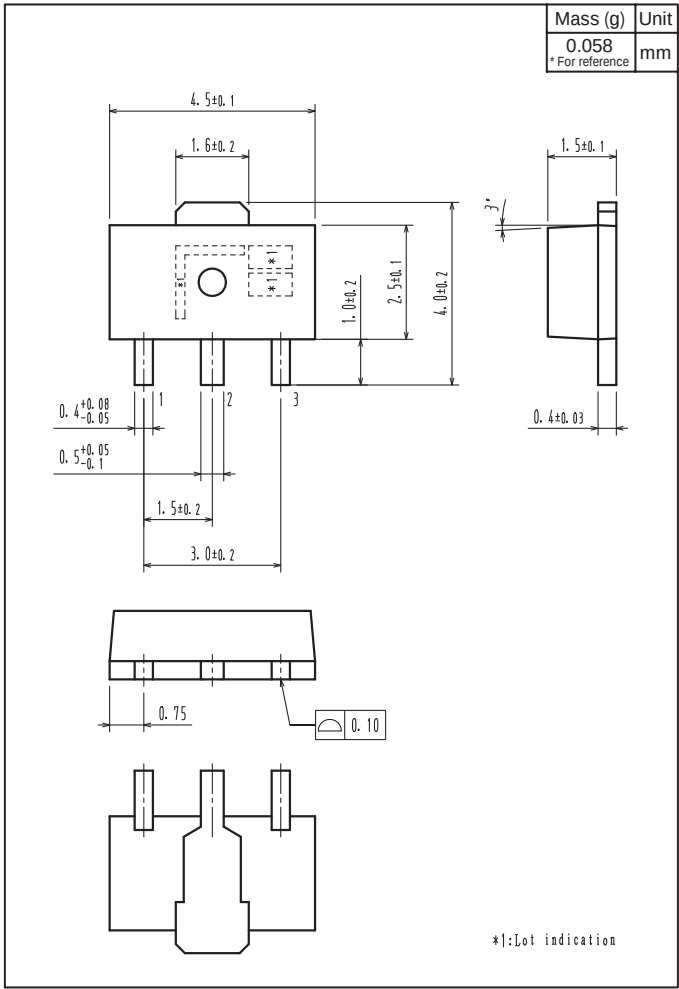
2-2. Device placement direction



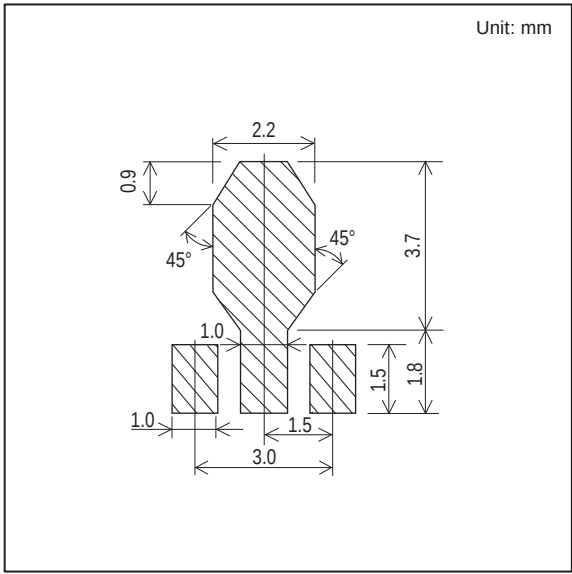
Those with pin 1 index on the feed hole side.....TD

Outline Drawing

2SB1124S-TD-E, 2SB1124S-TD-H, 2SB1124T-TD-E, 2SB1124T-TD-H, 2SD1624S-TD-E, 2SD1624S-TD-H, 2SD1624T-TD-E, 2SD1624T-TD-H



Land Pattern Example



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.