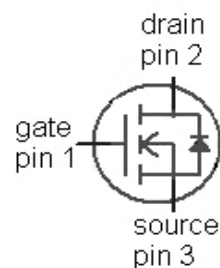


OptiMOS™3 Power-Transistor
Features
Package
Marking

- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹⁾ for target application
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free according to IEC61249-2-21


Halogen-Free

Type	IPA075N15N3 G
	
Package	PG-TO220-3
Marking	075N15N


Maximum ratings, at $T_j=25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$T_C=25\text{ °C}$	43	A
		$T_C=100\text{ °C}$	31	
Pulsed drain current ²⁾	$I_{D,pulse}$	$T_C=25\text{ °C}$	172	
Avalanche energy, single pulse	E_{AS}	$I_D=43\text{ A}$, $R_{GS}=25\text{ }\Omega$	1000	mJ
Gate source voltage	V_{GS}		± 20	V
Power dissipation	P_{tot}	$T_C=25\text{ °C}$	39	W
Operating and storage temperature	T_j , T_{stg}		-55 ... 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

¹⁾J-STD20 and JESD22

²⁾ See figure 3

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Thermal characteristics

Thermal resistance, junction - case	R_{thJC}		-	-	3.8	K/W
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Electrical characteristics, at $T_j=25\text{ °C}$, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{ V}, I_D=1\text{ mA}$	150	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=270\text{ }\mu\text{A}$	2	3	4	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=120\text{ V}, V_{GS}=0\text{ V}, T_j=25\text{ °C}$	-	0.1	1	μA
		$V_{DS}=120\text{ V}, V_{GS}=0\text{ V}, T_j=125\text{ °C}$	-	10	100	
Gate-source leakage current	I_{GSS}	$V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$	-	1	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{ V}, I_D=43\text{ A}$	-	5.9	7.5	$\text{m}\Omega$
		$V_{GS}=8\text{ V}, I_D=22\text{ A}$	-	6.2	7.9	
Gate resistance	R_G		-	2.3	-	Ω
Transconductance	g_{fs}	$ V_{DS} >2 I_D R_{DS(on)max}, I_D=43\text{ A}$	45	89	-	S

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Dynamic characteristics

Input capacitance	C_{iss}	$V_{GS}=0\text{ V}, V_{DS}=75\text{ V},$ $f=1\text{ MHz}$	-	5470	7280	pF
Output capacitance	C_{oss}		-	638	849	
Reverse transfer capacitance	C_{rss}		-	10	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=75\text{ V}, V_{GS}=10\text{ V},$ $I_D=43\text{ A}, R_G=1.6\ \Omega$	-	22	-	ns
Rise time	t_r		-	25	-	
Turn-off delay time	$t_{d(off)}$		-	50	-	
Fall time	t_f		-	15	-	

Gate Charge Characteristics⁴⁾

Gate to source charge	Q_{gs}	$V_{DD}=75\text{ V}, I_D=43\text{ A},$ $V_{GS}=0\text{ to }10\text{ V}$	-	27	-	nC
Gate to drain charge	Q_{gd}		-	11	-	
Switching charge	Q_{sw}		-	22	-	
Gate charge total	Q_g		-	70	93	
Gate plateau voltage	$V_{plateau}$		-	5	-	V
Output charge	Q_{oss}	$V_{DD}=75\text{ V}, V_{GS}=0\text{ V}$	-	179	239	nC

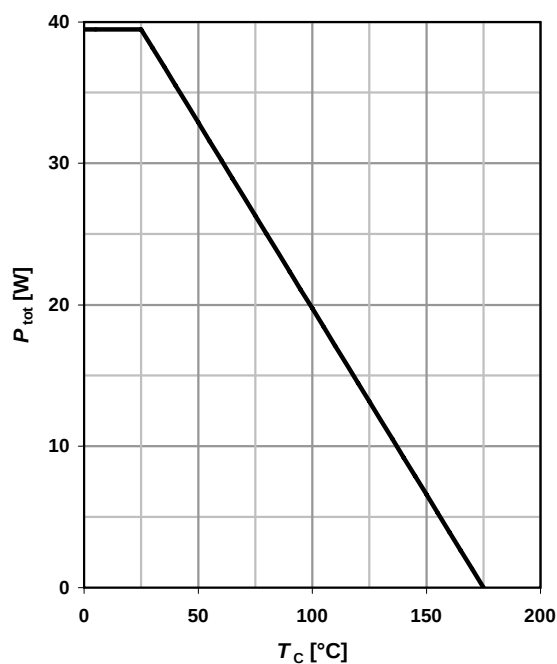
Reverse Diode

Diode continuous forward current	I_S	$T_C=25\text{ °C}$	-	-	43	A
Diode pulse current	$I_{S,pulse}$		-	-	172	
Diode forward voltage	V_{SD}	$V_{GS}=0\text{ V}, I_F=43\text{ A},$ $T_J=25\text{ °C}$	-	0.9	1.2	V
Reverse recovery time	t_{rr}	$V_R=75\text{ V}, I_F=I_S,$ $di_F/dt=100\text{ A}/\mu\text{s}$	-	144	-	ns
Reverse recovery charge	Q_{rr}		-	496	-	nC

⁴⁾ See figure 16 for gate charge parameter definition

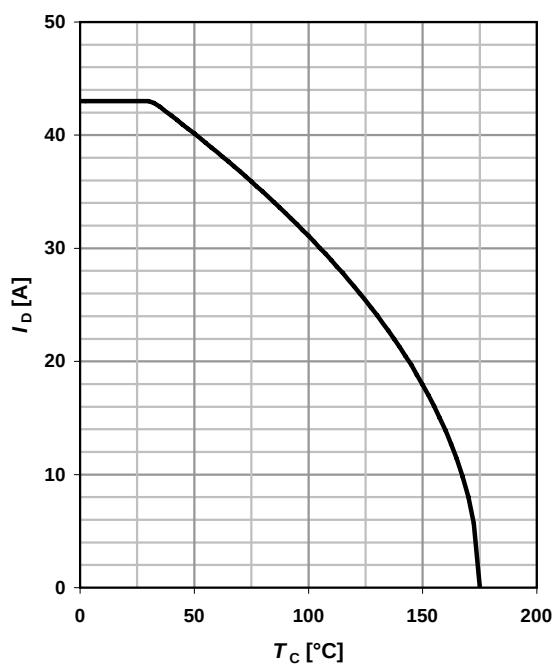
1 Power dissipation

$$P_{\text{tot}} = f(T_C)$$



2 Drain current

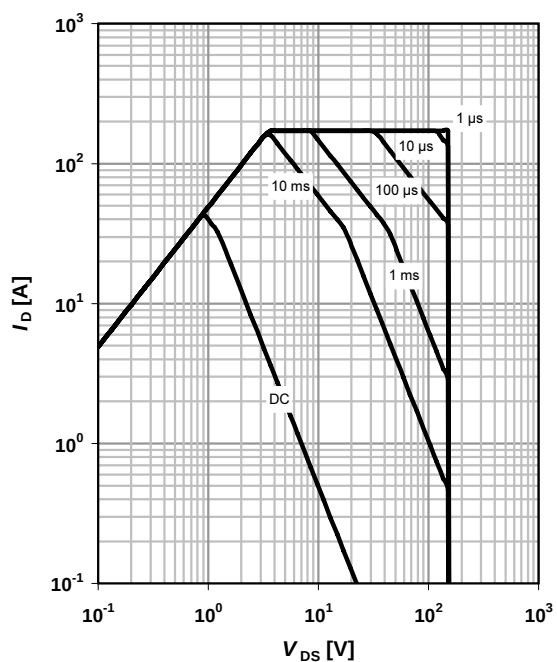
$$I_D = f(T_C); V_{GS} \geq 10 \text{ V}$$



3 Safe operating area

$$I_D = f(V_{DS}); T_C = 25 \text{ °C}; D = 0$$

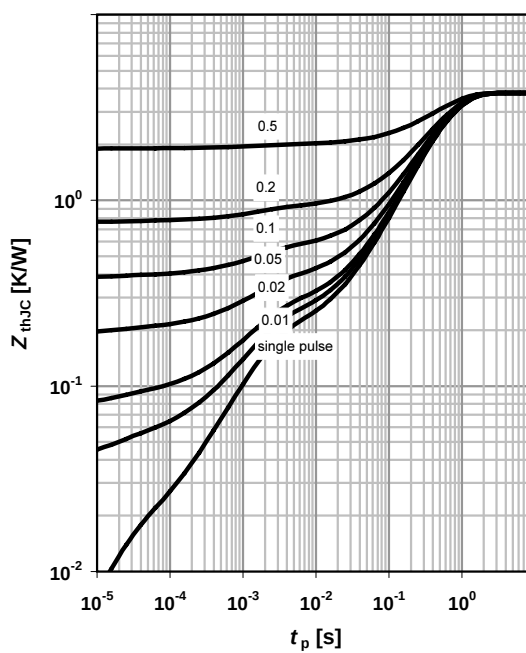
parameter: t_p



4 Max. transient thermal impedance

$$Z_{\text{thJC}} = f(t_p)$$

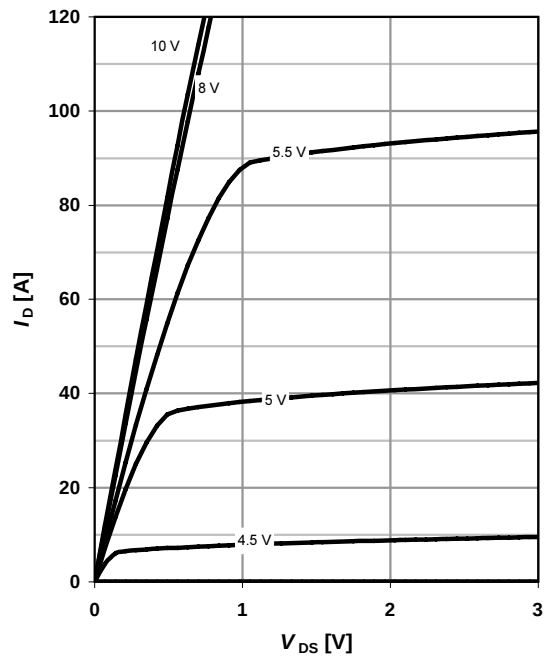
parameter: $D = t_p/T$



5 Typ. output characteristics

$$I_D = f(V_{DS}); T_J = 25^\circ\text{C}$$

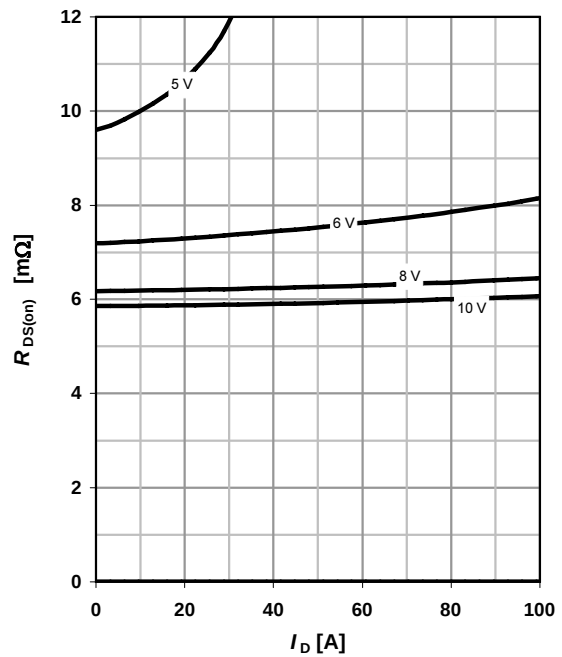
parameter: V_{GS}



6 Typ. drain-source on resistance

$$R_{DS(on)} = f(I_D); T_J = 25^\circ\text{C}$$

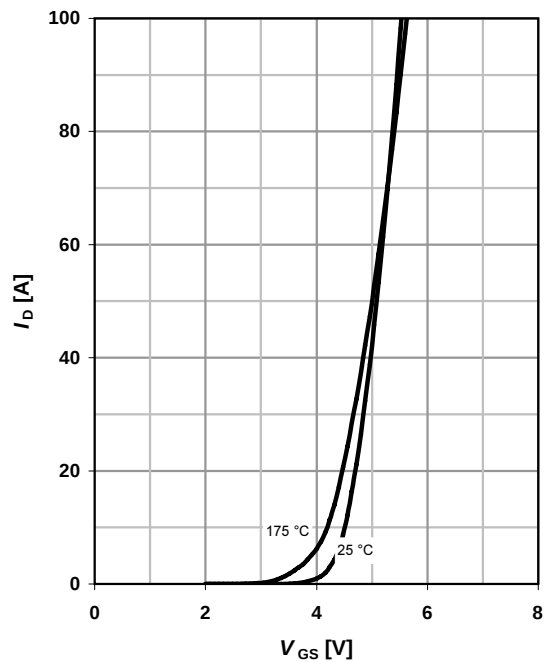
parameter: V_{GS}



7 Typ. transfer characteristics

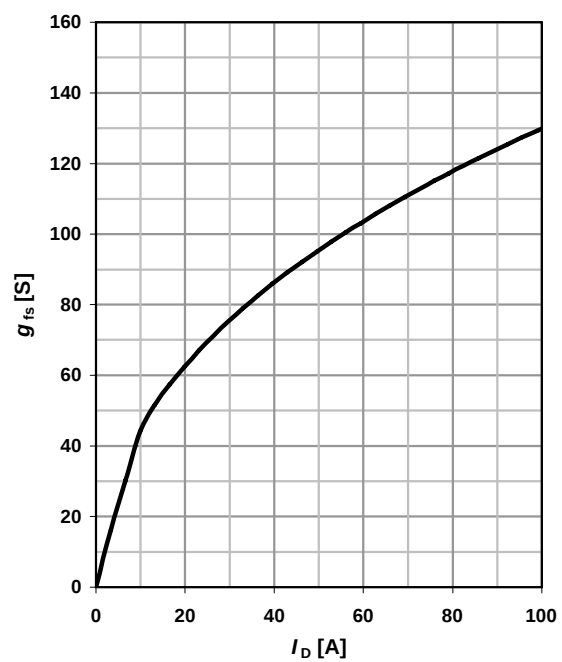
$$I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$$

parameter: T_J



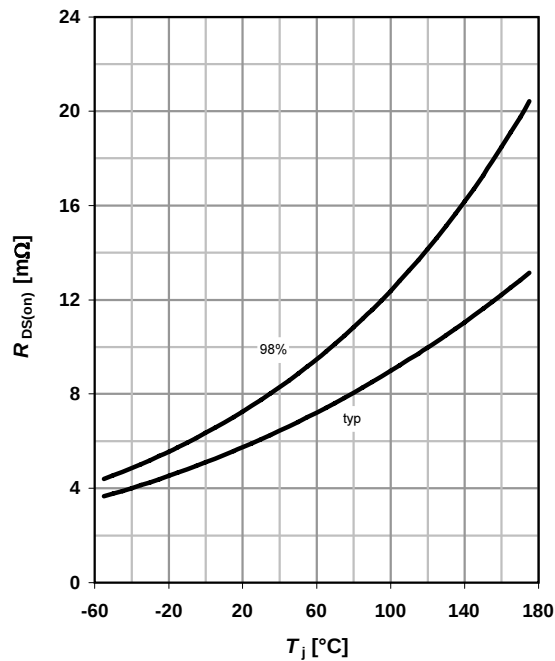
8 Typ. forward transconductance

$$g_{fs} = f(I_D); T_J = 25^\circ\text{C}$$



9 Drain-source on-state resistance

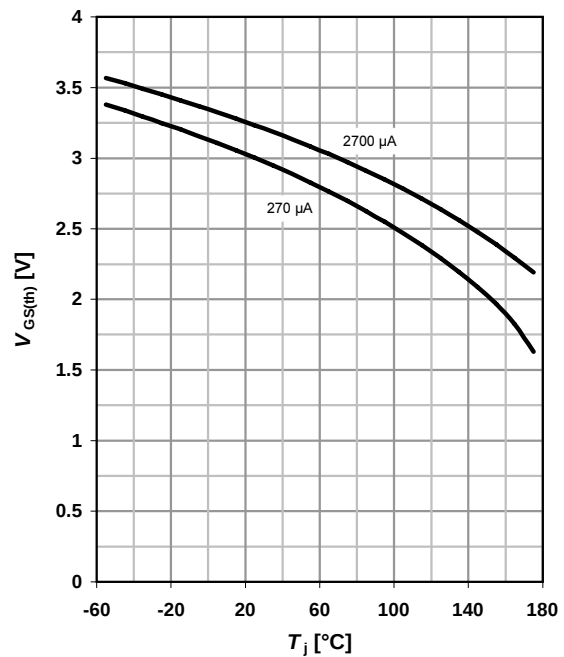
$$R_{DS(on)} = f(T_j); I_D = 43 \text{ A}; V_{GS} = 10 \text{ V}$$



10 Typ. gate threshold voltage

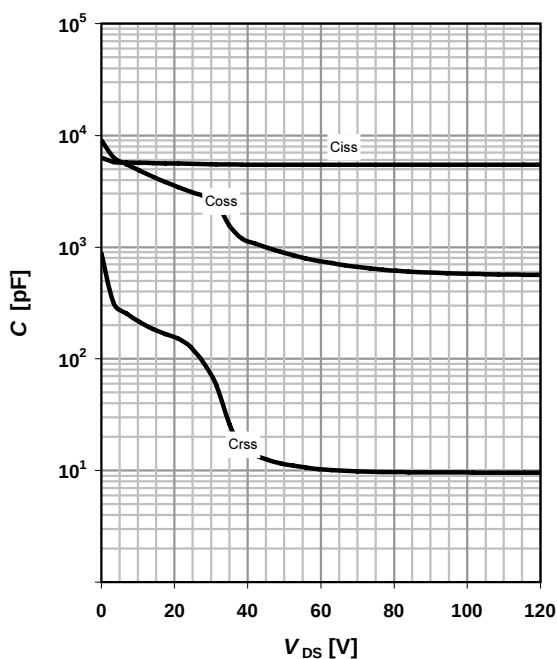
$$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$$

parameter: I_D



11 Typ. capacitances

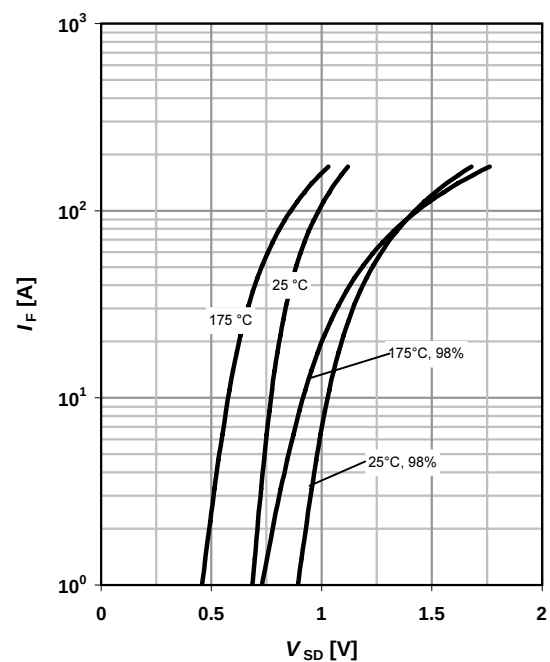
$$C = f(V_{DS}); V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}$$



12 Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

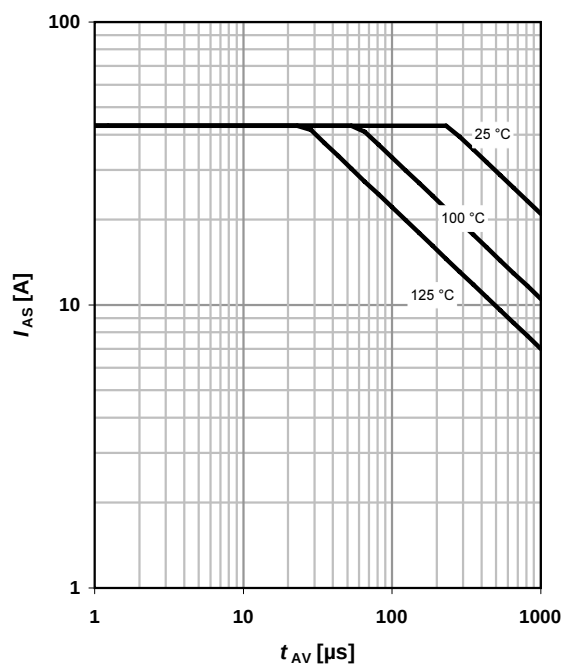
parameter: T_j



13 Avalanche characteristics

$$I_{AS}=f(t_{AV}); R_{GS}=25\ \Omega$$

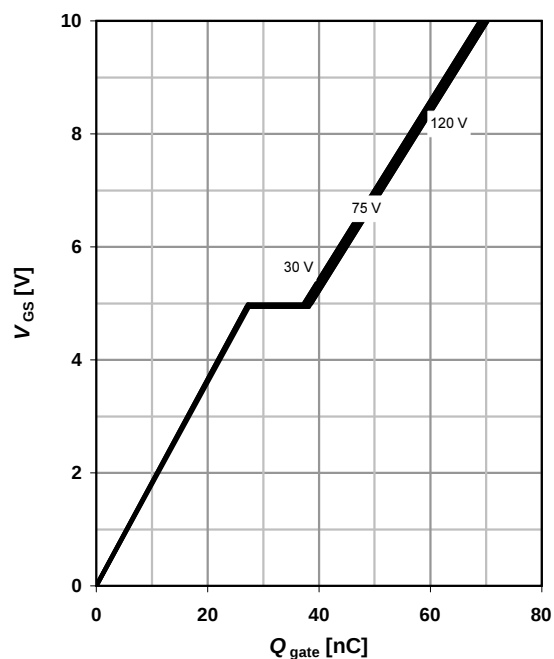
parameter: $T_{j(\text{start})}$



14 Typ. gate charge

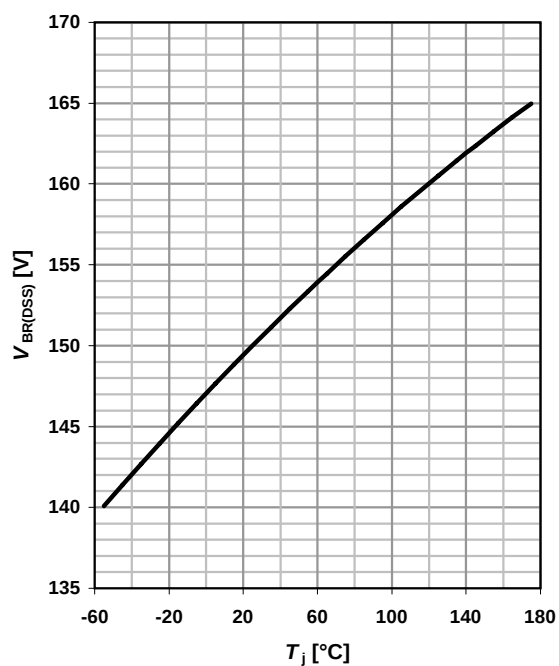
$$V_{GS}=f(Q_{\text{gate}}); I_D=43\ \text{A pulsed}$$

parameter: V_{DD}

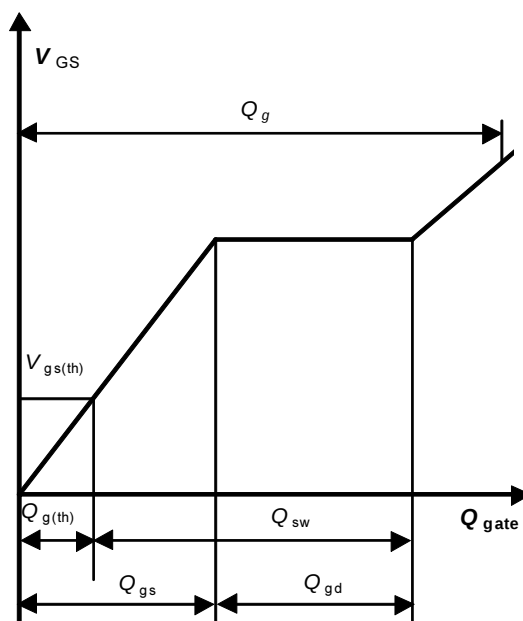


15 Drain-source breakdown voltage

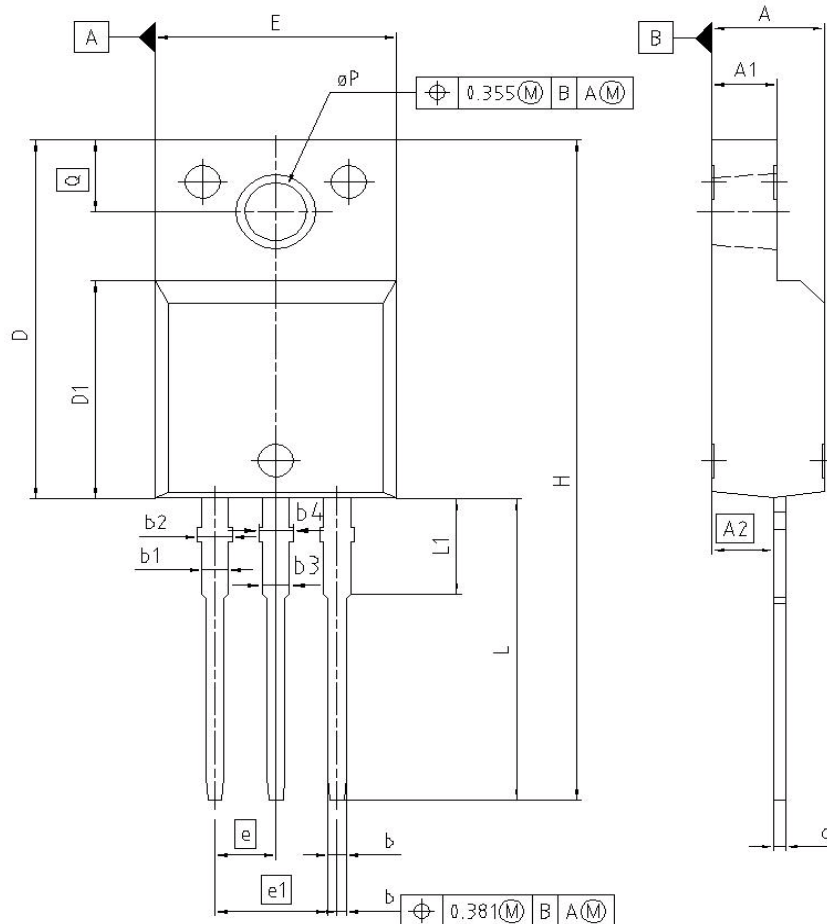
$$V_{BR(DSS)}=f(T_j); I_D=1\ \text{mA}$$



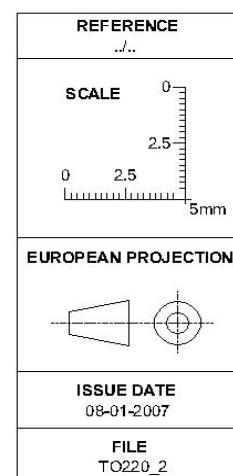
16 Gate charge waveforms



PG-TO220-FP: Outline



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.55	4.85	0.179	0.191
A1	2.55	2.85	0.100	0.112
A2	2.42	2.72	0.095	0.107
b	0.65	0.85	0.026	0.033
b1	0.95	1.33	0.037	0.052
b2	0.95	1.51	0.037	0.059
b3	0.65	1.33	0.026	0.052
b4	0.65	1.51	0.026	0.059
c	0.40	0.63	0.016	0.025
D	15.85	16.15	0.624	0.636
D1	9.53	9.83	0.375	0.387
E	10.35	10.65	0.407	0.419
e	2.54		0.100	
e1	5.08		0.200	
N	3		3	
H	29.45	29.75	1.159	1.171
L	13.45	13.75	0.530	0.541
L1	3.15	3.45	0.124	0.136
P	2.95	3.20	0.116	0.126
Q	3.15	3.50	0.124	0.138



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