

# 1.8V Drive Nch+Nch MOSFET

## US6K4

### ●Structure

Silicon N-channel MOSFET

### ●Features

- 1) Two Nch MOSFETs are put in TUMT6 package.
- 2) High-speed switching, Low On-resistance.
- 3) 1.8V drive.

### ●Applications

Switching

### ●Packaging specifications

| Type  | Package                      | Taping |
|-------|------------------------------|--------|
|       | Code                         | TR     |
| US6K4 | Basic ordering unit (pieces) | 3000   |
|       |                              | ○      |

### ●Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2>

| Parameter                      |            | Symbol      | Limits      | Unit        |
|--------------------------------|------------|-------------|-------------|-------------|
| Drain-source voltage           |            | $V_{DS}$    | 20          | V           |
| Gate-source voltage            |            | $V_{GS}$    | $\pm 10$    | V           |
| Drain current                  | Continuous | $I_D$       | $\pm 1.5$   | A           |
|                                | Pulsed     | $I_{DP}$ *1 | $\pm 3.0$   | A           |
| Source current<br>(Body diode) | Continuous | $I_S$       | 0.6         | A           |
|                                | Pulsed     | $I_{SP}$ *1 | 2.4         | A           |
| Total power dissipation        |            | $P_D$ *2    | 1.0         | W / TOTAL   |
|                                |            |             | 0.7         | W / ELEMENT |
| Channel temperature            |            | $T_{ch}$    | 150         | °C          |
| Range of storage temperature   |            | $T_{stg}$   | -55 to +150 | °C          |

\*1  $P_w \leq 10\mu s$ , Duty cycle  $\leq 1\%$

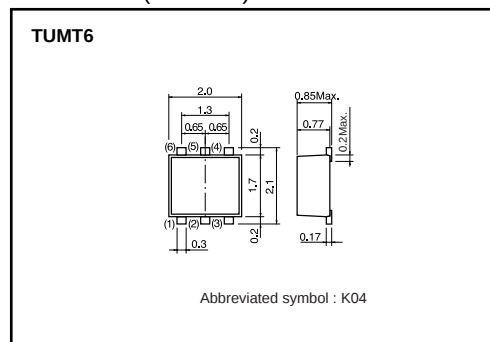
\*2 Mounted on a ceramic board

### ●Thermal resistance

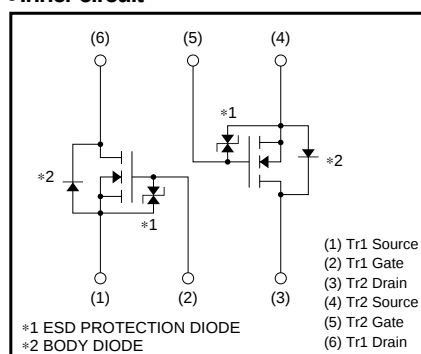
| Parameter          | Symbol           | Limits | Unit           |
|--------------------|------------------|--------|----------------|
| Channel to ambient | $R_{th(ch-a)}$ * | 125    | °C/W / TOTAL   |
|                    |                  | 179    | °C/W / ELEMENT |

\* Mounted on a ceramic board

### ●Dimensions (Unit : mm)



### ●Inner circuit



## Transistors

## ●Electrical characteristics (Ta=25°C)

&lt;It is the same characteristics for the Tr1 and Tr2&gt;

| Parameter                               | Symbol         | Min. | Typ. | Max. | Unit | Conditions                  |
|-----------------------------------------|----------------|------|------|------|------|-----------------------------|
| Gate-source leakage                     | $I_{GSS}$      | —    | —    | ±10  | μA   | $V_{GS}=±10V$ , $V_{DS}=0V$ |
| Drain-source breakdown voltage          | $V_{(BR) DSS}$ | 20   | —    | —    | V    | $I_D=1mA$ , $V_{GS}=0V$     |
| Zero gate voltage drain current         | $I_{DSS}$      | —    | —    | 1    | μA   | $V_{DS}=20V$ , $V_{GS}=0V$  |
| Gate threshold voltage                  | $V_{GS(th)}$   | 0.3  | —    | 1.0  | V    | $V_{DS}=10V$ , $I_D=1mA$    |
| Static drain-source on-state resistance | $R_{DS(on)}$ * | —    | 130  | 180  | mΩ   | $I_D=1.5A$ , $V_{GS}=4.5V$  |
|                                         |                | —    | 170  | 240  | mΩ   | $I_D=1.5A$ , $V_{GS}=2.5V$  |
|                                         |                | —    | 220  | 310  | mΩ   | $I_D=0.8A$ , $V_{GS}=1.8V$  |
| Forward transfer admittance             | $ Y_{fs} $ *   | 1.6  | —    | —    | S    | $V_{DS}=10V$ , $I_D=1.5A$   |
| Input capacitance                       | $C_{iss}$      | —    | 110  | —    | pF   | $V_{DS}=10V$                |
| Output capacitance                      | $C_{oss}$      | —    | 18   | —    | pF   | $V_{GS}=0V$                 |
| Reverse transfer capacitance            | $C_{rss}$      | —    | 15   | —    | pF   | $f=1MHz$                    |
| Turn-on delay time                      | $t_{d(on)}$ *  | —    | 5    | —    | ns   | $I_D=1.0A$                  |
| Rise time                               | $t_r$ *        | —    | 5    | —    | ns   | $V_{DD}=10V$                |
| Turn-off delay time                     | $t_{d(off)}$ * | —    | 20   | —    | ns   | $V_{GS}=4.5V$               |
| Fall time                               | $t_f$ *        | —    | 3    | —    | ns   | $R_L=10Ω$                   |
| Total gate charge                       | $Q_g$ *        | —    | 1.8  | 2.5  | nC   | $V_{DD}=10V$                |
| Gate-source charge                      | $Q_{gs}$ *     | —    | 0.3  | —    | nC   | $V_{GS}=4.5V$               |
| Gate-drain charge                       | $Q_{gd}$ *     | —    | 0.3  | —    | nC   | $I_D=1.5A$                  |

\*Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

&lt;It is the same characteristics for the Tr1 and Tr2&gt;

| Parameter       | Symbol   | Min. | Typ. | Max. | Unit | Conditions               |
|-----------------|----------|------|------|------|------|--------------------------|
| Forward voltage | $V_{SD}$ | —    | —    | 1.2  | V    | $I_S=0.6A$ , $V_{GS}=0V$ |

## Transistors

## ●Electrical characteristics curves

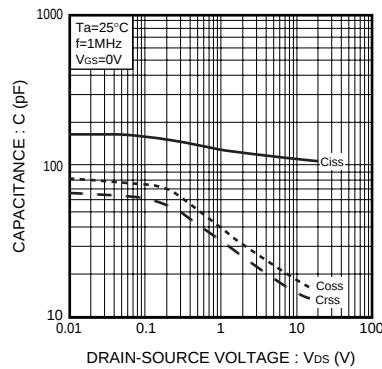


Fig.1 Typical Capacitance vs. Drain-Source Voltage

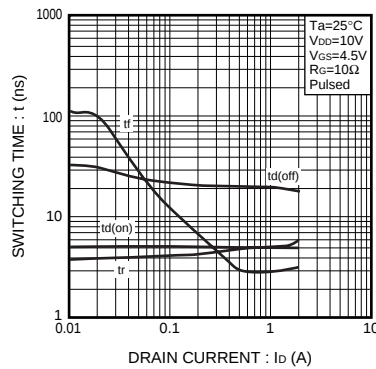


Fig.2 Switching Characteristics

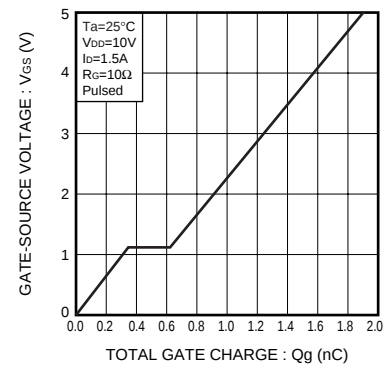


Fig.3 Dynamic Input Characteristics

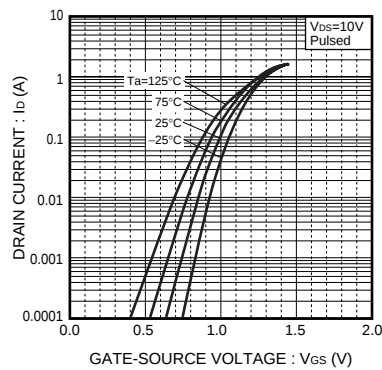


Fig.4 Typical Transfer Characteristics

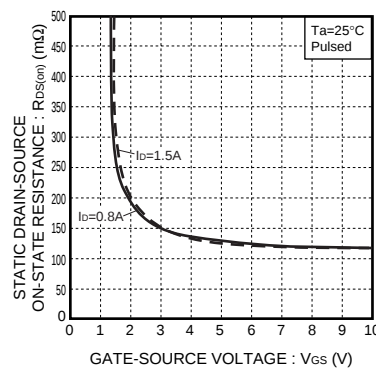


Fig.5 Static Drain-Source On-State Resistance vs. Gate-source Voltage

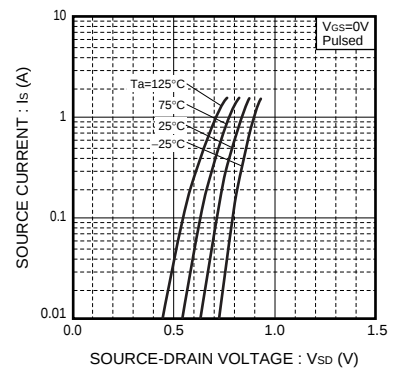


Fig.6 Source Current vs. Source-Drain Voltage

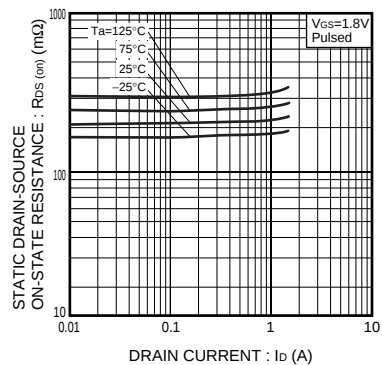


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current ( I )

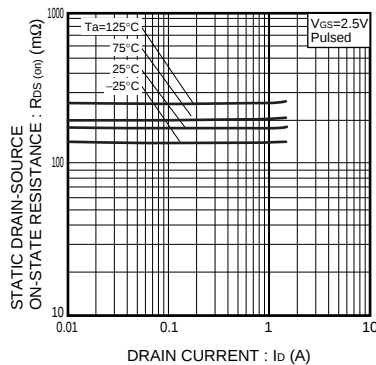


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current ( II )

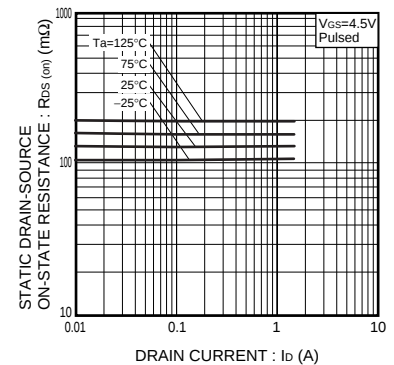


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current ( III )

## ●Notice

This product might cause chip aging and breakdown under the large electrified environment.  
Please consider to design ESD protection circuit.

### Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the product described in this document are for reference only. Upon actual use, therefore, please request that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or otherwise dispose of the same, no express or implied right or license to practice or commercially exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).

Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

It is our top priority to supply products with the utmost quality and reliability. However, there is always a chance of failure due to unexpected factors. Therefore, please take into account the derating characteristics and allow for sufficient safety features, such as extra margin, anti-flammability, and fail-safe measures when designing in order to prevent possible accidents that may result in bodily harm or fire caused by component failure. ROHM cannot be held responsible for any damages arising from the use of the products under conditions out of the range of the specifications or due to non-compliance with the NOTES specified in this catalog.

Thank you for your accessing to ROHM product informations.

More detail product informations and catalogs are available, please contact your nearest sales office.

**ROHM** Customer Support System

THE AMERICAS / EUROPE / ASIA / JAPAN

[www.rohm.com](http://www.rohm.com)

Contact us : [webmaster@rohm.co.jp](mailto:webmaster@rohm.co.jp)