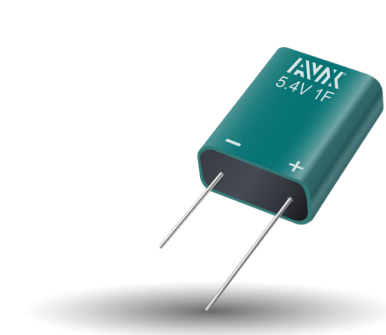


High Reliability SCM Series

Series-Connected SuperCapacitor Modules



This new series of plastic, epoxy-filled SuperCapacitor modules feature high reliability when used in elevated temperatures and/or high humidity conditions. In addition to moisture resistance features, these SuperCapacitor modules offer excellent pulse power handling characteristics based on the combination of very high capacitance and very low ESR. Degradation of electrical characteristics under normal conditions are lengthened in large part to the special plastic, epoxy-filled packaging technology of these SuperCapacitor modules. Used by themselves or in conjunction with primary or secondary batteries, they provide extended back up time, longer battery life, and provide instantaneous power pulses as needed. These modules offer great solutions to hold up, energy harvesting, pulse power applications, and battery replacement.

FEATURES

- High Pulse Power Capability
- Low ESR
- Low Leakage Current
- Plastic, Moisture Resistant
- High Reliability

APPLICATIONS

- Smart/Remote Metering
- Telemetry
- Hybrid Battery Packs
- Scanners
- Environmental Controls
- Network Power Hold-Up
- Pulse Power Handling
- Solid State Drives
- UPS/Industrial
- Energy Harvesting

HOW TO ORDER

SCM	R	14	C	474	P	S	B	A	0	H
Series SuperCap Module	Diameter R = 9.5mm	Case Length Two digits Represent case Length in mm	Voltage Code C = 5.0V D = 5.4V	Capacitance Code 1st two digits represent significant figures 3rd digit represents multiplier (number of zeros to follow)	Tolerance P = +100%/-0%	Package/Lead Format S = Plastic/Radial	Package B = Bulk T = Tray*	Balancing A = Unbalanced B = Passive Balanced	Lead Orientation 0 = Straight Leads 1 = 2mm Bent Leads*	Series Code H = High Reliability

*Inquire about availability

QUALITY INSPECTION

Parts are tested for life cycle, high temperature load life, temperature characteristics, vibration resistance, and humidity characteristics. See page 2 for more information.

TERMINATION

These SuperCapacitors are compatible with hand soldering and wave soldering processes, so long as appropriate precautions are followed. See page 4 for more information.



For RoHS compliant products, please select correct termination style.

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RATINGS & PART NUMBER REFERENCE

AVX Part Number	Diameter (mm)	Length (mm)	Rated Capacitance (F)	Capacitance Tolerance	Rated Voltage (V)	Rated Temperature (°C)	DCL Max @ 72 Hrs (μA)	ESR Max @ 1000 Hz (mΩ)	ESR Max @ DC (mΩ)	Peak Current (A)	Power Density (W/kg)	Max Energy (Wh)	Energy Density (Wh/kg)
Plastic / Radial Lead													
SCMR14C474PSBA0H	9.5	16	0.47	+100%/-0%	5.0/4.2*	65/85*	2	300	1720	0.65	447	0.0016	0.42
SCMR14D474PSBB0H	9.5	16	0.47	+100%/-0%	5.4/4.6*	65/85*	6	300	1720	0.70	522	0.0019	0.49
SCMR18C105PSBA0H	9.5	20	1	+100%/-0%	5.0/4.2*	65/85*	6	250	720	1.45	906	0.0035	0.75
SCMR18D105PSBB0H	9.5	20	1	+100%/-0%	5.4/4.6*	65/85*	10	250	720	1.57	1057	0.0041	0.88
SCMR22C155PSBA0H	9.5	24	1.5	+100%/-0%	5.0/4.2*	65/85*	10	200	560	2.04	974	0.0052	0.95
SCMR22D155PSBB0H	9.5	24	1.5	+100%/-0%	5.4/4.6*	65/85*	15	200	560	2.20	1136	0.0061	1.10

*with appropriate voltage derating operating temperature can be extended to 85°C

OPERATING TEMPERATURE

-40°C to +65°C @ 5.4V Balanced, 5.0V Unbalanced
-40°C to +85°C @ 4.6V Balanced, 4.2V Unbalanced

QUALIFICATION TEST SUMMARY

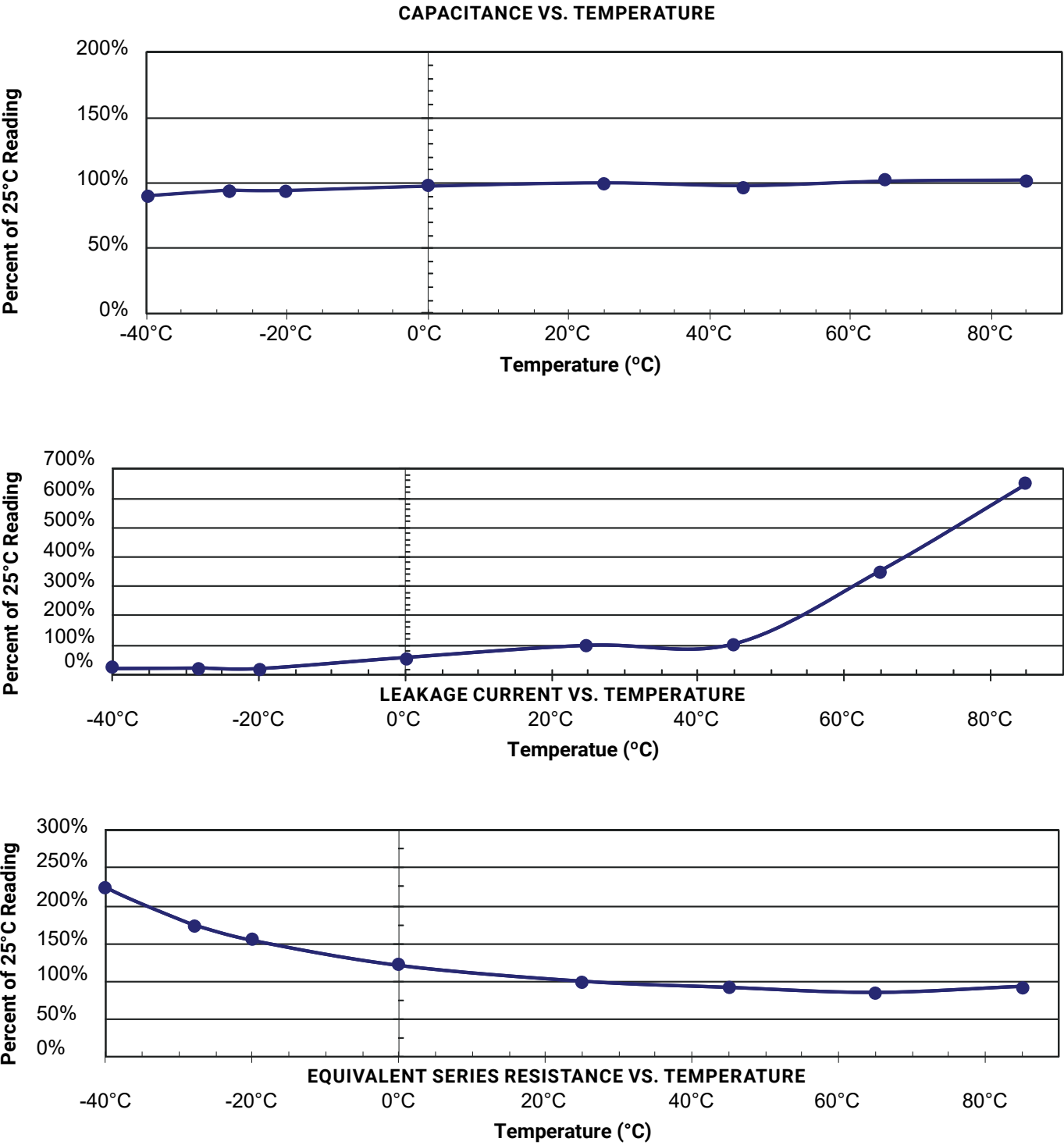
Test	Test Method	Parameter	Limits
Life Cycle	Capacitors are cycled between rated voltage and half-rated voltage under constant current at +25°C for 500,000 cycles	Capacitance ESR Appearance	≤30% of spec value ≤200% of spec value No remarkable defects
High Temperature Load Life	Temperature: 70°C Voltage: Rated Voltage Test Duration: 1,500 hours	Capacitance ESR Appearance	≤30% of spec value ≤200% of spec value No remarkable defects
Storage Temperature Characteristics	Storage Duration: 2 years No Load Temperature: +35°C	Capacitance ESR Appearance	≤30% of spec value ≤200% of spec value No remarkable defects
Vibration Resistance	Amplitude: 1.5mm Frequency: 10 ~ 55Hz Direction: X, Y, Z for 2 hours each	Capacitance ESR Appearance	≤30% of spec value ≤200% of spec value No remarkable defects
Humidity	Voltage: Rated Voltage RH: 90% Temperature: 60°C Test Duration: 2,000 hours	Capacitance ESR Appearance	≤30% of spec value ≤200% of spec value No remarkable defects

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QUALITY AND RELIABILITY



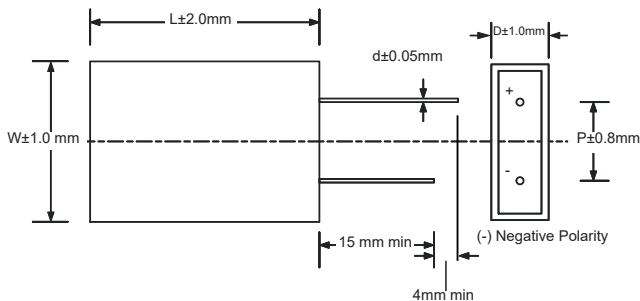
High Reliability SCM Series

Series-Connected SuperCapacitor Modules

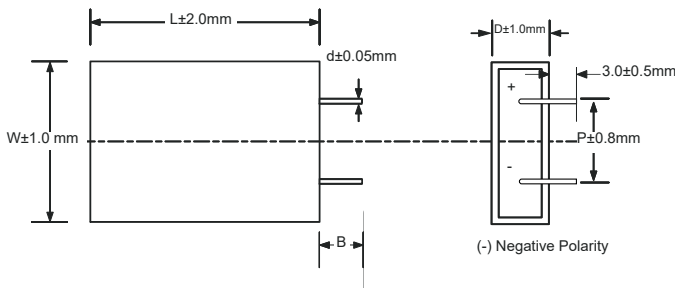


MECHANICAL SPECIFICATIONS

PLASTIC TYPE - STRAIGHT LEADS



PLASTIC TYPE - BENT LEADS



Cap (F)	D (mm)	W (mm)	L (mm)	P (mm)	d (mm)	B (mm)*
0.47	9.5	18.5	16.0	11.5	0.6	2.0
1	9.5	18.5	20.0	11.5	0.6	2.0
1.5	9.5	18.5	24.0	11.5	0.6	2.0

*for version with bent leads

SOLDERING RECOMMENDATIONS

When soldering SuperCapacitors to a PCB, the temperature & time that the body of the SuperCapacitor sees during soldering can have a negative effect on performance. We advise following these guidelines:

- Do not immerse the SuperCapacitors in solder. Only the leads should come in contact with the solder.
- Ensure that the body of the SuperCapacitor is never in contact with the molten solder, the PCB or other components during soldering.
- Excessive temperatures or excessive temperature cycling during soldering may cause the safety vent to burst or the case to shrink or crack, potentially damaging the PCB or other components, and significantly reduce the life of the capacitor.

PRECAUTION: For all products with shrink wrap sleeves, washing in any type of cleaning agent is prohibited. During all soldering processes, it's recommended to protect the shrink wrap from any kind of liquid (including but not limited to: water, strong acid, strong alkali, strong oxidizing solutions, and strong solvents) to avoid the risk of damage, cracking, and fading of the outer shrink wrap.

HAND SOLDERING

Keep some distance between the SuperCapacitor body and the tip of the soldering iron; contact between SuperCapacitor body and soldering iron will cause extensive damage to the SuperCapacitor. It is recommended that the soldering iron temperature should be less than 350°C, and contact time should be limited to no more than 4 seconds. Too much exposure to terminal heat during soldering can cause heat to transfer to the body of the SuperCapacitor, potentially damaging the SuperCapacitor.

WAVE SOLDERING

Only use wave soldering on Radial type SuperCapacitors. The PCB should be preheated only from the bottom and for less than 60 seconds, with temperature at, or below, 100°C on the top side of the board for PCBs equal to or greater than 0.8 mm thick.

Solder Temperature (°C)	Suggested Solder Time (s)	Maximum Solder Time (s)
220	7	9
240	7	9
250	5	7
260	3	5

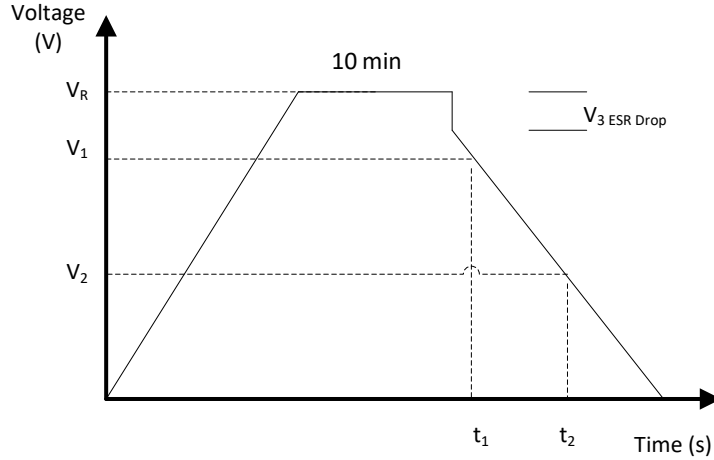
High Reliability SCM Series

Series-Connected SuperCapacitor Modules

TEST METHODS

IEC CAPACITANCE TEST METHOD

Procedure: Charge module under constant current to rated voltage at room temperature, then hold 10 minutes on charge under constant voltage. After 10 minutes, discharge under constant current (as shown in chart below), recording voltage at V_1 , V_2 , and time intervals at t_1 and t_2 . Use the capacitance formula to determine cap value.



I – Discharge Current, $4 \times C \times V_R$ (mA)

V_R – Rated Voltage (V)

V_1 – Initial Test Voltage, 80% Of V_R (V)

V_2 – Final Test Voltage, 40% Of V_R (V)

t_1 – Initial Test Time (s)

t_2 – Final Test Time (s)

$$C = \frac{I \times (t_2 - t_1)}{V_1 - V_2}$$

DC ESR MEASUREMENT

A six-step ESR_{DC} test method is illustrated to the right and carried out as follows:

- Rest 10 Seconds
- Charge under constant current (I_1) to rated voltage (V_R)
- Rest 5 seconds
- Rest 10 seconds, record V_3 and t_4
- Discharge under constant current (I_2) to half rated voltage, Record I_2 , V_4 , And t_5
- Rest 5 seconds, record V_5 And t_6

Repeat steps 1-6 recording I , V , And t accordingly, finally discharging to below 0.1V under constant current (I_2).

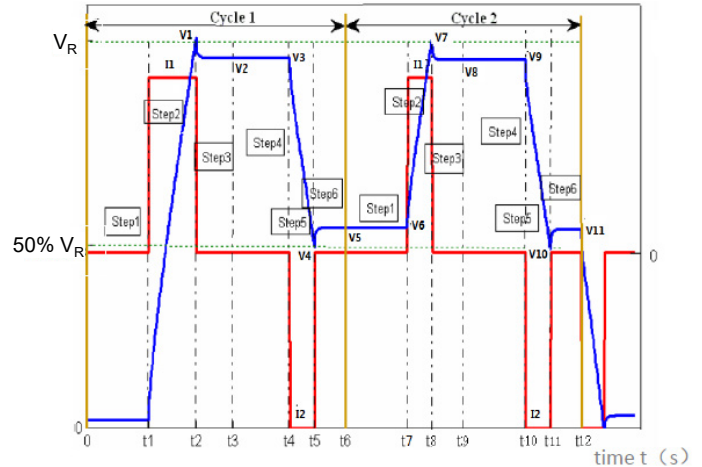
Formulas to calculate:

• Two cycle discharge capacitances: $C_{dch1} = I_2 \times \frac{(t_5 - t_4)}{V_3 - V_4}$; $C_{dch2} = I_2 \times \frac{(t_{11} - t_{10})}{(V_9 - V_{10})}$

• Discharge capacitance: $C_{dch} = \frac{(C_{dch1} + C_{dch2})}{2}$

• Two cycle discharge DC ESR: $ESR_{dch1} = \frac{(V_5 - V_4)}{I_2}$; $ESR_{dch2} = \frac{(V_{11} - V_{10})}{I_2}$

• Discharge DC ESR: $ESR_{dch} = \frac{(ESR_{dch1} + ESR_{dch2})}{2}$



Note: $I_1 = I_2 = 75\text{mA/F}$, the rated capacitance in the chart means discharge capacitance, and DC ESR (ESR_{DC}) means discharge DC resistance.

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TEST METHODS (continued)

MAXIMUM CONTINUOUS CURRENT

- This is the maximum current when temperature rise of the supercapacitor during its operation is less than 15°C

MAXIMUM PEAK CURRENT

- This is the maximum current during 1 second time interval (dt)

WATT DENSITY

- Watt Density = $(0.12 \cdot V^2 / R_{DC}) / \text{mass}$

ENERGY DENSITY

- Energy Density = $(\frac{1}{2} CV^2) / (3600 \cdot \text{mass})$

POLARITY AND REVERSE VOLTAGE

For product consistency and optimum performance, it is recommended that the capacitor be connected with polarity indicated. Reversing polarity could result in permanent damage to the circuit including much higher leakage current for a short duration of time and the life time of the supercapacitors will be reduced.

LIFE TIME AND TEMPERATURE PERFORMANCE

The life of a supercapacitor is impacted by a combination of operating voltage and the operating temperature according to the following Time to Failure equation:

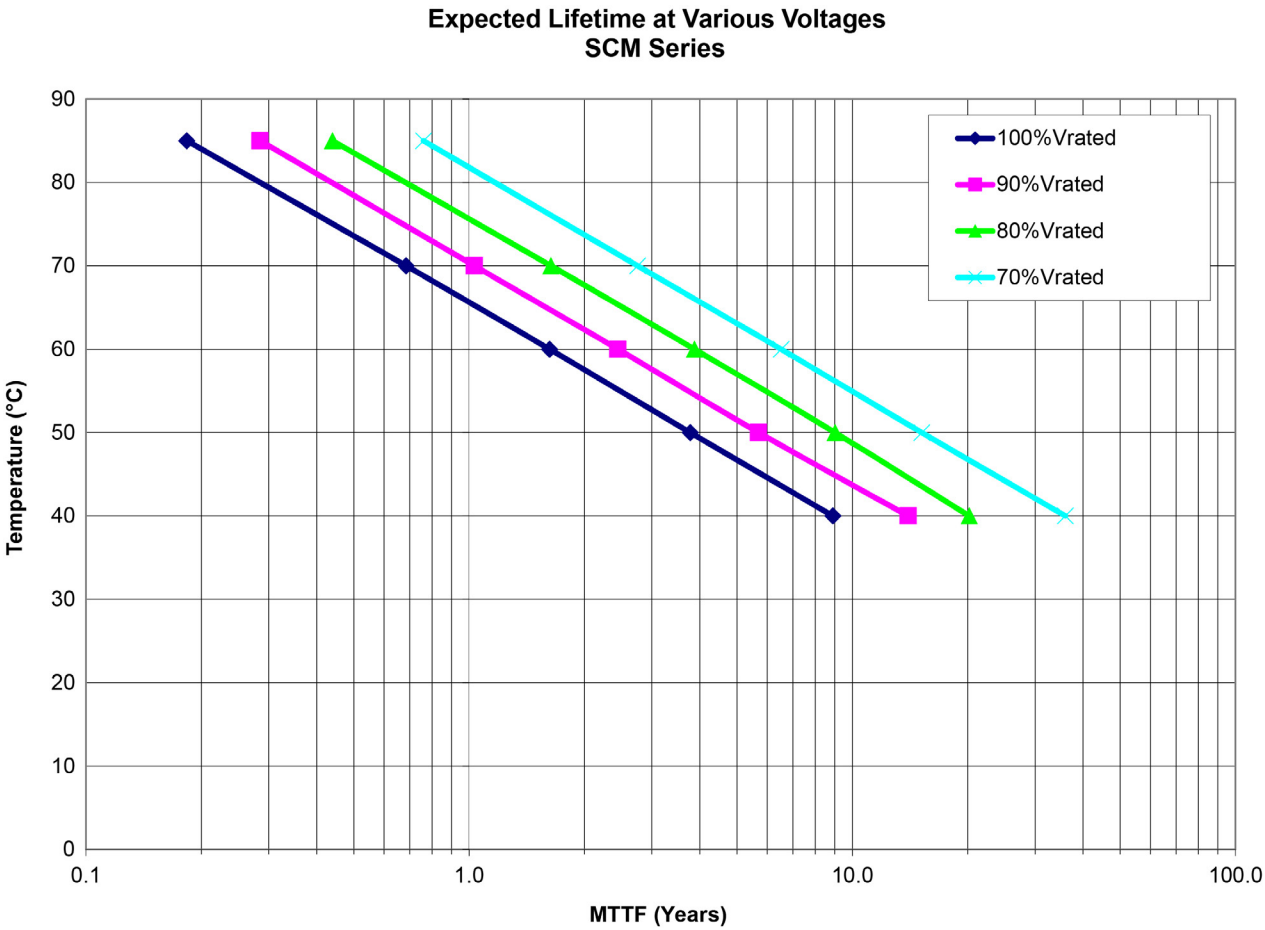
$$t \propto V^n \times e^{\left(\frac{-Q}{kT}\right)}$$

where V is the operating voltage, Q is the activation energy in electron volts (eV), k is the Boltzmann constant in eV, and T is the operating temperature in Kelvin (K). Typical values for the voltage exponent, n, is between 2.5-3.5, and Q is between 1.0-1.2 eV in the normal operating temperature range of -40° to 65°C.

The industry standard for supercapacitor end of life is when the equivalent series resistance, ESR, increases to 200% of the specified value and the capacitance drops by 30% from specified value. Typically a supercapacitor shows an initial “jump” in the ESR value and then levels off. If the supercapacitors are exposed to excessive temperatures the ESR will show a continuous degradation (increase). In the extreme case, if the temperature or voltage are substantially higher than the rated specifications, this could result in the part venting and the product showing a faster degradation of capacitance and ESR, which may be many times the specified value.

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SAFETY RECOMMENDATIONS

WARNINGS

- To avoid short circuit, after usage or test, SuperCapacitor voltage needs to discharge to $\leq 0.1V$
- Do not apply over-voltage, reverse charge, burn or heat higher than $150^{\circ}C$, explosion-proof valve may break open
- Do not press, damage or disassemble the SuperCapacitor, housing could heat to high temperature causing burns
- If you observe overheating or burning smell from the capacitor disconnect power immediately, and do not touch

EMERGENCY APPLICATIONS

- If housing is leaking:
- Skin contact: use soap and water thoroughly to wash the area of the skin
- Eye contact: flush with flowing water or saline, and immediately seek medical treatment
- Ingestion: immediately wash with water and seek medical treatment

TRANSPORTATION

Not subjected to US DOT or IATA regulations
UN3499, <10Wh, Non-Hazardous Goods
International shipping description –
“Electronic Products – Capacitor”

Licensed by CAP-XX

REGULATORY

- UL 810A
- RoHS Compliant
- REACH Compliant
- Halogen free according to IEC 61249-2-21: 2003 and IPC/JEDEC-J-STD-709

STORAGE

Capacitors may be stored within the temperature range of $-40^{\circ}C$ to $+70^{\circ}C$ with humidity $< 60\%$. Lower storage temperature is preferred as it extends the shelf life of the capacitor. Product over one year and within two years of the date code, we recommend recharging the product at the beginning of use for at least 24 hours.

Optimum storage conditions are as follows:

- $25^{\circ}C$ and $RH \leq 60\%$ without voltage applied
- Not in direct sunlight
- Not in direct contact with water, salt oil or other chemicals
- Not in direct contact with corrosive materials, acids, alkalis, or toxic gases
- Not in dusty environments
- Not in environments with shock and vibration conditions



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A light gray world map serves as a background for the lower half of the page. It shows the continents of North America, Central America, Europe, Asia, and Australia.

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