Diode - Fast



Features:

RoHS Compliant



- High efficiency, low V_{F}
- · High current capability
- High reliability
- · High surge current capability
- Low power loss

Specifications:

Mechanical Data:

Cases : Moulded plastic

Lead : Pure tin plated, lead free, solderable per MIL-STD-202, Method 208 guaranteed

Polarity : Colour band denotes cathode end

High temperature soldering guaranteed : 260°C/10 seconds/0.375 inch, (9.5mm) lead lengths at 5lbs., (2.3kg) tension

Mounting position : Any
Weight : 1.2 grams

Maximum Ratings and Electrical Characteristics:

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

| Characteristics | Symbol | Values | Units |
|---|------------------|-------------|----------|
| Maximum Recurrent Peak Reverse Voltage | V_{RRM} | 1000 | V |
| Maximum RMS Voltage | V _{RMS} | 700 | |
| Maximum DC Blocking Voltage | V _{DC} | 1000 | |
| Maximum Average Forward Rectified Current 0.375 inch (9.5mm) Lead Length at $T_A = 55^{\circ}C$ | I(AV) | 3 | A |
| Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method) | I _{FSM} | 125 | |
| Maximum Instantaneous Forward Voltage at 3A | V _F | 1.3 | V |
| Maximum DC Reverse Current at $T_A = 25$ °C at Rated DC Blocking Voltage at $T_A = 125$ °C | I _R | 5 100 | μA μA |
| Maximum Reverse Recovery Time (Note 1) | T _{rr} | 500 | nS |
| Typical Junction Capacitance (Note 2) | C _i | 30 | pF |
| Typical Thermal Resistance | R _{θJA} | 35 | °C/W |
| Operating Temperature Range | TJ | -65 to +150 | °C |
| Storage Temperature Range | T _{STG} | | |

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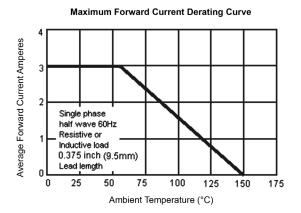
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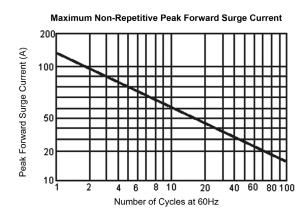


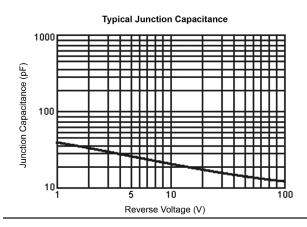
Notes:

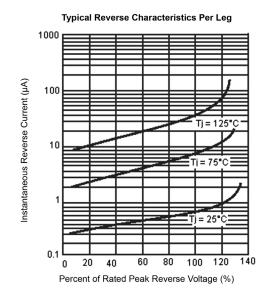
- 1. Reverse Recovery Test Conditions: I_F = 0.5A, I_R = 1A, I_{RR} = 0.25A.
- 2. Measured at 1MHz and Applied Reverse Voltage of 4 Volts DC.
- 3. Mount on Cu-Pad Size 16mm x 16mm on PCB.

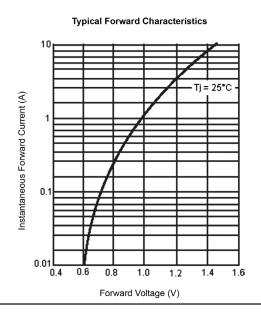
Ratings and Characteristic Curves











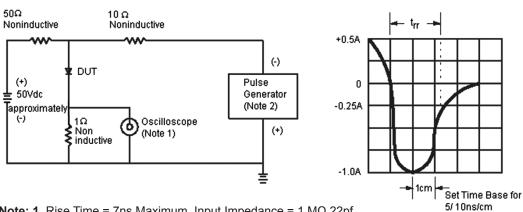
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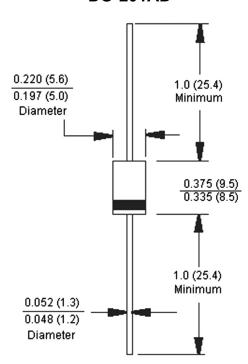


Reverse Recovery Time Characteristic and Test Circuit Diagram



Note: 1. Rise Time = 7ns Maximum. Input Impedance = $1 \text{ M}\Omega$ 22pf **Note:** 2. Rise Time = 10 ns Maximum Source Impedance = 50Ω

DO-201AD



Dimensions: Inches (Millimetres)

Part Number Table

| Description | Part Number | |
|------------------------|-------------|--|
| Diode, Fast, 3A, 1000V | FR307G+ | |

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