



PHPT60606PY

60 V, 6 A PNP high power bipolar transistor

9 December 2014

Product data sheet

1. General description

PNP high power bipolar transistor in a SOT669 (LFAK56) Surface-Mounted Device (SMD) power plastic package.

NPN complement: PHPT60606NY.

2. Features and benefits

- High thermal power dissipation capability
- Suitable for high temperature applications up to 175 °C
- Reduced Printed-Circuit Board (PCB) requirements comparing to transistors in DPAK
- High energy efficiency due to less heat generation
- AEC-Q101 qualified

3. Applications

- Power management
- Load switch
- Linear mode voltage regulator
- Backlighting applications
- Motor drive
- Relay replacement

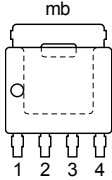
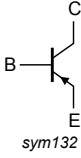
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-60	V
I_C	collector current		-	-	-6	A
I_{CM}	peak collector current	$t_p \leq 1$ ms; pulsed	-	-	-12	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = -6$ A; $I_B = -600$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	66	88	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 LFPAK56; Power-SO8 (SOT669)	 <i>sym132</i>
2	E	emitter		
3	E	emitter		
4	B	base		
mb	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PHPT60606PY	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669

7. Marking

Table 4. Marking codes

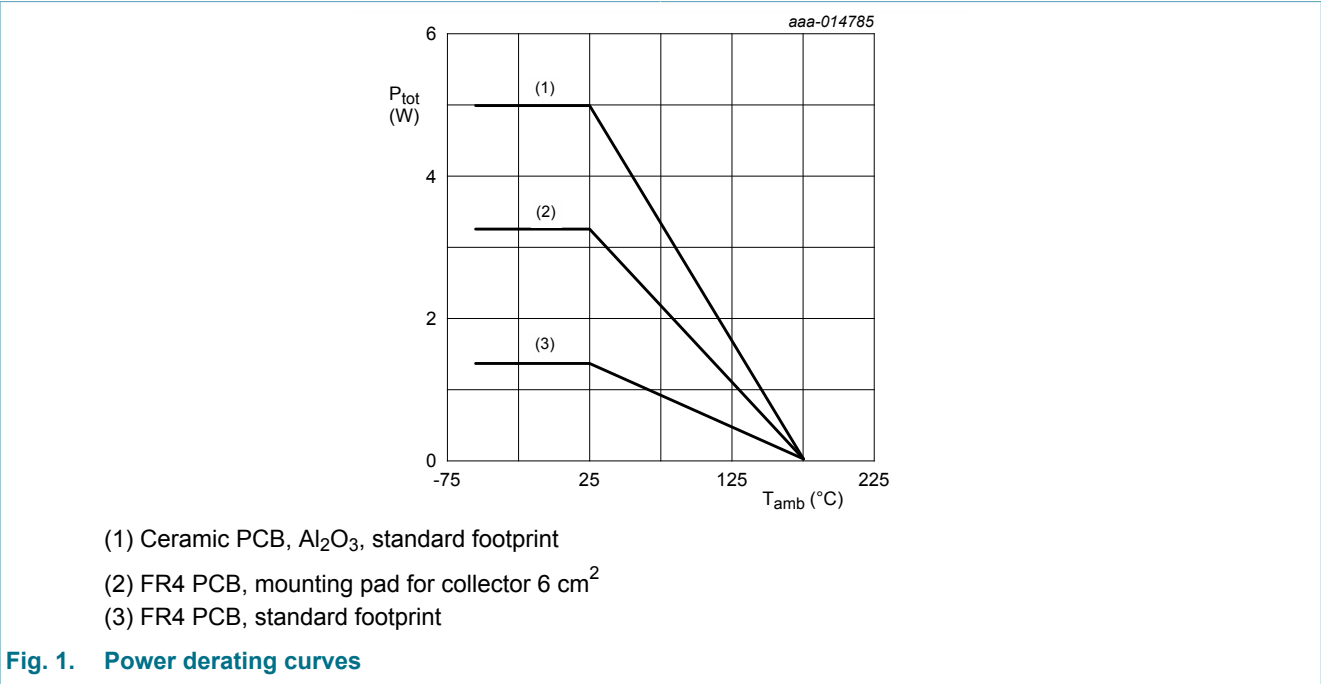
Type number	Marking code
PHPT60606PY	0606PAB

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-8	V
I _C	collector current			-	-6	A
I _{CM}	peak collector current	t _p ≤ 1 ms; pulsed		-	-12	A
I _B	base current			-	-800	mA
I _{BM}	peak base current	t _p ≤ 1 ms; pulsed		-	-1.2	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.35	W
			[2]	-	3.25	W
			[3]	-	5	W
			[4]	-	25	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [4] Power dissipation from junction to mounting base.

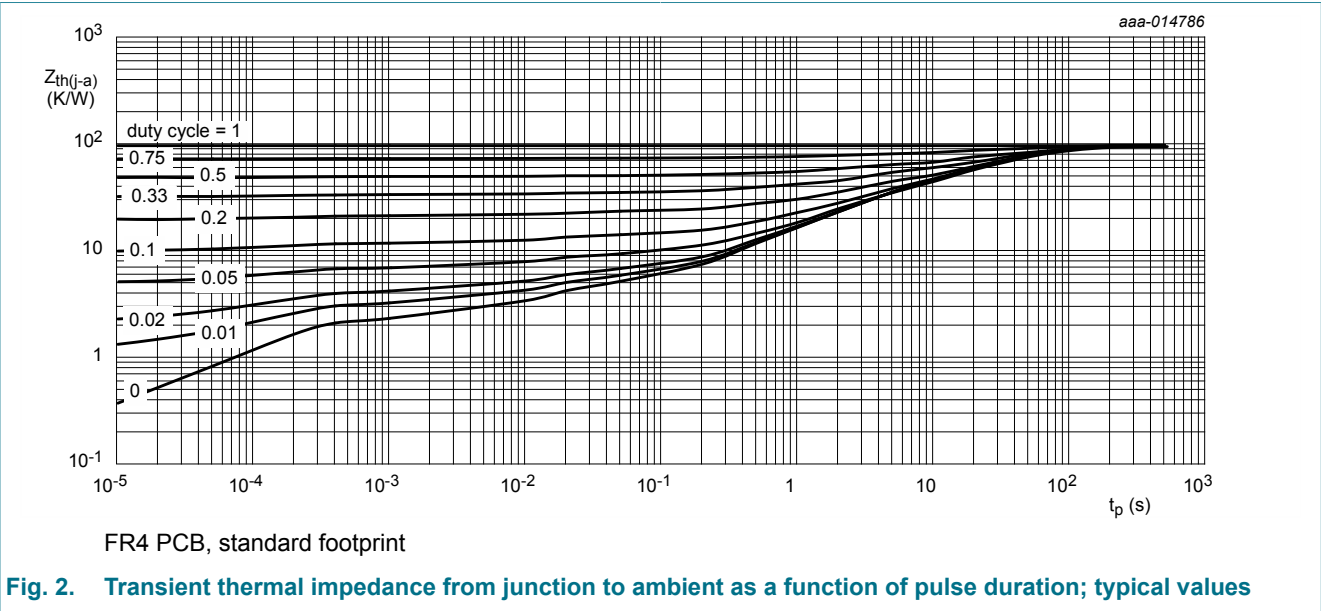


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	111	K/W
			[2]	-	-	46	K/W
			[3]	-	-	30	K/W
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	-	6	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



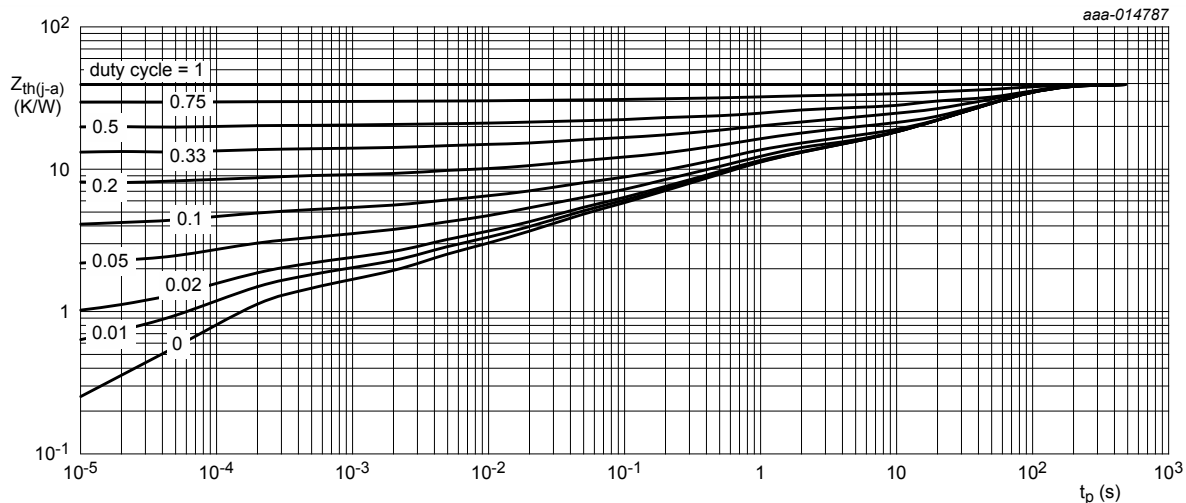


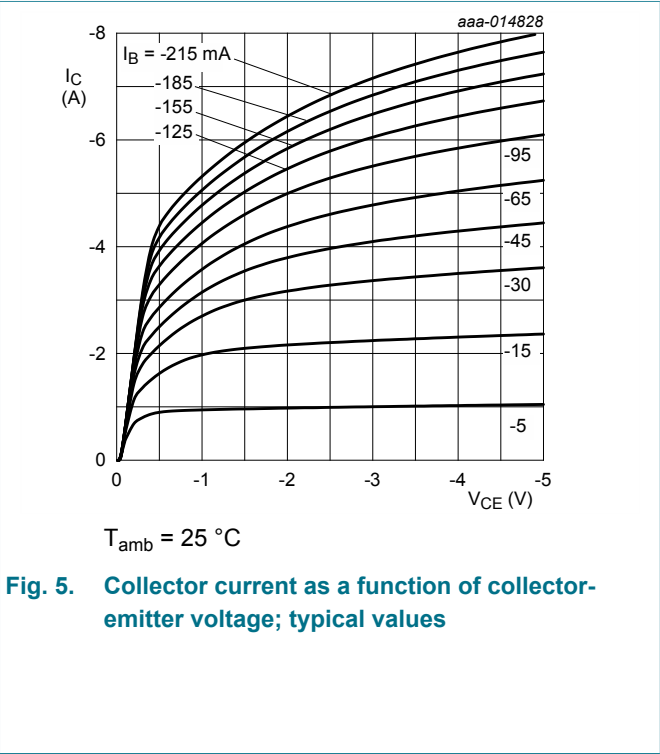
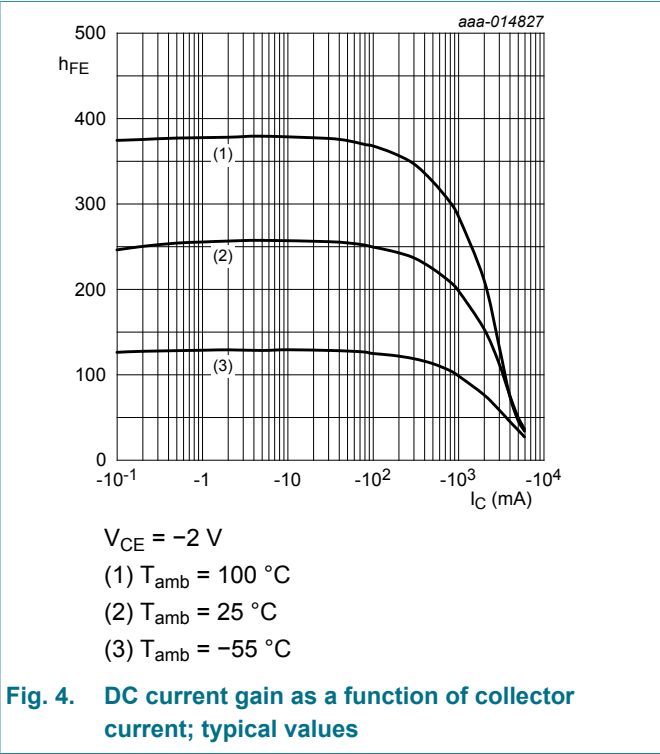
Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = -48\text{ V}; I_E = 0\text{ A}; T_{amb} = 25\text{ °C}$	-	-	-100	nA
		$V_{CB} = -48\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ °C}$	-	-	-50	μA
I_{CES}	collector-emitter cut-off current	$V_{CE} = -48\text{ V}; V_{BE} = 0\text{ V}; T_{amb} = 25\text{ °C}$	-	-	-100	nA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -8\text{ V}; I_C = 0\text{ A}; T_{amb} = 25\text{ °C}$	-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -2\text{ V}; I_C = -500\text{ mA}; T_{amb} = 25\text{ °C}$	120	200	-	
		$V_{CE} = -2\text{ V}; I_C = -1\text{ A}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}; \text{pulsed}$	110	180	-	
		$V_{CE} = -2\text{ V}; I_C = -3\text{ A}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}; \text{pulsed}$	60	100	-	
		$V_{CE} = -2\text{ V}; I_C = -6\text{ A}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$	20	30	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -1\text{ A}; I_B = -50\text{ mA}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$	-	-75	-110	mV
		$I_C = -3\text{ A}; I_B = -300\text{ mA}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}; \text{pulsed}$	-	-155	-230	mV
		$I_C = -6\text{ A}; I_B = -600\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$	-	-395	-525	mV
R_{CEsat}	collector-emitter saturation resistance	$I_C = -6\text{ A}; I_B = -600\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$	-	66	88	mΩ

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.85	-0.95	V
		I _C = -3 A; I _B = -300 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-1	-1.1	V
		I _C = -6 A; I _B = -600 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-1.1	-1.3	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -2 V; I _C = -0.5 A; T _{amb} = 25 °C	-	-0.75	-0.85	V
t _d	delay time	V _{CC} = -12.5 V; I _C = -3 A; I _{Bon} = -150 mA; I _{Boff} = 150 mA; T _{amb} = 25 °C	-	15	-	ns
t _r	rise time		-	110	-	ns
t _{on}	turn-on time		-	125	-	ns
t _s	storage time		-	185	-	ns
t _f	fall time		-	70	-	ns
t _{off}	turn-off time		-	255	-	ns
f _T	transition frequency	V _{CE} = -10 V; I _C = -500 mA; f = 100 MHz; T _{amb} = 25 °C	-	110	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	57	-	pF



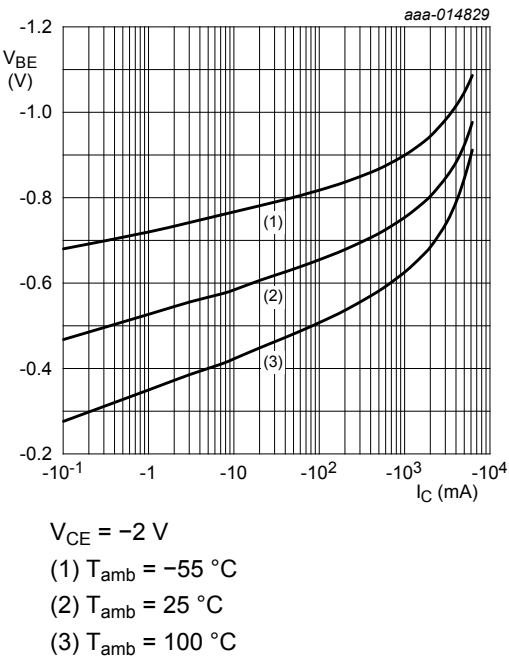


Fig. 6. Base-emitter voltage as a function of collector current; typical values

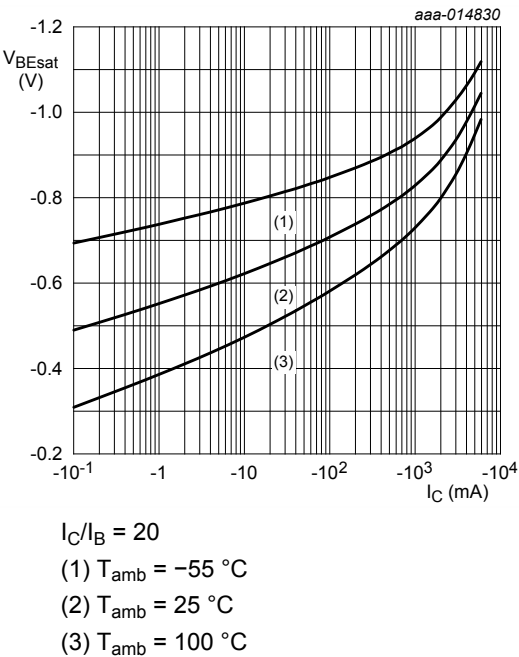


Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

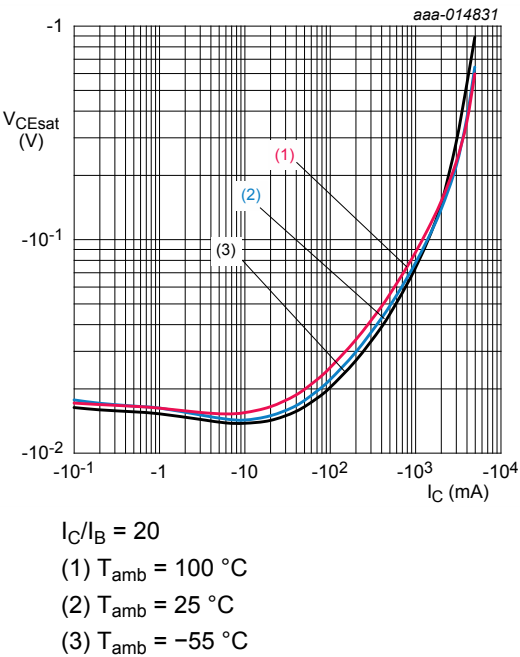


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

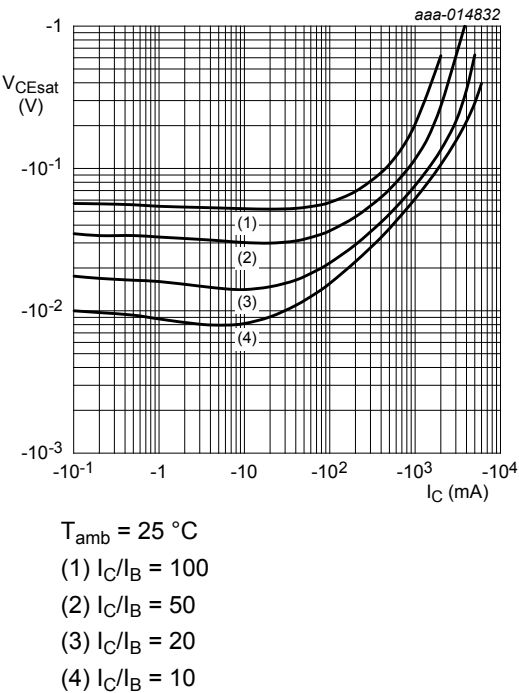
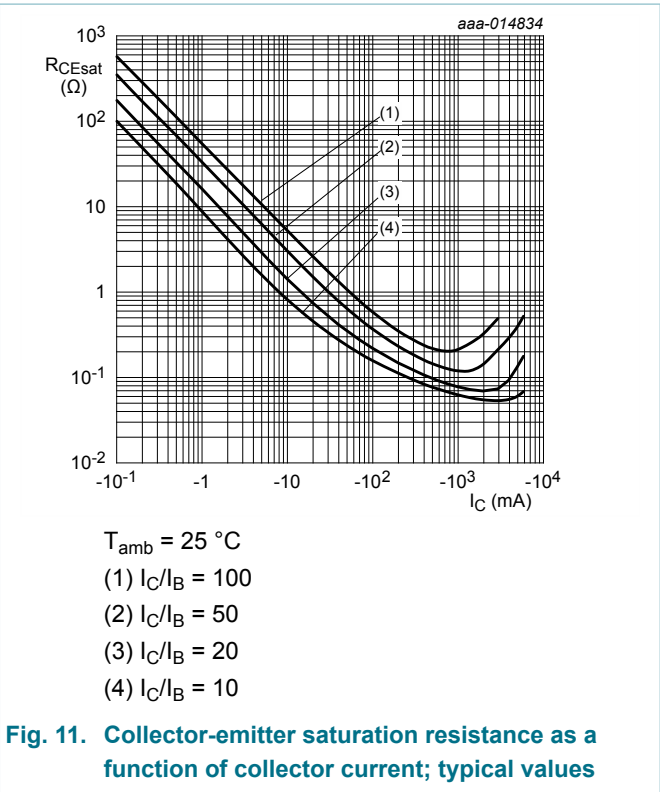
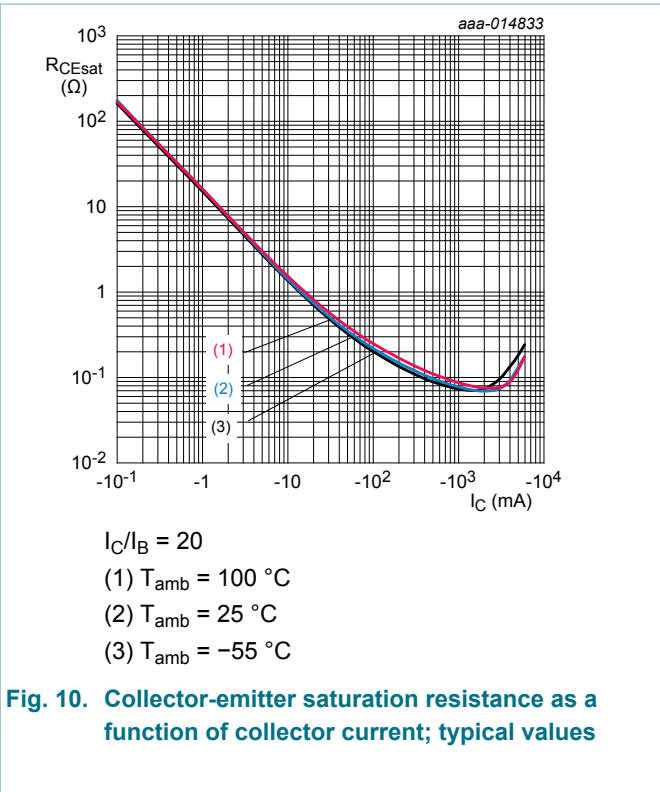


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



11. Test information

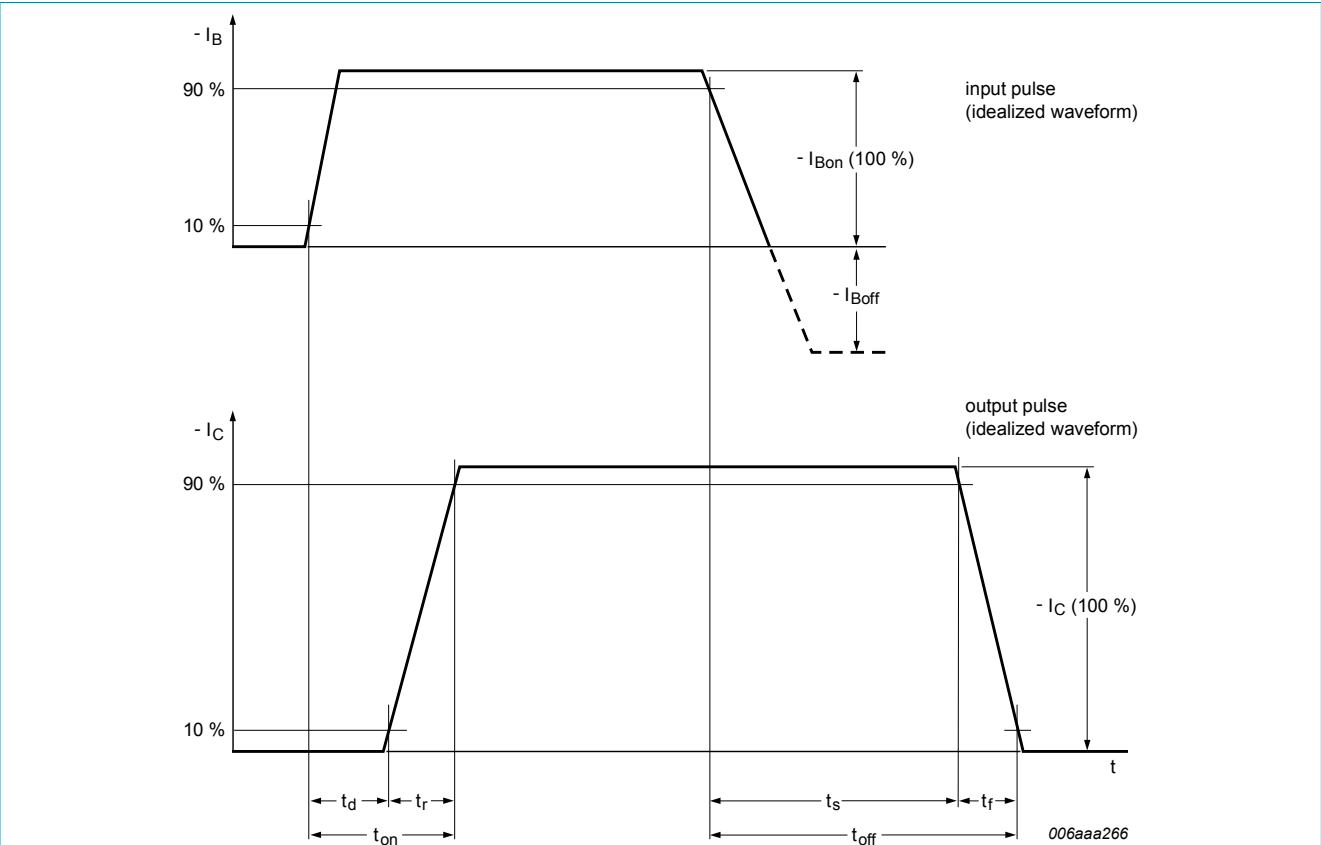


Fig. 12. BISS transistor switching time definition

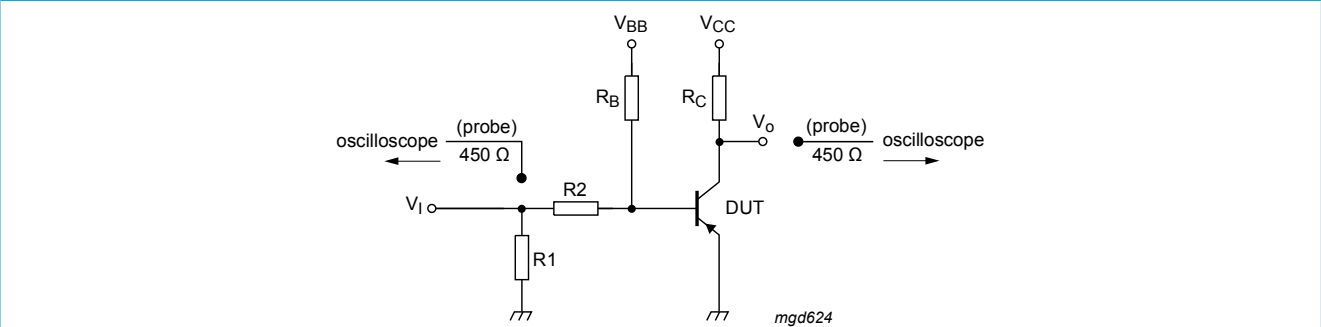


Fig. 13. Test circuit for switching times

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

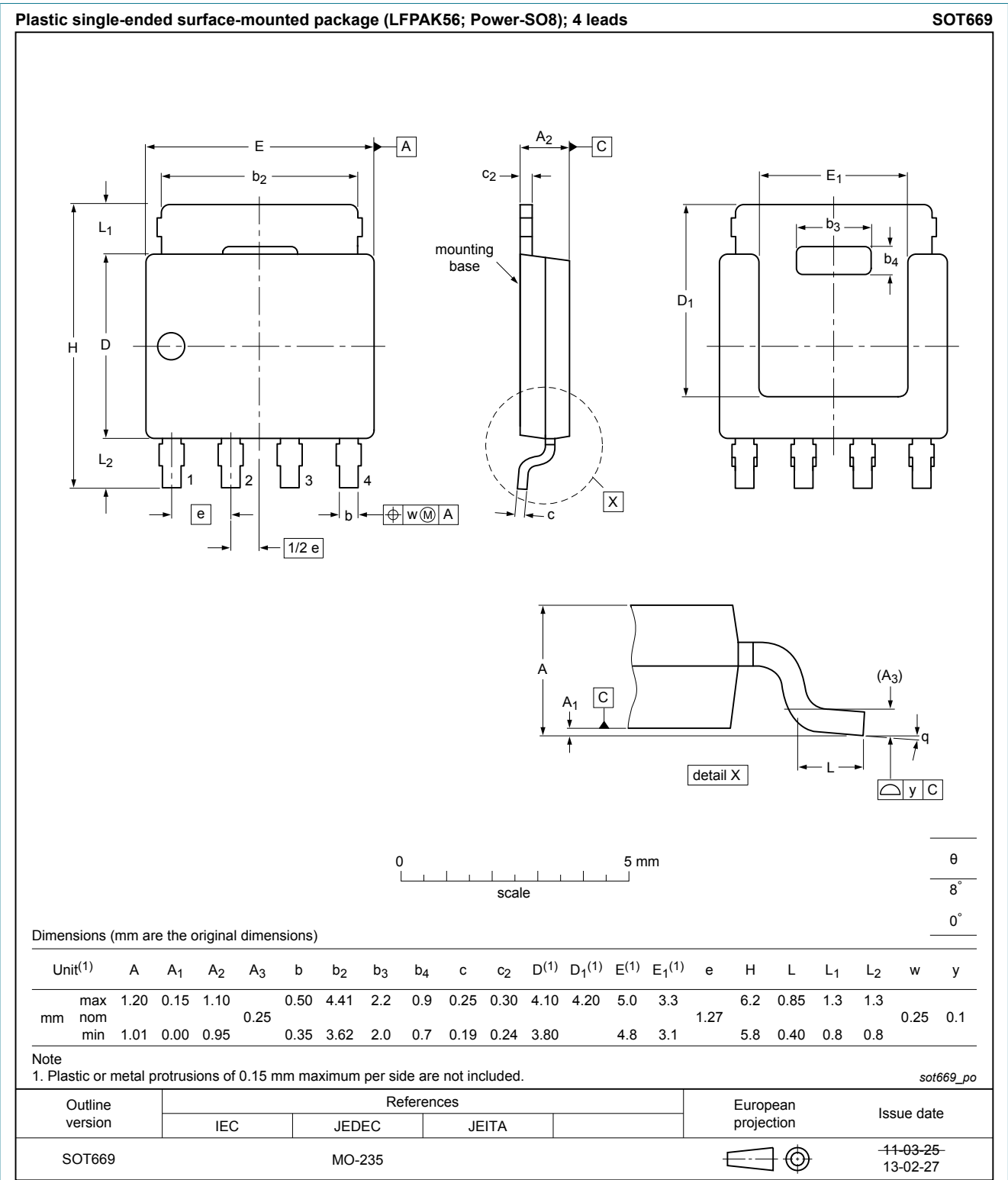


Fig. 14. Package outline LPAK56; Power-SO8 (SOT669)

13. Soldering

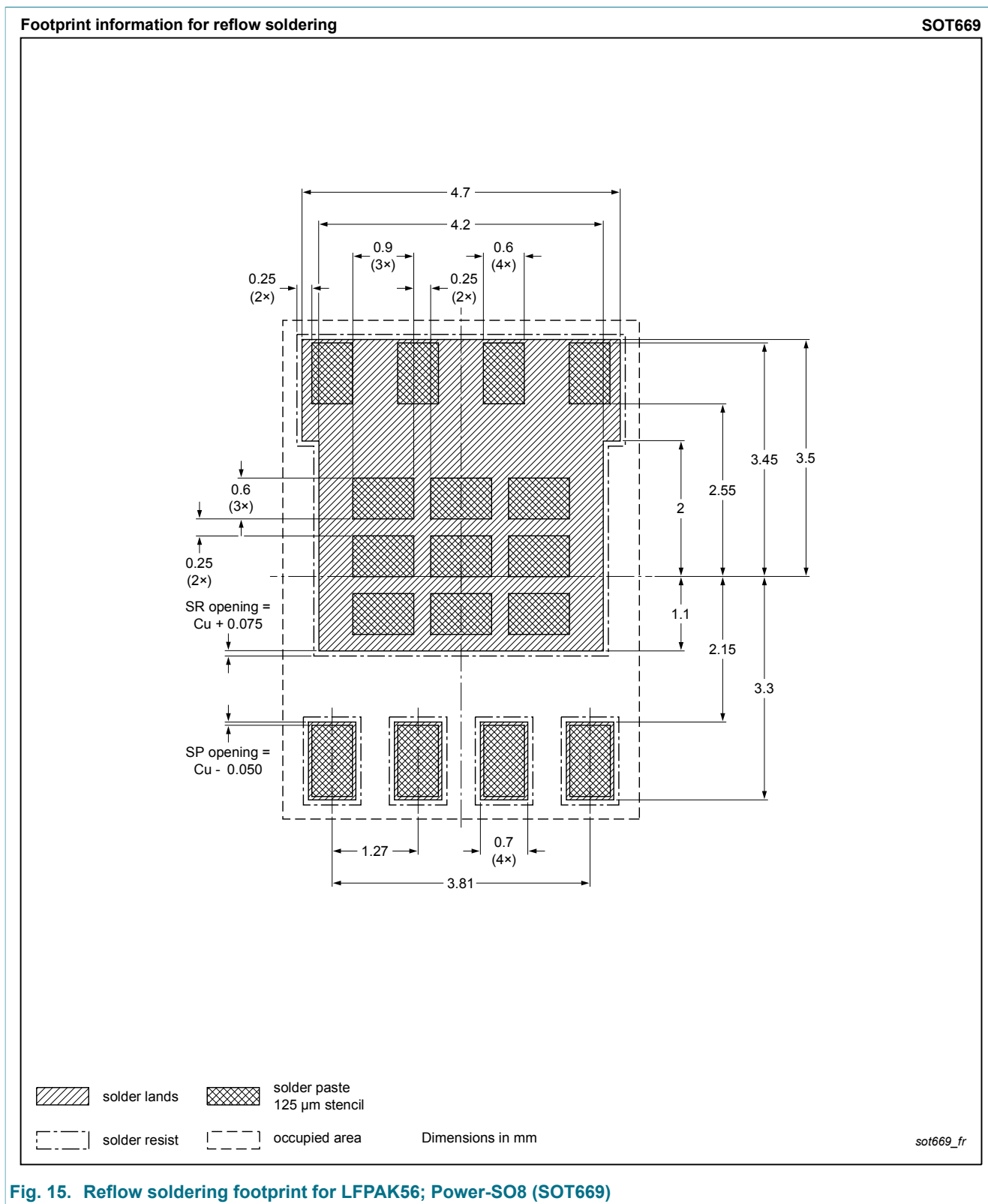


Fig. 15. Reflow soldering footprint for LPAK56; Power-SO8 (SOT669)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PHPT60606PY v.1	20141209	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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