



NX3008PBKV

30 V, 220 mA dual P-channel Trench MOSFET

Rev. 1 — 29 July 2011

Product data sheet

1. Product profile

1.1 General description

Dual P-channel enhancement mode Field-Effect Transistor (FET) in an ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Very fast switching
- Low threshold voltage
- Trench MOSFET technology
- ESD protection up to 2 kV
- AEC-Q101 qualified

1.3 Applications

- Relay driver
- High-speed line driver
- High-side loadswitch
- Switching circuits

1.4 Quick reference data

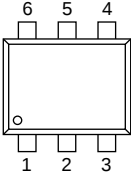
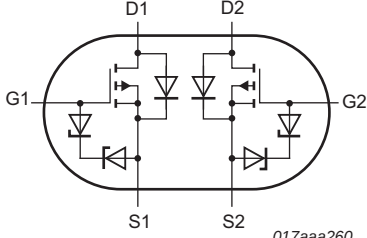
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$	-	-	-30	V
V_{GS}	gate-source voltage		-8	-	8	V
I_D	drain current	$V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ °C}$ [1]	-	-	-220	mA
Static characteristics (per transistor)						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = -4.5\text{ V}; I_D = -200\text{ mA}; T_j = 25\text{ °C}$	-	2.8	4.1	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 SOT666 (SOT666)	 017aaa260
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NX3008PBKV	SOT666	plastic surface-mounted package; 6 leads	SOT666

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
NX3008PBKV	AB

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

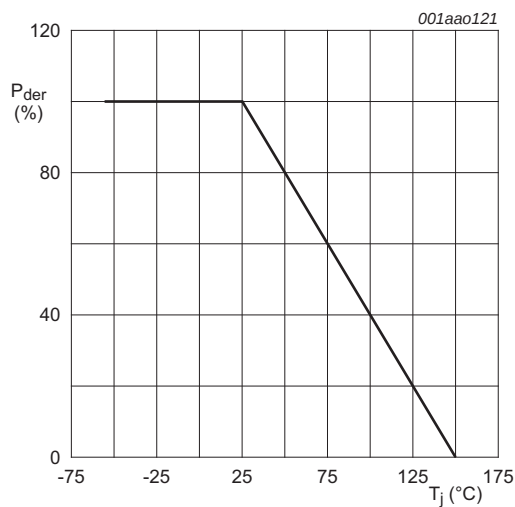
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per transistor					
V _{DS}	drain-source voltage	T _j = 25 °C	-	-30	V
V _{GS}	gate-source voltage		-8	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1] -	-220	mA
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1] -	-140	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs	-	-0.9	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2] -	330	mW
			[1] -	390	mW
		T _{sp} = 25 °C	-	1090	mW
Per device					
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2] -	500	mW
T _j	junction temperature		-55	150	°C
T _{amb}	ambient temperature		-55	150	°C
T _{stg}	storage temperature		-65	150	°C
Source-drain diode					
I _S	source current	T _{amb} = 25 °C	[1] -	-220	mA
ESD maximum rating					
V _{ESD}	electrostatic discharge voltage	HBM	[3] -	2000	V

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

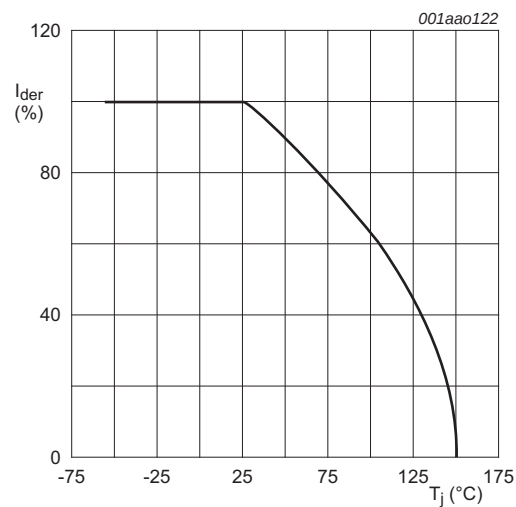
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.



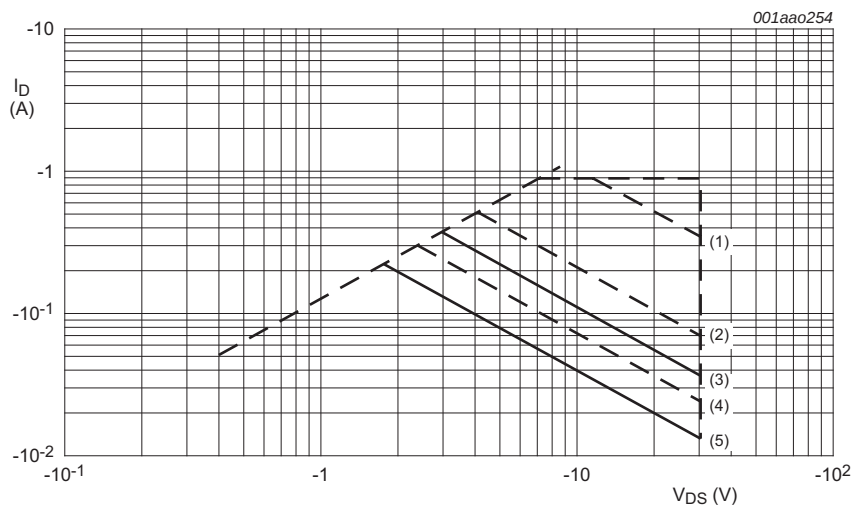
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}\text{C})}} \times 100\%$$

Fig 1. Normalized total power dissipation as a function of junction temperature



$$I_{der} = \frac{I_D}{I_{D(25^{\circ}\text{C})}} \times 100\%$$

Fig 2. Normalized continuous drain current as a function of junction temperature



I_{DM} is a single pulse

(1) $t_p = 1$ ms

(2) $t_p = 10$ ms

(3) DC; $T_{sp} = 25^{\circ}\text{C}$

(4) $t_p = 100$ ms

(5) DC; $T_{amb} = 25^{\circ}\text{C}$; 1 cm^2 drain mounting pad

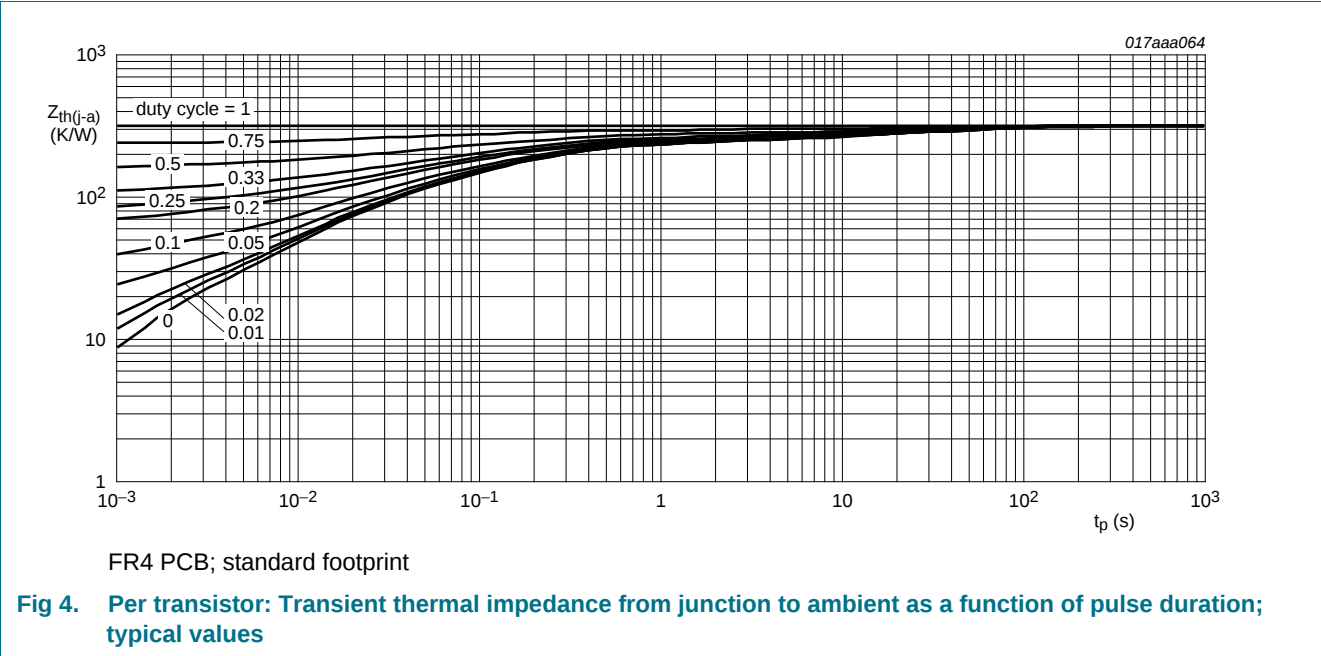
Fig 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

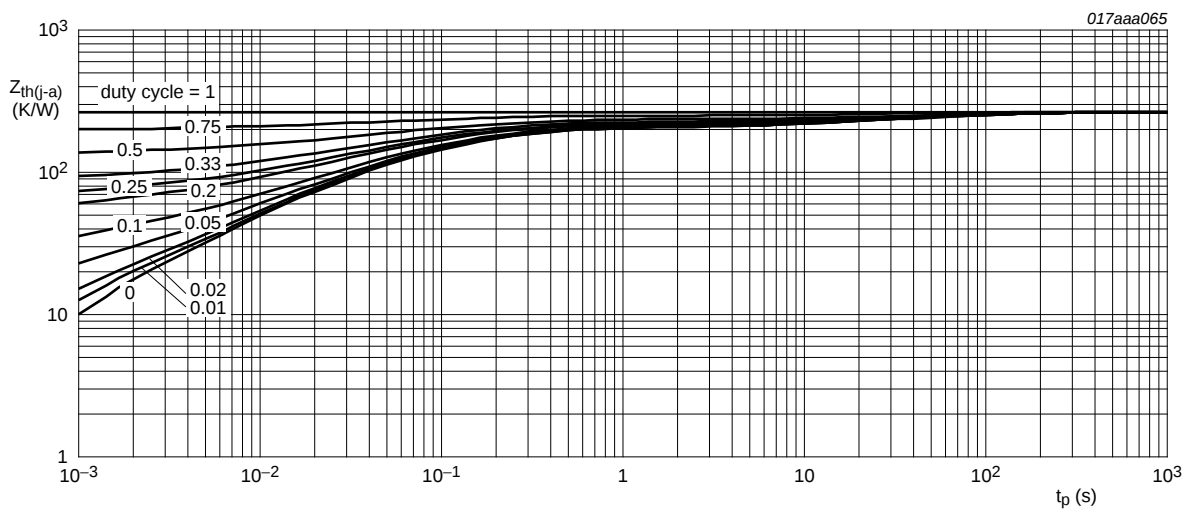
6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per device						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] -	-	250	K/W
Per transistor						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] -	330	380	K/W
			[2] -	280	320	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	115	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².





FR4 PCB, mounting pad for drain 1 cm²

Fig 5. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics (per transistor)						
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-30	-	-	V
V _{GStH}	gate-source threshold voltage	I _D = -250 μA; V _{DS} = V _{GS} ; T _j = 25 °C	-0.6	-0.9	-1.1	V
I _{DSS}	drain leakage current	V _{DS} = -30 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
		V _{DS} = -30 V; V _{GS} = 0 V; T _j = 150 °C	-	-	-10	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-0.2	-1	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-0.2	-1	μA
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-10	-	nA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-10	-	nA
		V _{GS} = 2.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-1	-	nA
		V _{GS} = -2.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-1	-	nA
		R _{DSon}	drain-source on-state resistance	V _{GS} = -4.5 V; I _D = -200 mA; T _j = 25 °C	-	2.8
V _{GS} = -4.5 V; I _D = -200 mA; T _j = 150 °C	-			5.3	7.8	Ω
V _{GS} = -2.5 V; I _D = -10 mA; T _j = 25 °C	-			5.3	6.5	Ω
g _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -200 mA; T _j = 25 °C	-	160	-	mS
Dynamic characteristics (per transistor)						
Q _{G(tot)}	total gate charge	V _{DS} = -15 V; I _D = -200 mA;	-	0.55	0.72	nC
Q _{GS}	gate-source charge	V _{GS} = -4.5 V; T _j = 25 °C	-	0.23	-	nC
Q _{GD}	gate-drain charge		-	0.09	-	nC
C _{iSS}	input capacitance	V _{DS} = -15 V; f = 1 MHz; V _{GS} = 0 V;	-	31	46	pF
C _{oSS}	output capacitance	T _j = 25 °C	-	6.5	-	pF
C _{rSS}	reverse transfer capacitance		-	2.3	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = -20 V; R _L = 250 Ω; V _{GS} = -4.5 V;	-	19	38	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	30	-	ns
t _{d(off)}	turn-off delay time		-	65	130	ns
t _f	fall time		-	38	-	ns
Source-drain diode (per transistor)						
V _{SD}	source-drain voltage	I _S = -200 mA; V _{GS} = 0 V; T _j = 25 °C	-0.47	-0.88	-1.2	V

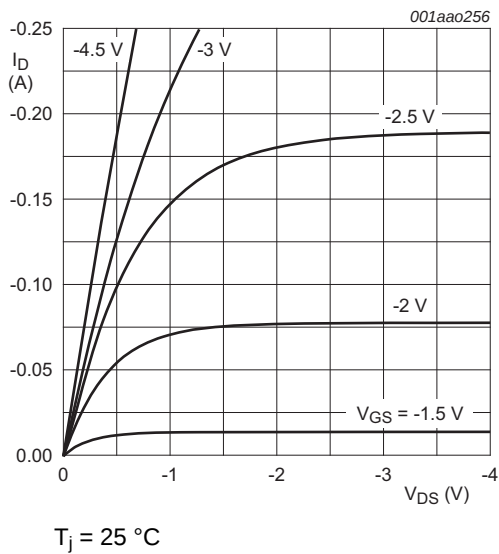


Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values

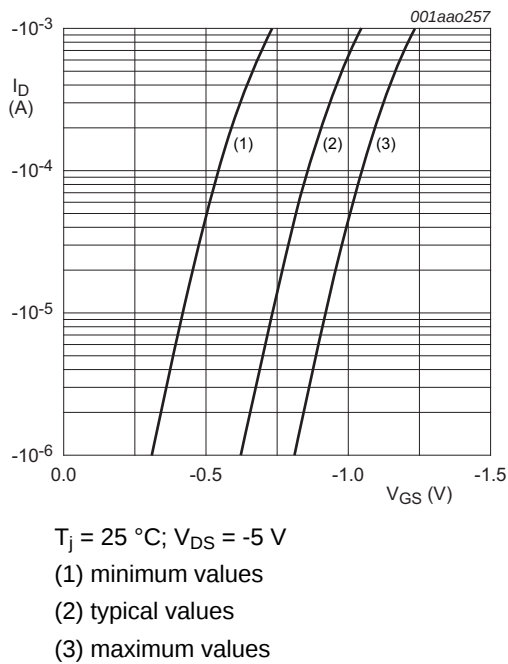


Fig 7. Sub-threshold drain current as a function of gate-source voltage

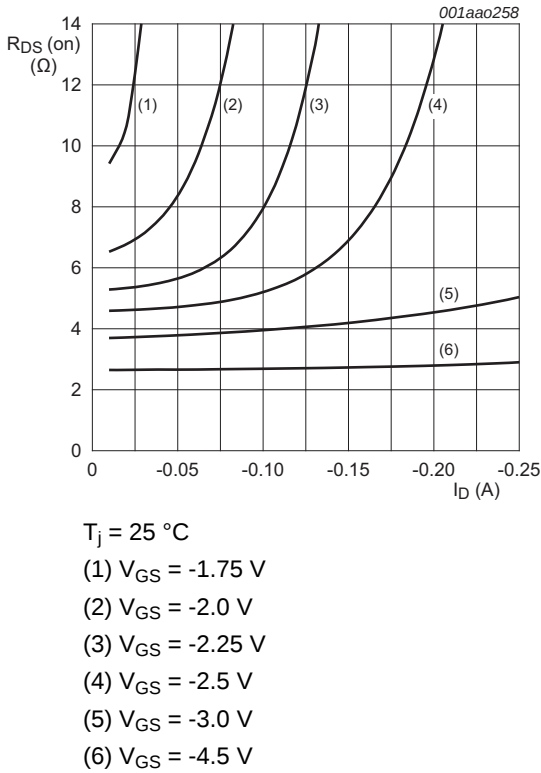


Fig 8. Drain-source on-state resistance as a function of drain current; typical values

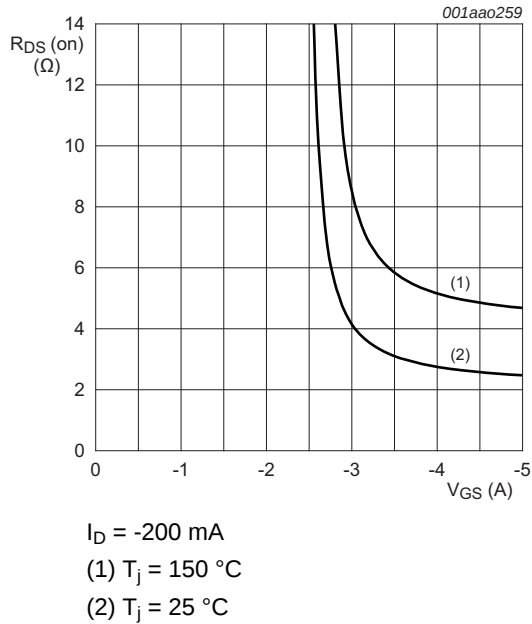
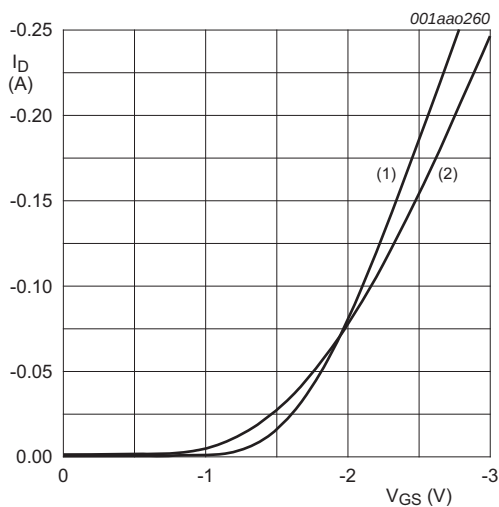
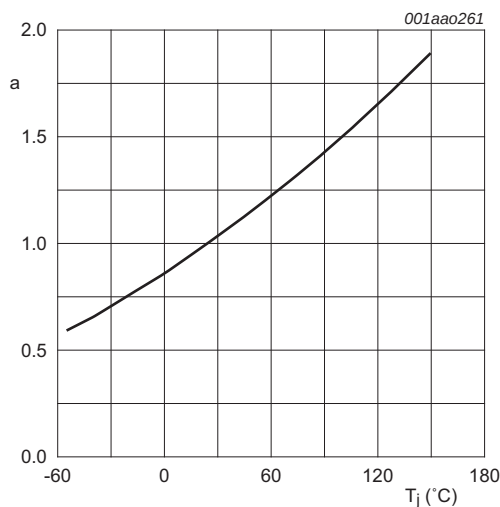


Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



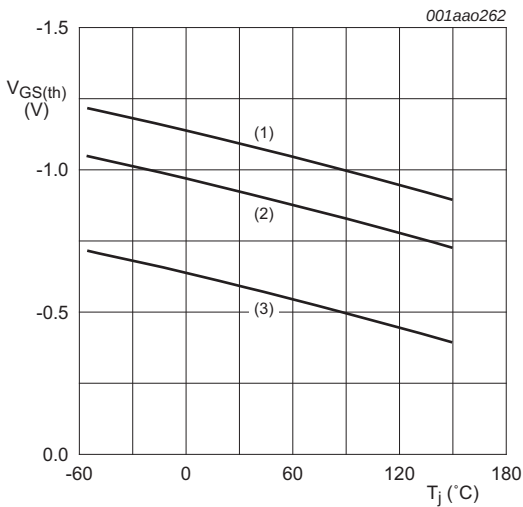
$V_{DS} > I_D \times R_{DSon}$
(1) $T_j = 25\text{ }^{\circ}\text{C}$
(2) $T_j = 150\text{ }^{\circ}\text{C}$

Fig 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values



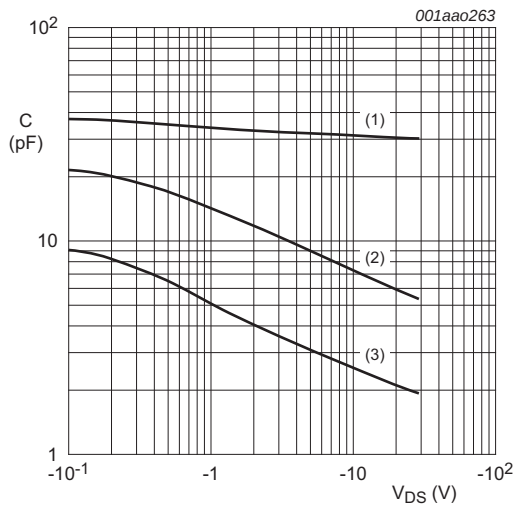
$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}\text{C})}}$$

Fig 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values



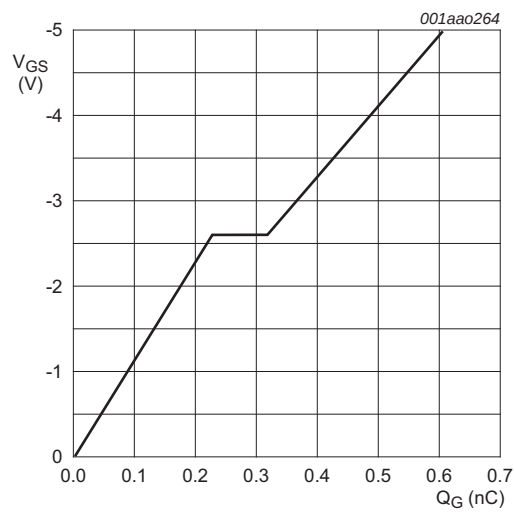
$I_D = -0.25\text{ mA}$; $V_{DS} = V_{GS}$
(1) maximum values
(2) typical values
(3) minimum values

Fig 12. Gate-source threshold voltage as a function of junction temperature



$f = 1\text{ MHz}$; $V_{GS} = 0\text{ V}$
(1) C_{iss}
(2) C_{oss}
(3) C_{rss}

Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = -200$ mA; $V_{DS} = -15$ V; $T_{amb} = 25$ °C

Fig 14. Gate-source voltage as a function of gate charge; typical values

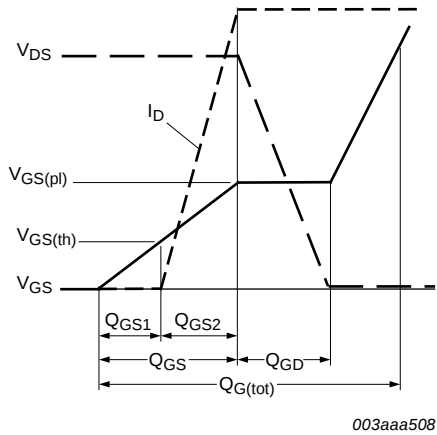
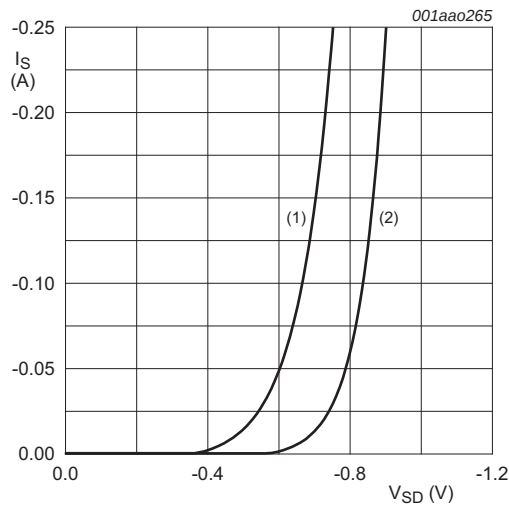


Fig 15. Gate charge waveform definitions



$V_{GS} = 0$ V
(1) $T_j = 150$ °C
(2) $T_j = 25$ °C

Fig 16. Source current as a function of source-drain voltage; typical values

8. Test information

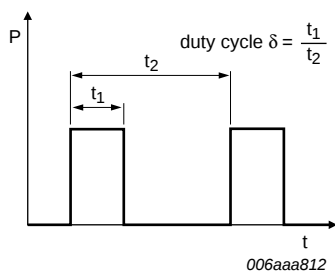


Fig 17. Duty cycle definition

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline

Plastic surface-mounted package; 6 leads

SOT666

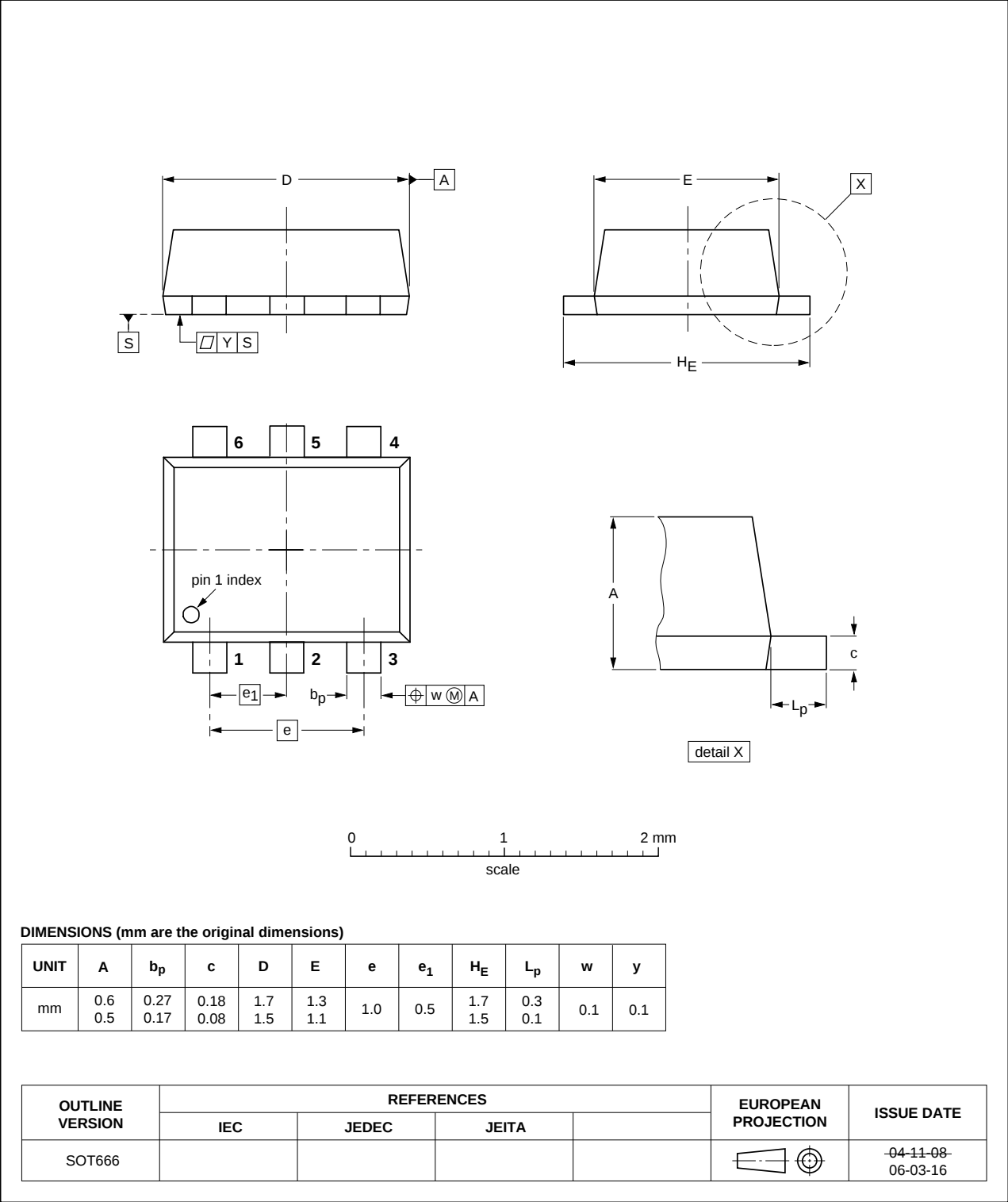


Fig 18. Package outline SOT666 (SOT666)

10. Soldering

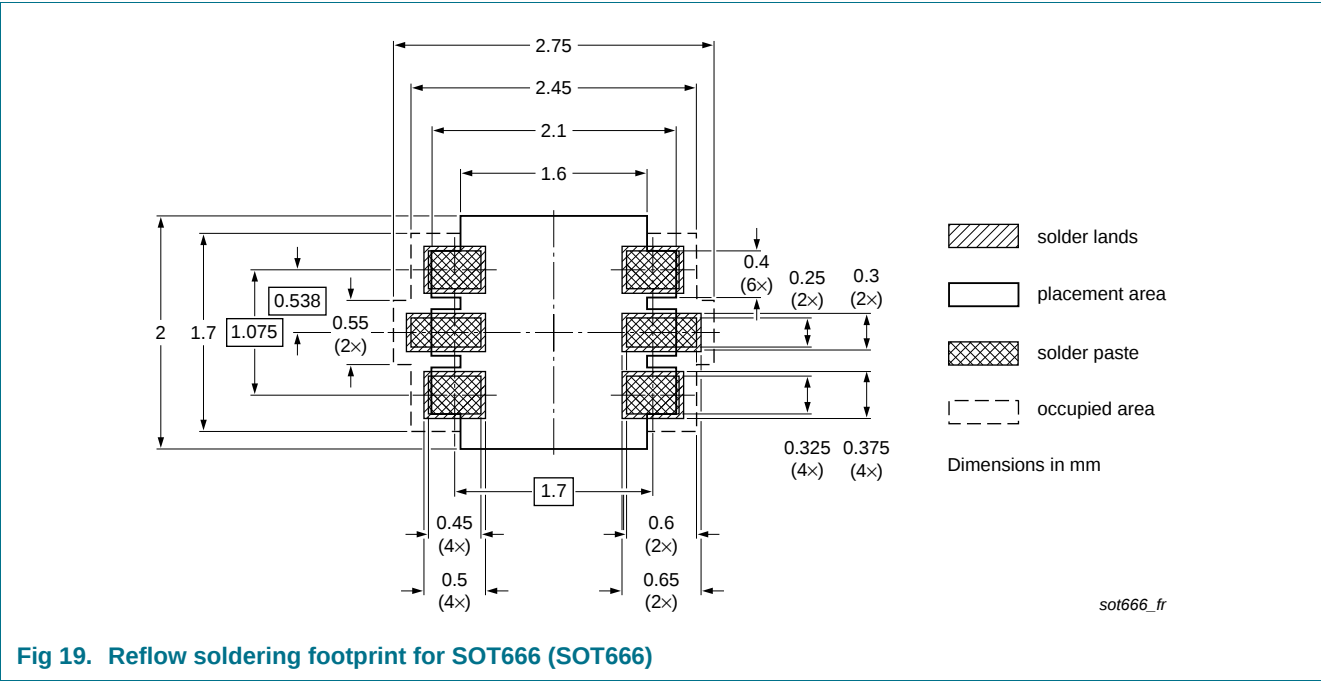


Fig 19. Reflow soldering footprint for SOT666 (SOT666)

11. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NX3008PBKV v.1	20110729	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1] [2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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14. Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	2
3	Ordering information	2
4	Marking	2
5	Limiting values	3
6	Thermal characteristics	5
7	Characteristics	7
8	Test information	11
8.1	Quality information	11
9	Package outline	12
10	Soldering	13
11	Revision history	14
12	Legal information	15
12.1	Data sheet status	15
12.2	Definitions	15
12.3	Disclaimers	15
12.4	Trademarks	16
13	Contact information	16