

TK60D08J1

Switching Regulator Application

- High-Speed switching
- Small gate charge: $Q_g = 86 \text{ nC}$ (typ.)
- Low drain-source ON resistance: $R_{DS(ON)} = 6.2 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 120 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \text{ }\mu\text{A}$ (max) ($V_{DS} = 75 \text{ V}$)
- Enhancement-mode: $V_{th} = 1.1 \text{ to } 2.3 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	75	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	75	V
Gate-source voltage		V_{GSS}	± 20	V
Drain current	DC (Note 1)	I_D	60	A
	Pulse (Note 1)	I_{DP}	240	
Drain power dissipation ($T_c = 25^\circ\text{C}$)		P_D	140	W
Single pulse avalanche energy (Note 2)		E_{AS}	498	mJ
Avalanche current		I_{AR}	60	A
Repetitive avalanche energy (Note 3)		E_{AR}	9.2	mJ
Channel temperature		T_{ch}	150	$^\circ\text{C}$
Storage temperature range		T_{stg}	-55 to 150	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th(ch-c)}$	0.89	$^\circ\text{C/W}$
Thermal resistance, channel to ambient	$R_{th(ch-a)}$	83.3	$^\circ\text{C/W}$

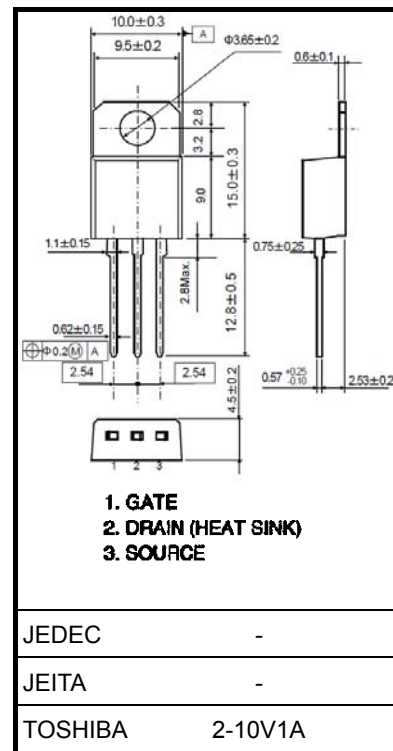
Note 1: Ensure that the channel & lead temperature does not exceed 150°C .

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^\circ\text{C}$, $L = 200 \text{ }\mu\text{H}$, $I_{AR} = 60 \text{ A}$, $R_G = 1 \Omega$

Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

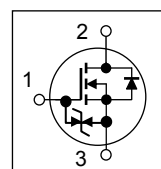
This transistor is an electrostatic sensitive device. Handle with care.

Unit: mm

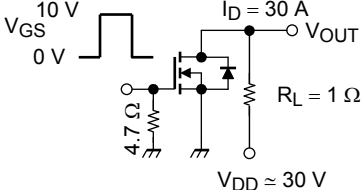


Weight: 1.35 g (typ.)

Internal Connection



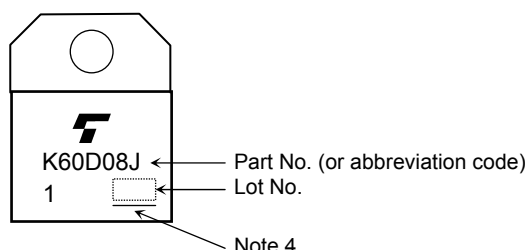
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	—	—	±10	μA
Drain cut-OFF current		I _{DSS}	V _{DS} = 75 V, V _{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	75	—	—	V
		V (BR) DSX	I _D = 10 mA, V _{GS} = -20 V	60	—	—	
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.1	—	2.3	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4.5 V, I _D = 30A	—	7.1	9.3	mΩ
			V _{GS} = 10 V, I _D = 30A	—	6.2	7.8	
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 30 A	60	120	—	S
Input capacitance		C _{iss}	V _{DS} = 10V, V _{GS} = 0 V, f = 1 MHz	—	5450	—	pF
Reverse transfer capacitance		C _{rss}		—	320	—	
Output capacitance		C _{oss}		—	1260	—	
Switching time	Rise time	t _r	 Duty ≤ 1%, t _w = 10 μs	—	5	—	ns
	Turn-ON time	t _{on}		—	20	—	
	Fall time	t _f		—	15	—	
	Turn-OFF time	t _{off}		—	96	—	
Total gate charge (gate-source plus gate-drain)		Q _g	V _{DD} ≈ 60 V, V _{GS} = 5 V, I _D = 60A	—	48	—	nC
			V _{DD} ≈ 60 V, V _{GS} = 10 V, I _D = 60A	—	86	—	
Gate-source charge 1		Q _{gs1}	V _{DD} ≈ 60 V, V _{GS} = 10 V, I _D = 60A	—	16	—	
Gate-drain (“miller”) charge		Q _{gd}		—	20	—	
Gate switch charge		Q _{SW}		—	27	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	60	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	240	A
Forward voltage (diode)	V_{DSF}	$I_{DR} = 60 \text{ A}$, $V_{GS} = 0 \text{ V}$	—	-0.9	-1.2	V
Reverse recovery time	t_{rr}	$I_{DR} = 60 \text{ A}$, $V_{GS} = 0 \text{ V}$,	—	63	—	ns
Reverse recovery charge	Q_{rr}	$dI_{DR}/dt = 50 \text{ A}/\mu\text{s}$	—	63	—	nC

Marking

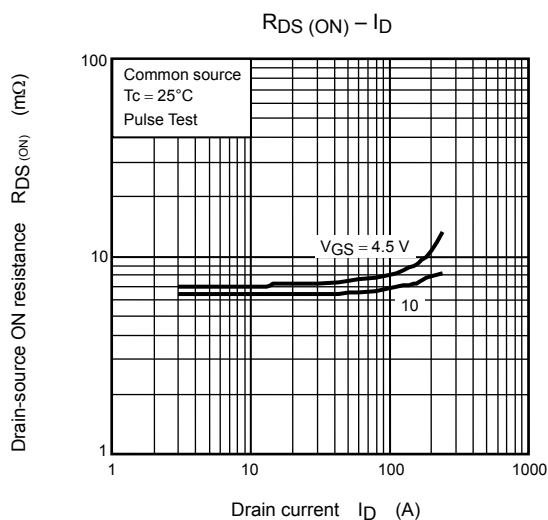
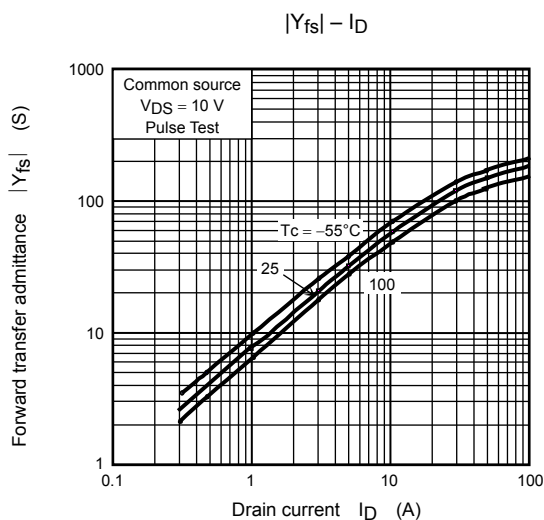
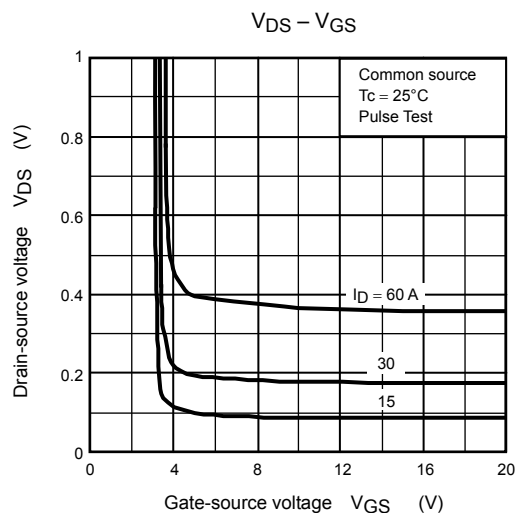
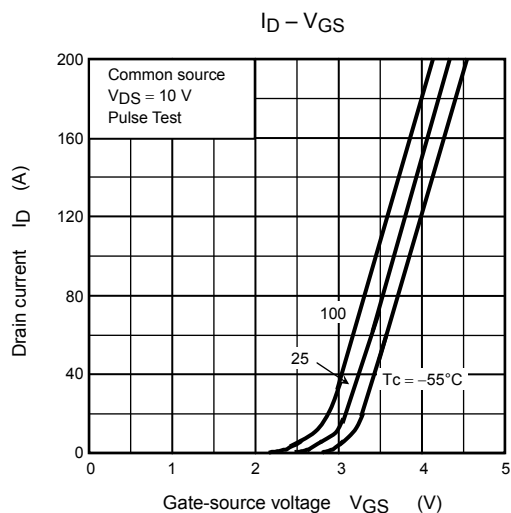
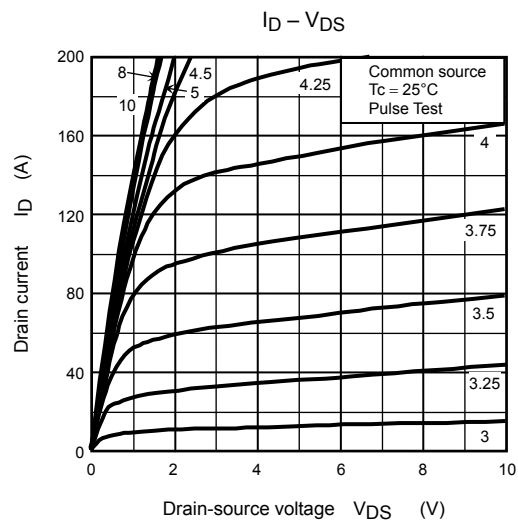
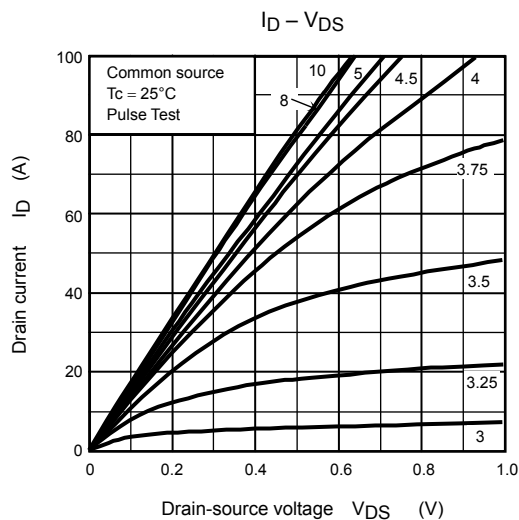


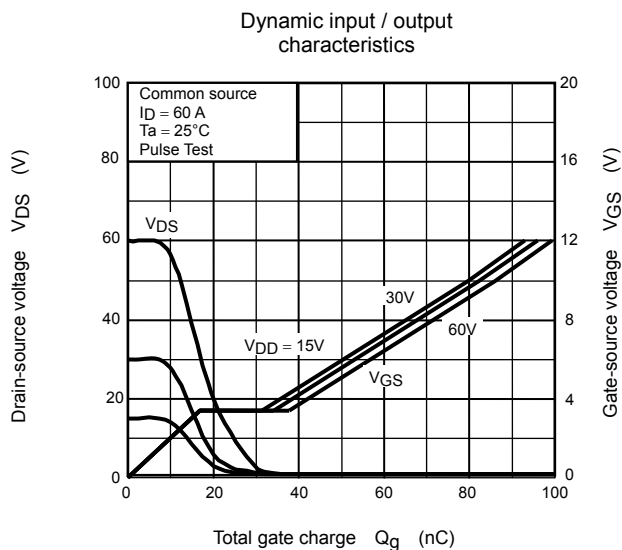
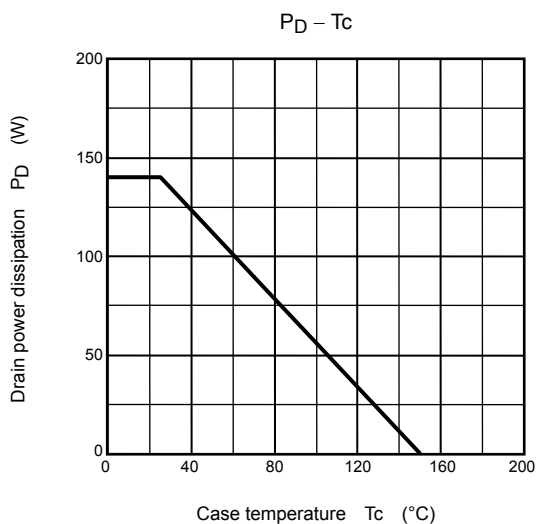
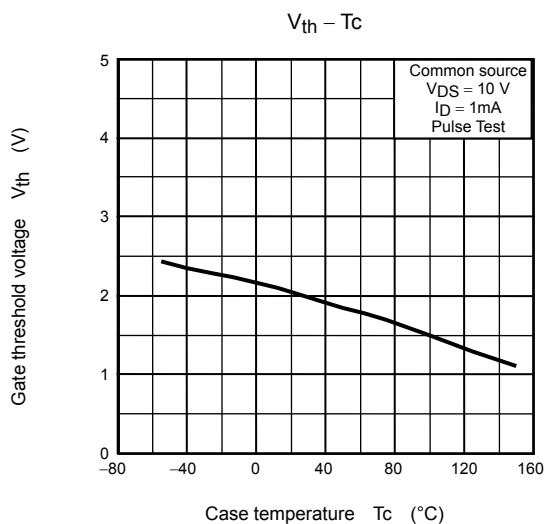
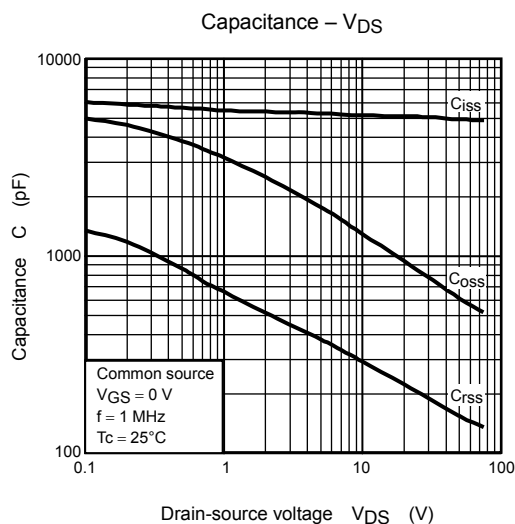
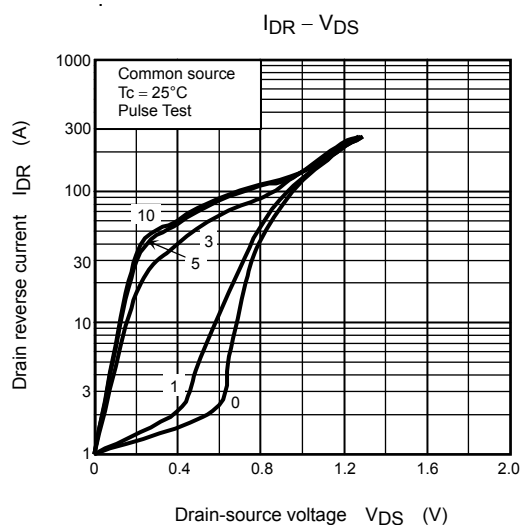
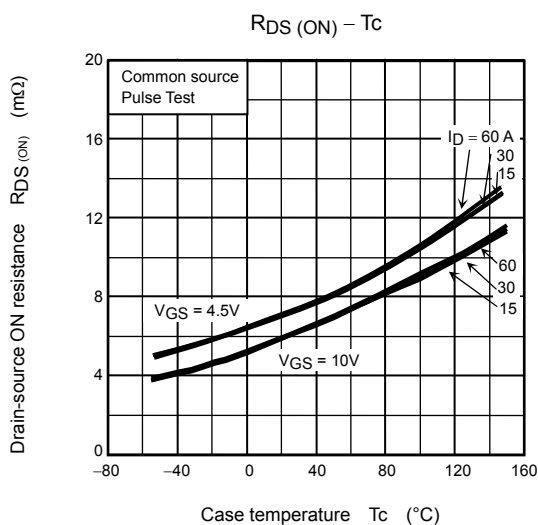
Note 4: A line under a Lot No. identifies the indication of product Labels.

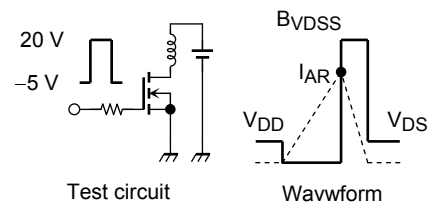
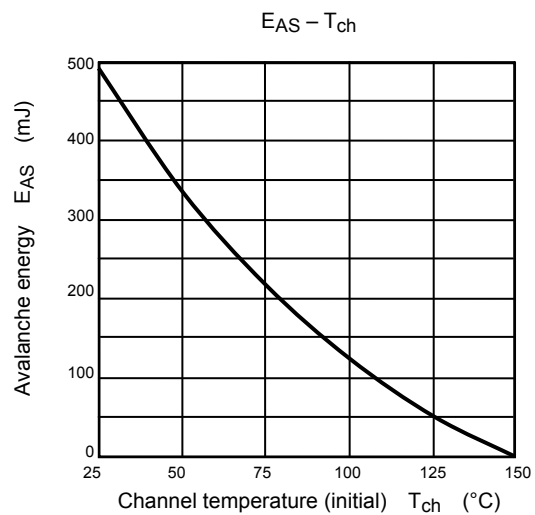
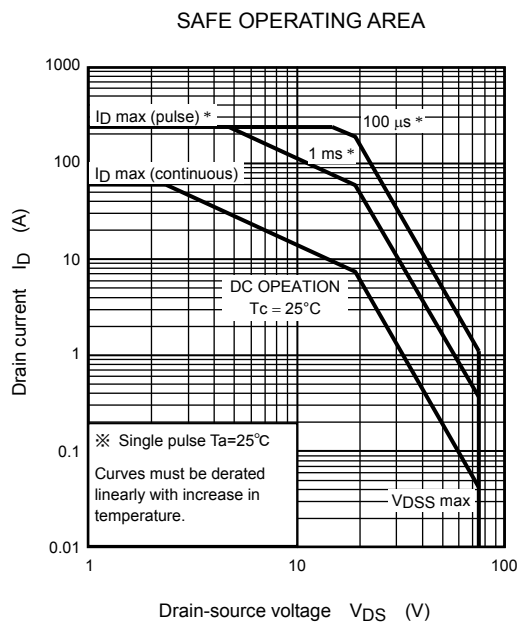
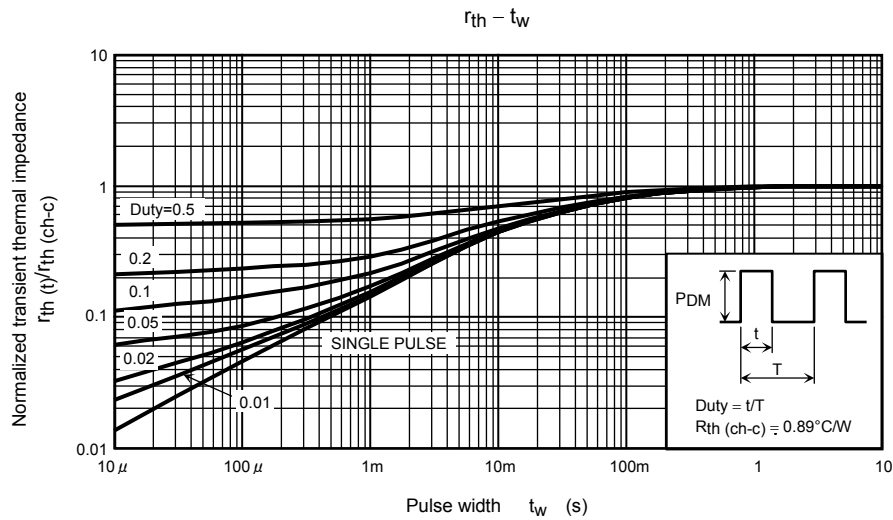
Not underlined: [[Pb]]/INCLUDES > MCV

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$$R_G = 1\Omega$$

$$V_{DD} = 25\text{ V}, L = 200\text{ }\mu\text{H}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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