

# N-Channel 100-V (D-S) MOSFET

## PRODUCT SUMMARY

| $V_{DS}$ (V) | $R_{DS(on)}$ ( $\Omega$ ) | $I_D$ (A) | $Q_g$ (Typ.) |
|--------------|---------------------------|-----------|--------------|
| 100          | 0.0183 at $V_{GS} = 10$ V | 60        | 48           |
|              | 0.023 at $V_{GS} = 8.0$ V | 53        |              |

## FEATURES

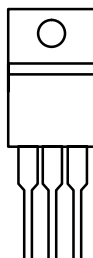
- TrenchFET® Power MOSFET
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC


**RoHS**  
COMPLIANT

## APPLICATIONS

- Industrial
- Power Supply

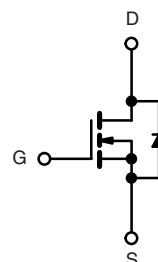
TO-220AB



G D S

Top View

Ordering Information: SUP60N10-18P-E3 (Lead (Pb)-free)



N-Channel MOSFET

## ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted

| Parameter  | Symbol         | Limit            | Unit |
|--|----------------|------------------|------|
| Drain-Source Voltage                             | $V_{DS}$       | 100              | V    |
| Gate-Source Voltage                              | $V_{GS}$       | $\pm 20$         |      |
| Continuous Drain Current ( $T_J = 175$ °C)       | $I_D$          | 60               | A    |
|  |                | 50               |      |
| Pulsed Drain Current                             | $I_{DM}$       | 100              |      |
| Avalanche Current                                | $I_{AS}$       | 45               |      |
| Single Avalanche Energy <sup>a</sup>             | $E_{AS}$       | 101              | mJ   |
| Maximum Power Dissipation <sup>a</sup>           | $P_D$          | 150 <sup>b</sup> | W    |
|  |                | 3.75             |      |
| Operating Junction and Storage Temperature Range | $T_J, T_{stg}$ | - 55 to 175      | °C   |

## THERMAL RESISTANCE RATINGS

| Parameter                                    | Symbol     | Limit | Unit |
|--|------------|-------|------|
| Junction-to-Ambient (PCB Mount) <sup>c</sup> | $R_{thJA}$ | 40    | °C/W |
| Junction-to-Case (Drain)                     | $R_{thJC}$ | 1.0   |      |

Notes:

a. Duty cycle  $\leq 1$  %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

| SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted               |                      |  |      |       |        |      |
|---|----------------------|--|------|-------|--------|------|
| Parameter   | Symbol               | Test Conditions  | Min. | Typ.  | Max.   | Unit |
| Static  |                      |  |      |       |        |      |
| Drain-Source Breakdown Voltage  | V <sub>DS</sub>      | V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA   | 100  |       |        | V    |
| Gate Threshold Voltage  | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA  | 2.5  |       | 4.5    |      |
| Gate-Body Leakage   | I <sub>GSS</sub>     | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V  |      |       | ± 250  | nA   |
| Zero Gate Voltage Drain Current   | I <sub>DSS</sub>     | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V   |      |       | 1      | μA   |
|   |                      | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C  |      |       | 50     |      |
|   |                      | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C  |      |       | 250    |      |
| On-State Drain Current <sup>a</sup>   | I <sub>D(on)</sub>   | V <sub>DS</sub> ≥ 10 V, V <sub>GS</sub> = 10 V   | 50   |       |        | A    |
| Drain-Source On-State Resistance <sup>a</sup>                               | R <sub>DS(on)</sub>  | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A  |      | 0.015 | 0.0183 | Ω    |
|   |                      | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C   |      | 0.027 | 0.033  |      |
|   |                      | V <sub>GS</sub> = 8.0 V, I <sub>D</sub> = 10 A   |      | 0.018 | 0.023  |      |
| Forward Transconductance <sup>a</sup>                                       | g <sub>fs</sub>      | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A  |      | 33    |        | S    |
| Dynamic <sup>b</sup>  |                      |  |      |       |        |      |
| Input Capacitance   | C <sub>iss</sub>     | V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 50 V, f = 1 MHz   |      | 2600  |        | pF   |
| Output Capacitance  | C <sub>oss</sub>     |  |      | 230   |        |      |
| Reverse Transfer Capacitance  | C <sub>rss</sub>     |  |      | 80    |        |      |
| Total Gate Charge <sup>c</sup>  | Q <sub>g</sub>       | V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A  |      | 48    | 75     | nC   |
| Gate-Source Charge <sup>c</sup>   | Q <sub>gs</sub>      |  |      | 16    |        |      |
| Gate-Drain Charge <sup>c</sup>  | Q <sub>gd</sub>      |  |      | 13    |        |      |
| Gate Resistance   | R <sub>g</sub>       | f = 1 MHz  | 0.25 | 1.1   | 2.4    | Ω    |
| Turn-On Delay Time <sup>c</sup>   | t <sub>d(on)</sub>   | V <sub>DD</sub> = 50 V, R <sub>L</sub> = 1.0 Ω<br>I <sub>D</sub> ≅ 50 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω |      | 12    | 20     | ns   |
| Rise Time <sup>c</sup>  | t <sub>r</sub>       |  |      | 10    | 20     |      |
| Turn-Off Delay Time <sup>c</sup>  | t <sub>d(off)</sub>  |  |      | 18    | 35     |      |
| Fall Time <sup>c</sup>  | t <sub>f</sub>       |  |      | 8     | 15     |      |
| Drain-Source Body Diode Characteristics T <sub>C</sub> = 25 °C <sup>b</sup> |                      |  |      |       |        |      |
| Continuous Current  | I <sub>S</sub>       |  |      |       | 60     | A    |
| Pulsed Current  | I <sub>SM</sub>      |  |      |       | 100    |      |
| Forward Voltage <sup>a</sup>  | V <sub>SD</sub>      | I <sub>F</sub> = 15 A, V <sub>GS</sub> = 0 V   |      | 0.85  | 1.5    | V    |
| Reverse Recovery Time   | t <sub>rr</sub>      | I <sub>F</sub> = 50 A, dI/dt = 100 A/μs  |      | 80    | 120    | ns   |
| Peak Reverse Recovery Current   | I <sub>RM(REC)</sub> |  |      | 4     |        | A    |
| Reverse Recovery Charge   | Q <sub>rr</sub>      |  |      | 160   | 240    | nC   |

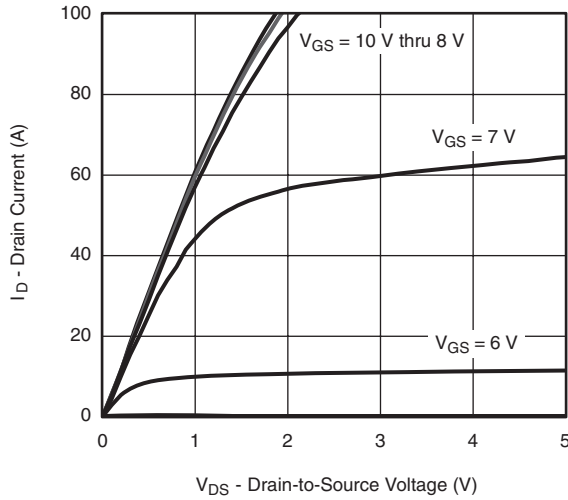
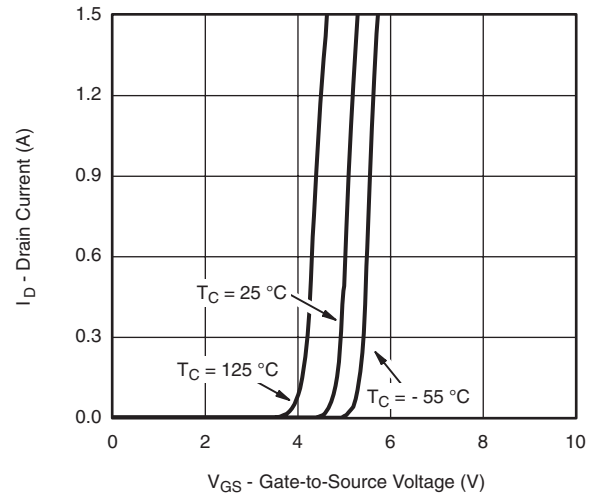
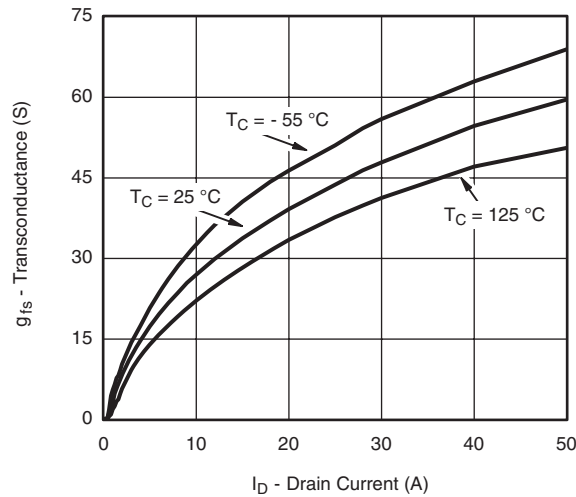
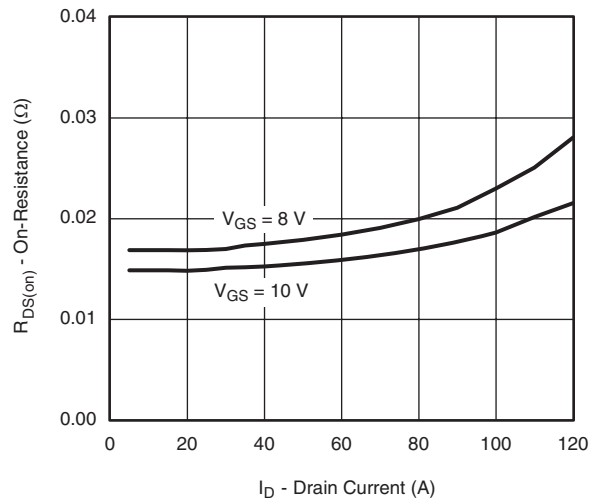
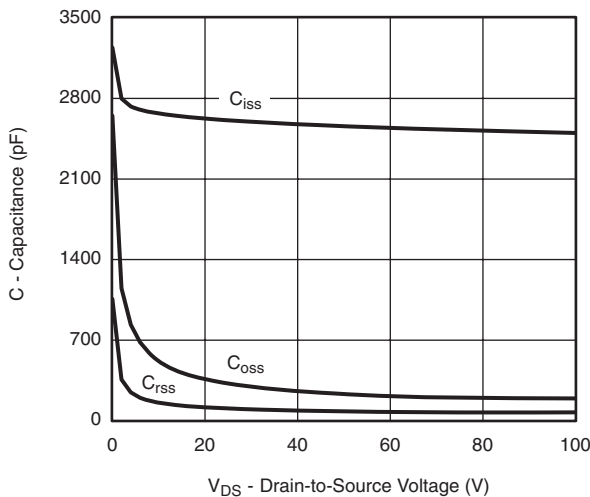
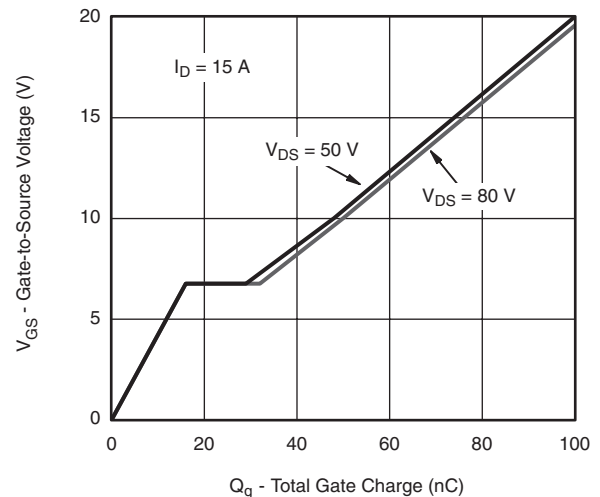
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

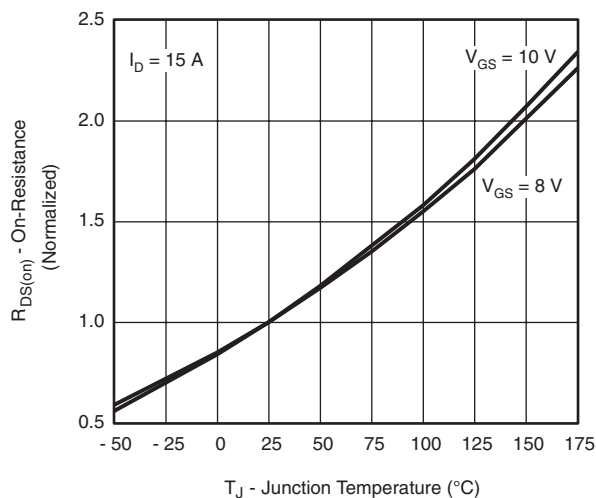
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

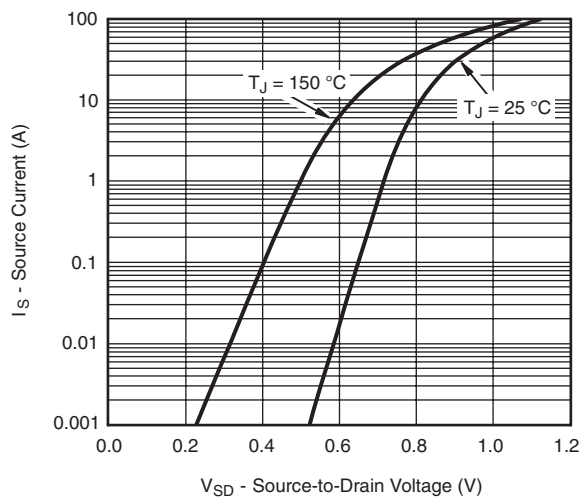
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**Output Characteristics**

**Transfer Characteristics**

**Transconductance**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

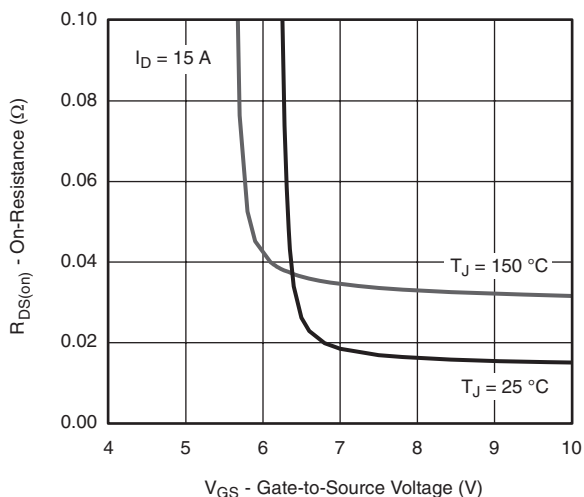
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



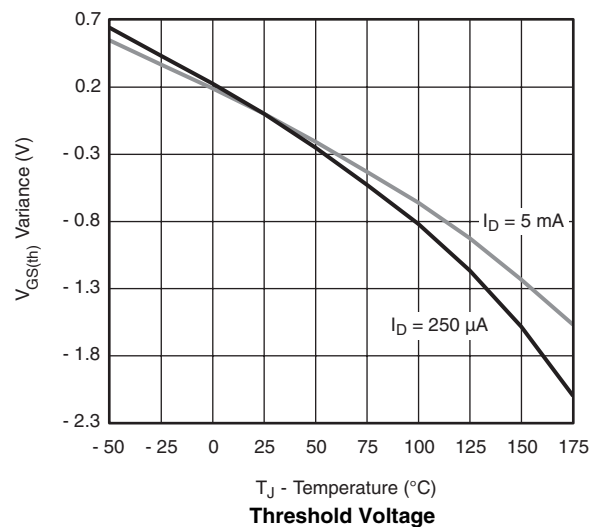
On-Resistance vs. Junction Temperature



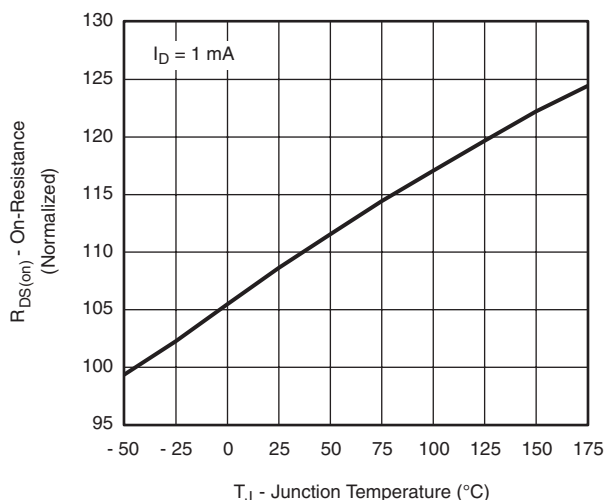
Source-Drain Diode Forward Voltage



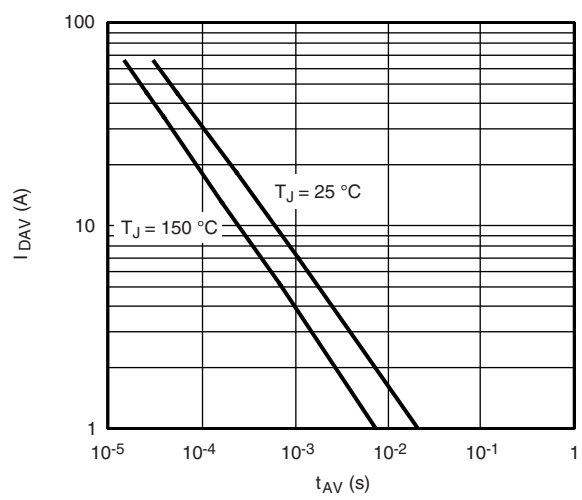
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

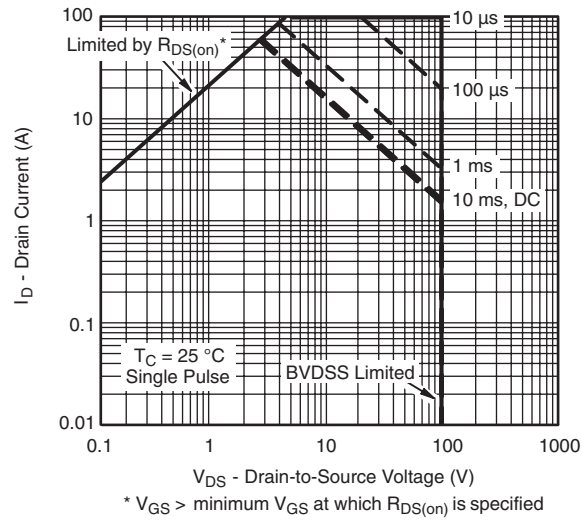


On-Resistance vs. Junction Temperature

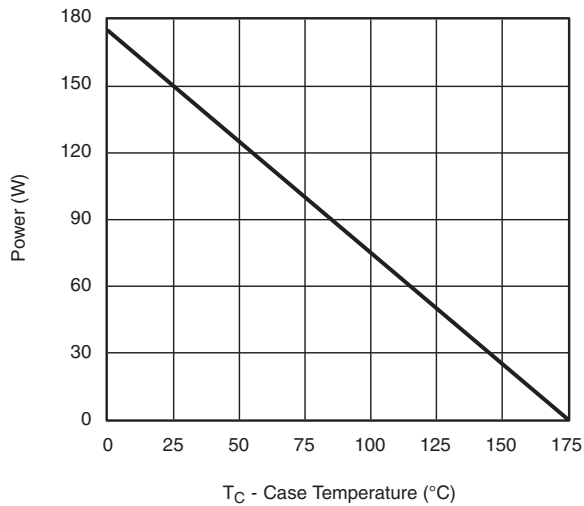


Avalanche Current vs. Time

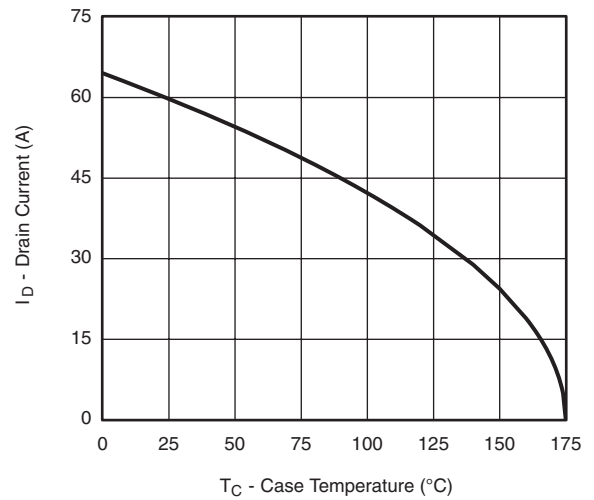
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



**Safe Operating Area**



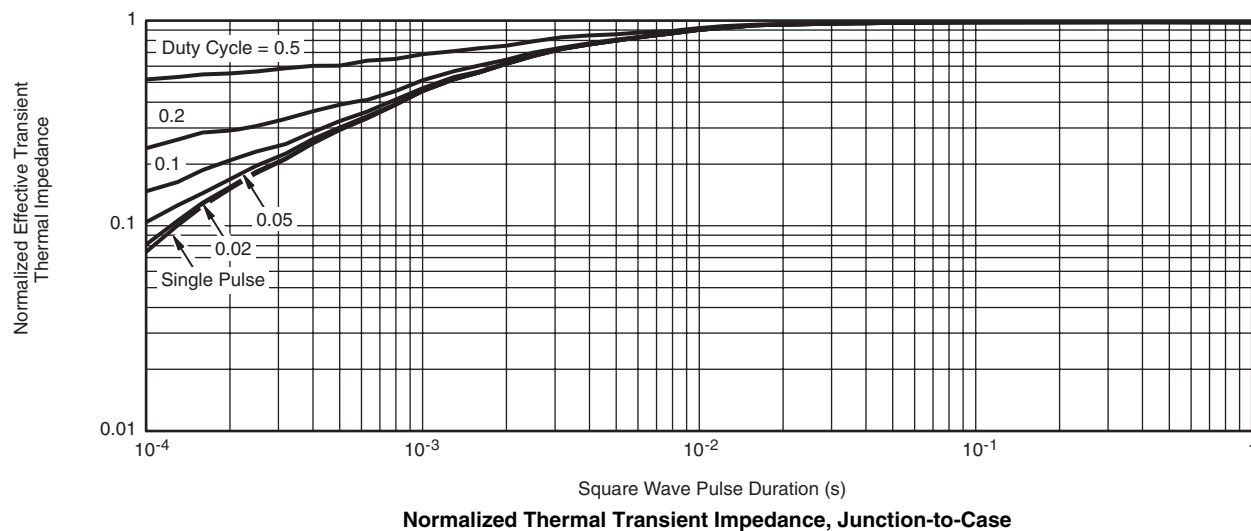
**Power Derating, Junction-to-Case**



**Current Derating\***

\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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