

PDS1040CTL

# 10A DUAL LOW VF SCHOTTKY BARRIER RECTIFIER POWERDI®

## **Product Summary**

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F max</sub> (V)	I <sub>R max</sub> (mA) @ 40V
40	10	0.50	0.2

### **Description and Applications**

PDS1040CTL is a dual die Schottky barrier rectifier in PowerDI5 package. It is designed for use in Low Voltage, High Frequency Inverters, OR'ing, and Polarity Protection Applications.

#### **Features and Benefits**

- Guard Ring Die Construction for Transient Protection
- Low Forward Voltage Drop
- Very Low Reverse Leakage Current
- High Forward Surge Current Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: POWERDI5
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Polarity: See Diagram
- Weight: 0.096 grams (approximate)

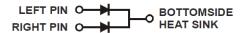
#### POWERDI5







**Bottom View** 



### Ordering Information (Note 4)

Part Number	Case	Packaging		
PDS1040CTL-13	POWERDI5	5000/Tape & Reel		
PDS1040CTL-7	POWERDI5	1500/Tape & Reel		

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



S1040CTL = Product type marking code

OH:= Manufacturers' code marking

YYWW = Date code marking

YY = Last digit of year (ex: 14 for 2014)

WW = Week code (01 - 53)

K = Factory Designator Code



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	<b>&gt;</b>
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current (See also Figure 5) per element total device	lo	5 10	А
Non-Repetitive Peak Forward Surge Current, per element 8.3ms Single half sine-wave Superimposed on Rated Load	IFSM	110	А

## **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{\theta JS}$	_	2.0	°C/W
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{\theta JA}$	95	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 6)	$R_{ heta JA}$	75	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 7)	$R_{\theta JA}$	50	_	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to	+150	°C

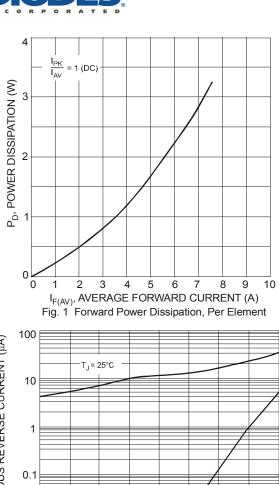
# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

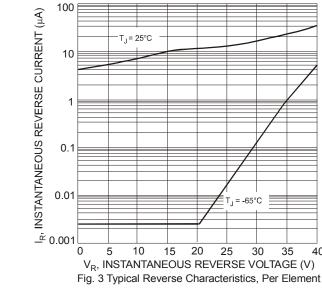
Characteristic		Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V <sub>(BR)R</sub>	40	_		>	I <sub>R</sub> = 500μA
		_	0.465	0.50		I <sub>F</sub> = 5A, T <sub>S</sub> = +25°C
		_	0.41	0.45		I <sub>F</sub> = 5A, T <sub>S</sub> = +100°C
Forward Voltage Per Element	\/_	_	0.39	0.43	V	I <sub>F</sub> = 5A, T <sub>S</sub> = +125°C
Toward voitage Fer Element	V <sub>F</sub>	_	0.55	0.60		I <sub>F</sub> = 10A, T <sub>S</sub> = +25°C
		_	0.53	0.57		I <sub>F</sub> = 10A, T <sub>S</sub> = +100°C
		_	0.52	0.56		I <sub>F</sub> = 10A, T <sub>S</sub> = +125°C
		_	20	200	μΑ	V <sub>R</sub> = 40V, T <sub>S</sub> = +25°C
		_	3	25	mA	V <sub>R</sub> = 40V, T <sub>S</sub> = +100°C
Reverse Leakage Current (Note 8) Per Element		_	15	15 150 μA V <sub>R</sub> =	$V_R = 35V, T_S = +25^{\circ}C$	
Reverse Leakage Current (Note o) Fer Element	I <sub>R</sub>	_	2.5	10	mA	$V_R = 35V, T_S = +100^{\circ}C$
		_	6	80	μA	V <sub>R</sub> = 17.5V, T <sub>S</sub> = +25°C
		_	1	5	mA	V <sub>R</sub> = 17.5V, T <sub>S</sub> = +100°C

Notes:

- 5. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
- Polyimide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
   Polyimide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.
- 8. Short duration pulse test used to minimize self-heating effect.







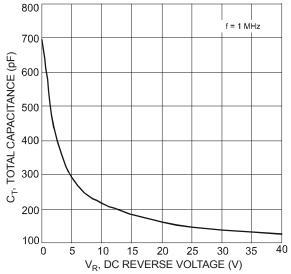
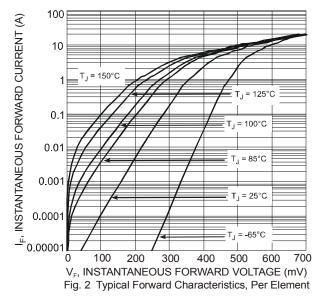
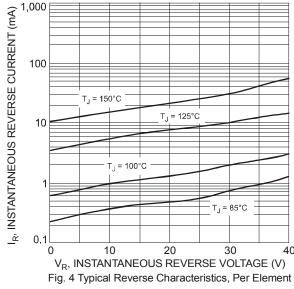


Fig. 5 Total Capacitance vs. Reverse Voltage, Per Element





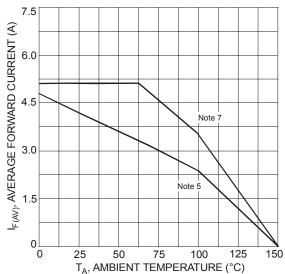


Fig. 6 Forward Current Derating Curve, Per Element



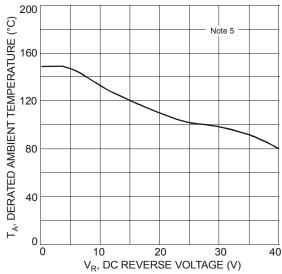
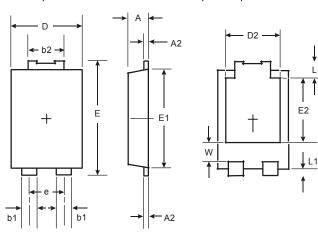


Fig. 7 Operating Temperature Derating, Per Element

# **Package Outline Dimensions**

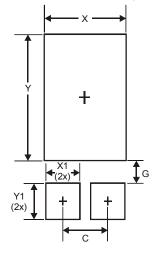
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI <sup>®</sup> 5				
Dim	Min	Max		
Α	1.05	1.15		
A2	0.33	0.43		
b1	0.80	0.99		
b2	1.70	1.88		
D	3.90	4.05		
D2	3.054 Typ			
Е	6.40	6.60		
е	1.84 Typ			
E1	5.30 5.45			
E2	3.549 Typ			
Ĺ	0.75 0.95			
L1	0.50	0.65		
W	1.10 1.41			
All Dimensions in mm				

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	1.840		
G	0.852		
Х	3.360		
X1	1.390		
Υ	4.860		
Y1	1.400		



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