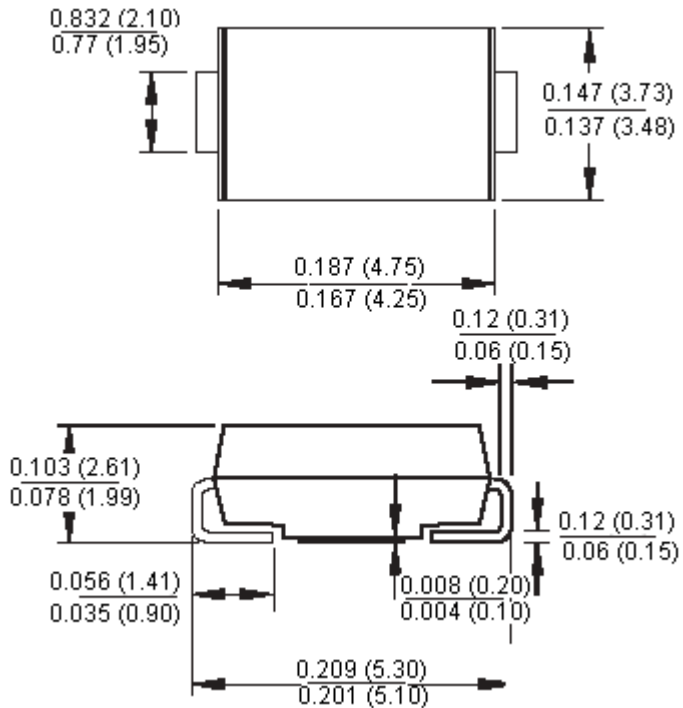




Features:

- Glass passivated junction chip.
- For surface mounted application.
- Low profile package.
- Built-in strain relief.
- Ideal for automated placement.
- Easy pick and place.
- Superfast recovery time for high efficiency.
- Glass passivated chip junction.
- High temperature soldering: 260°C/10 seconds at terminals.
- Plastic material.

SMB/DO-214AA



Dimensions : Inches (Millimetres)

Mechanical Data:

Cases	: Moulded plastic.
Terminals	: Pure tin plated, lead free.
Polarity	: Indicated by cathode band.
Packing	: 12mm tape per EIA STD RS-481.
Weight	: 0.093 gram.

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Type Number	Symbol	ES2A	ES2B	ES2C	ES2D	ES2F	ES2G	ES2H	ES2J	Units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	V _{RMS}	35	70	105	140	210	280	350	420	
Maximum DC Blocking Voltage	V _{DC}	50	100	150	200	300	400	500	600	
Maximum Average Forward Rectified Current	I _(AV)	2.0								A
Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	50								
Maximum Instantaneous Forward Voltage at 2.0A	V _F	0.95				1.3		1.7		V
Maximum DC Reverse Current at T _A = 25°C at Rated DC Blocking Voltage at T _A = 100°C	I _R	10 350								μA μA
Maximum Reverse Recovery Time (Note 1)	T _{rr}	35								nS
Typical Junction Capacitance (Note 2)	C _j	25				20				pF
Maximum Thermal Resistance (Note 3)	R _{θJA} R _{θJL}	75 20								°C/W
Operating Temperature Range	T _J	-55 to +150								°C
Storage Temperature Range	T _{STG}									

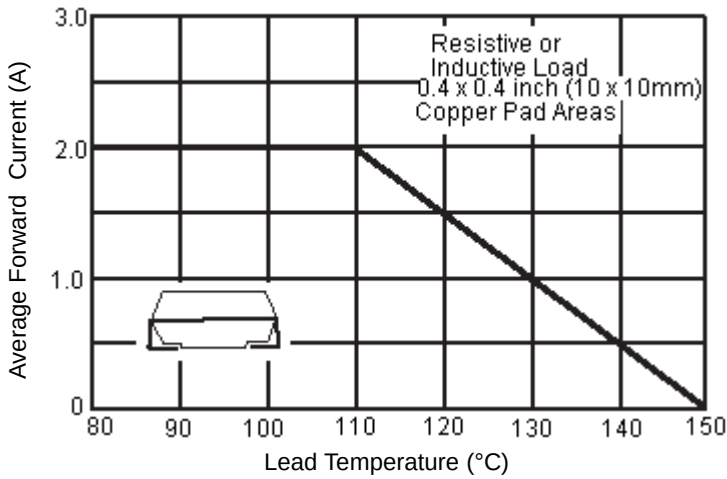
Notes: 1. Reverse Recovery Test Conditions: $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $I_{RR} = 0.25\text{A}$.

2. Measured at 1MHz and Applied $V_R = 4.0$ Volts.

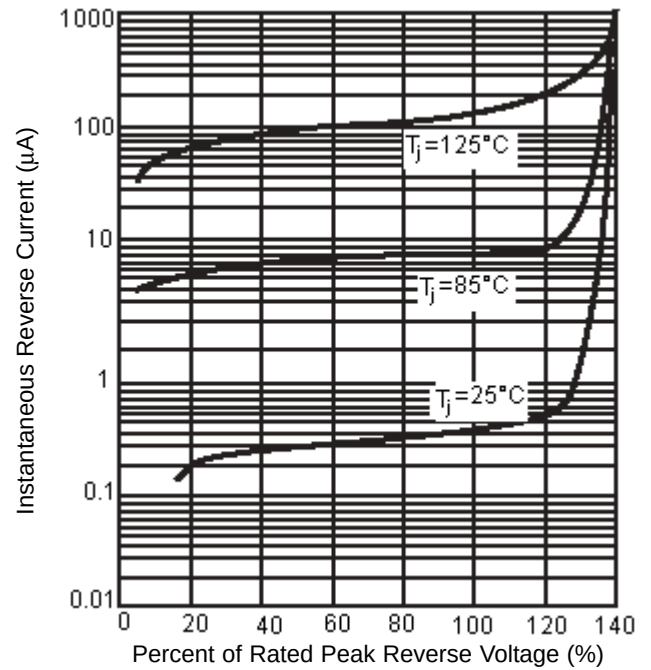
3. Units Mounted on PCB 0.4 x 0.4 inches (10mm x 10mm) Pad Areas.

Ratings and Characteristic Curves (ES2A thru ES2J)

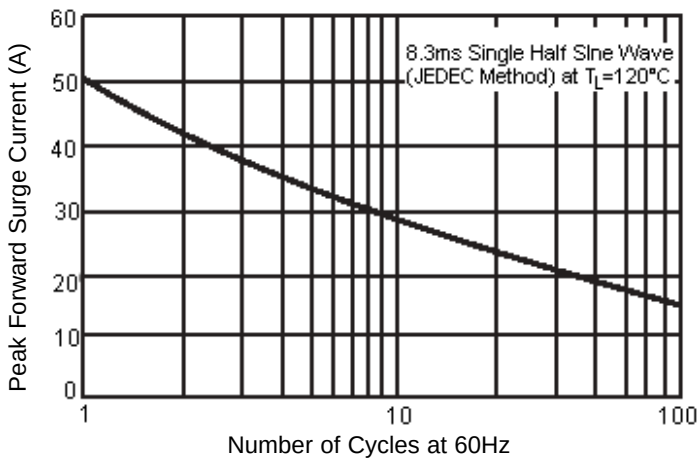
Maximum Forward Current Derating Curve



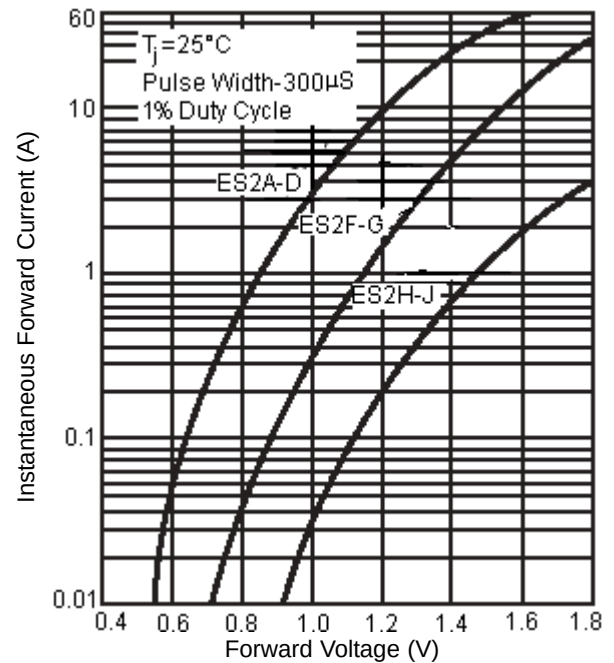
Typical Reverse Characteristics



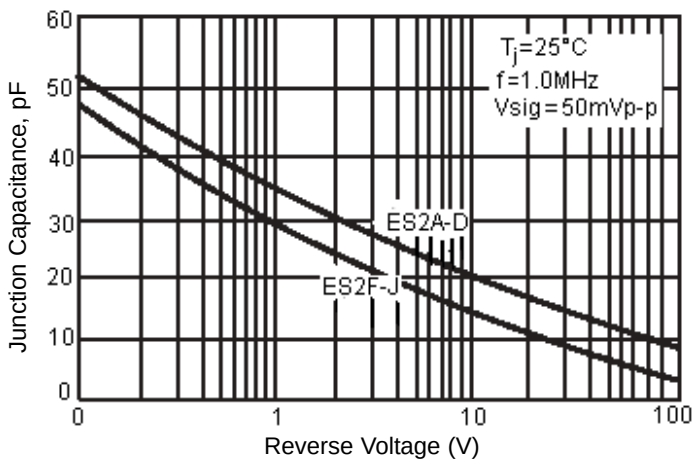
Maximum Non-Repetitive Peak Forward Surge Current



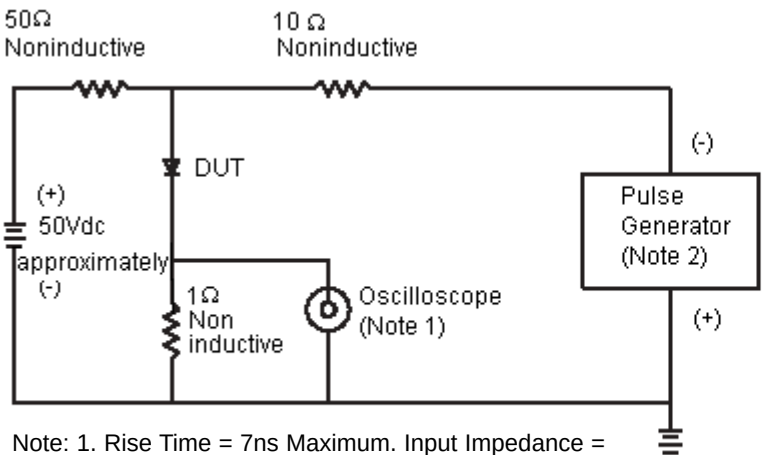
Typical Instantaneous Forward Characteristics



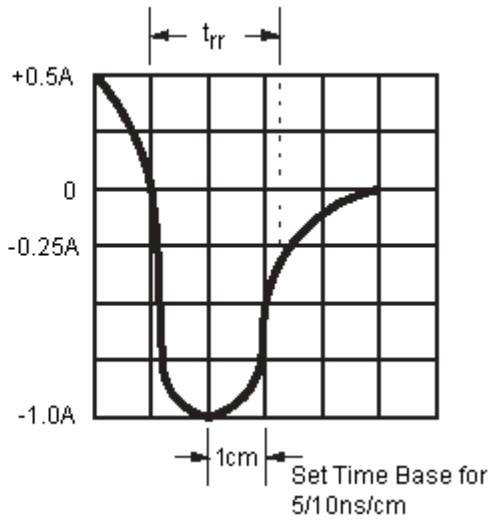
Typical Junction Capacitance



Reverse Recovery Time Characteristic and Test Circuit Diagram



Note: 1. Rise Time = 7ns Maximum. Input Impedance = 1 Megohm 22pf
2. Rise Time = 10ns Maximum Source Impedance = 50 ohms.



Part Number Table

Description	Part Number
Diode, Ultra-Fast, 2A, 50V	ES2A
Diode, Ultra-Fast, 2A, 100V	ES2B
Diode, Ultra-Fast, 2A, 150V	ES2C
Diode, Ultra-Fast, 2A, 200V	ES2D
Diode, Ultra-Fast, 2A, 300V	ES2F
Diode, Ultra-Fast, 2A, 400V	ES2G
Diode, Ultra-Fast, 2A, 500V	ES2H
Diode, Ultra-Fast, 2A, 600V	ES2J

Notes:

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