

CFPS-67, -68, -69

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Delivery Options

- Please contact our sales office for current leadtimes

Output Compatibility

- Tri-state CMOS
- Drive Capability 15pF max

Package Outline

- 5.0 × 3.2 × 0.95mm SMD Ceramic Package

Standard Frequency Stabilities

- ±25ppm, ±50ppm, ±100ppm (inclusive of supply voltage & output load variations over the operating temperature range)

Operating Temperature Ranges

- -10 to 70°C (CFPS-67, -68, -69)
- -40 to 85°C (CFPS-67I, -68I, -69I)

Storage Temperature Range

- -55 to 125°C

Tri-state Operation

- Logic '1' to pad 1 enables oscillator output ($\geq 70\% V_s$)
- Logic '0' to pad 1 disables oscillator output; when the oscillator output goes to the high impedance state ($\leq 30\% V_s$)
- No connection to pad 1 enables oscillator output
- Standby current 1µA max

Solder Conditions

- For typical soldering conditions, please see the relevant pages in Applications Notes

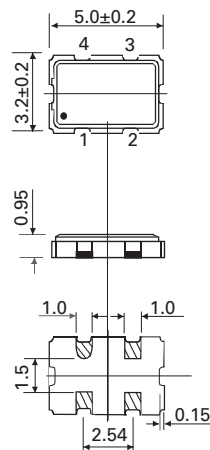
Marking

- Model number
- Frequency

Minimum Order Information Required

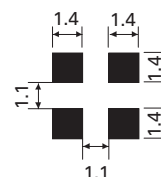
- Frequency + Model Number + Operating Temperature Code (if applicable) + Frequency Stability

Outline in mm

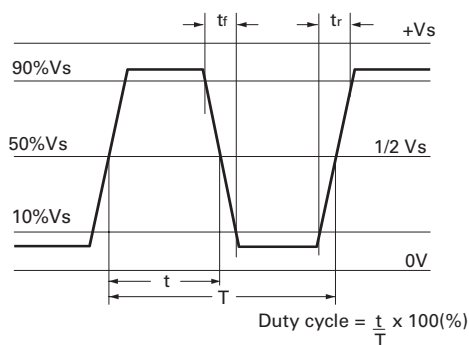


Pad Connections
 1. Enable/Disable
 2. GND
 3. Output
 4. +Vs

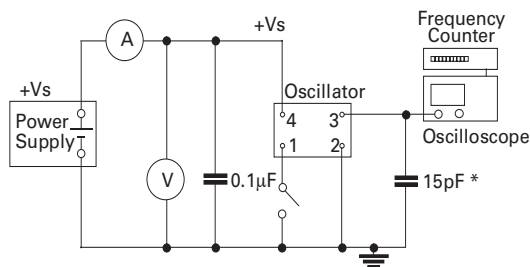
Solder pad layout



Output Waveform



Test circuit



* Inclusive of jigging & equipment capacitance

