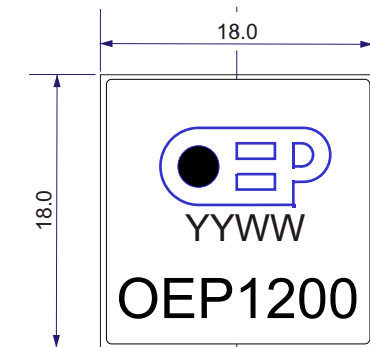
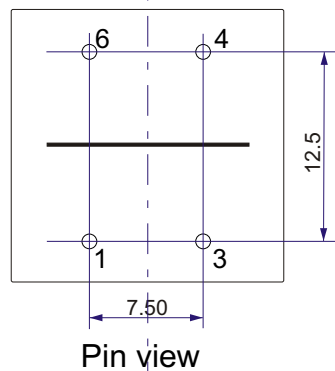
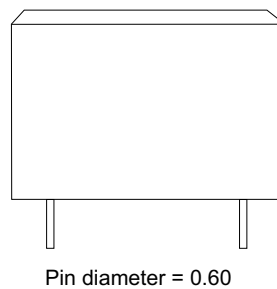
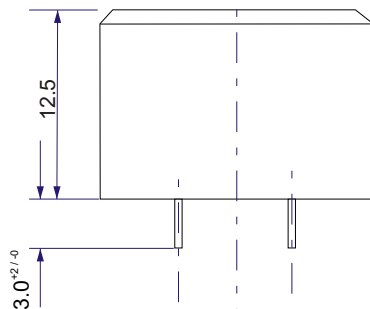


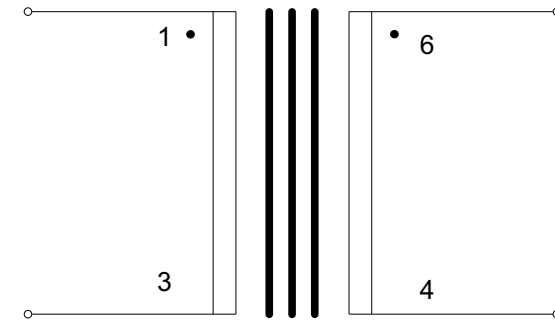


YY WW = date code



Pin view

Recommended PCB hole diameter 1.00 +0.15 / -0
Tolerance on all dimensions +/-0.20 unless stated otherwise



Electrical schematic

Specification:

Turns ratio (+/-1%) 1 to 1 @ 6kHz, 0.1Vrms

DC resistance (+/-15%): 65 Ω per winding

Impedance matching: 600 Ω to 600 Ω

Frequency response (reference 1kHz): 300 - 4kHz (+/- 0.2dB)

Leakage inductance (10mV, 200Hz) (1 - 3): 20mH max.

Return loss: (ref. 600 Ω) 200 to 4kHz: 18dB min.

Insertion loss: (@ 2kHz, 0dBu): 1.4dB max. ($R_L=468 \Omega$)

Longitudinal balance: 200Hz - 4kHz: 80dB min.

Distortion: 200Hz, 0dBu: -75dBm typ.

Saturation: 10Vrms (65V peak)

Signal to noise ratio: >33dB

Hi-pot, primary to secondary: 4kV, 1mA for 1 minute

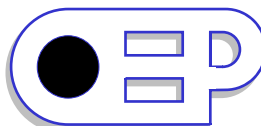
Core loss (@ 1kHz, 1Vrms): 13k

Note: Do not pass DC current through windings.

Operating temperature range: -20 to +70°C

Storage temperature range: -40°C to +125°C

Approvals: certified to IEC 62368-1:2014

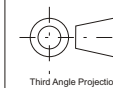


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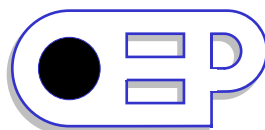
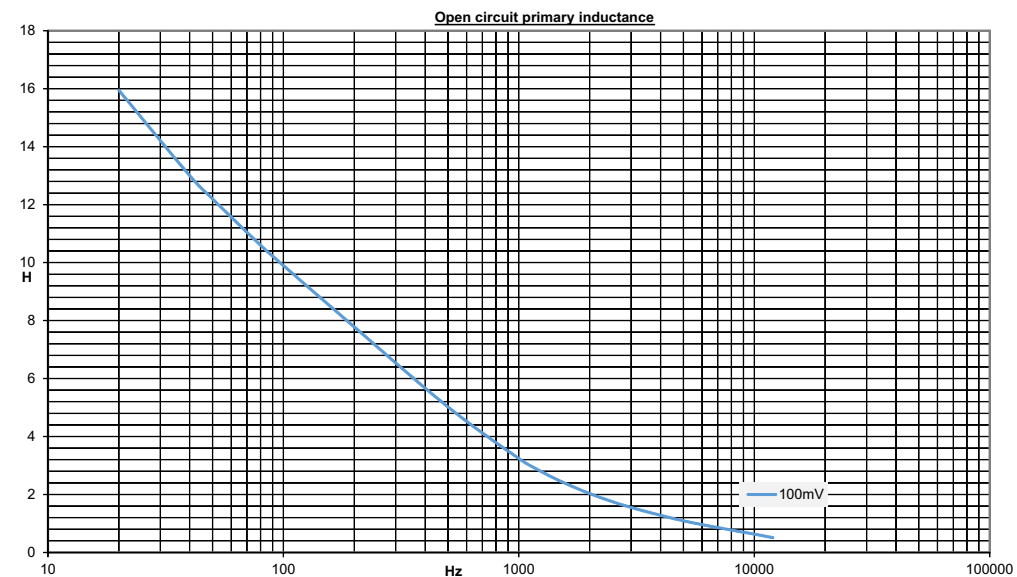
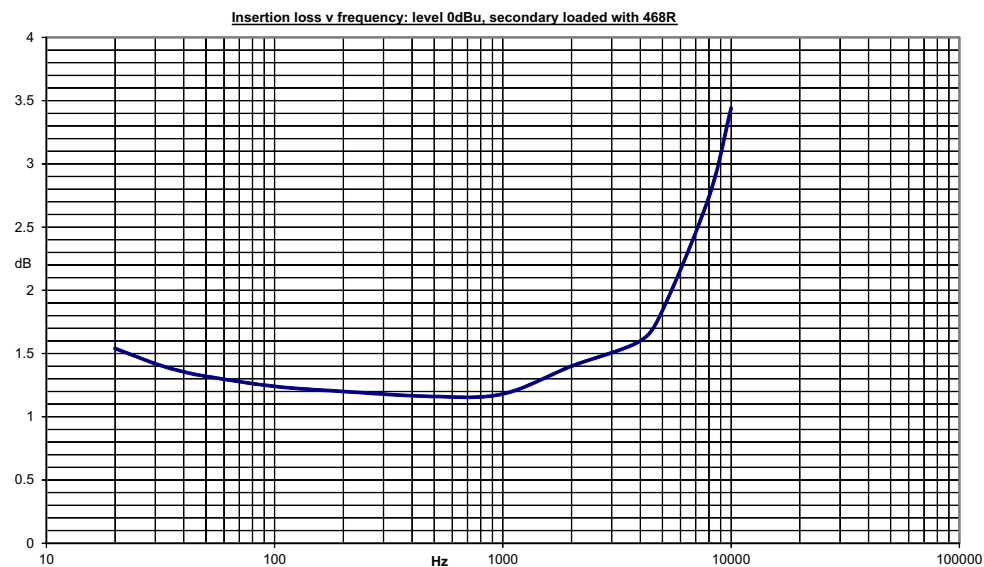
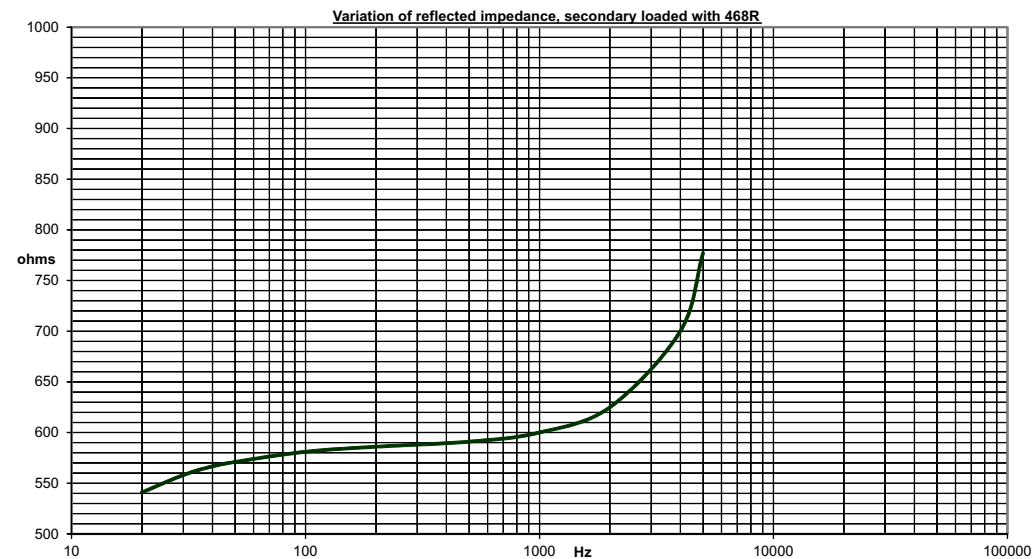
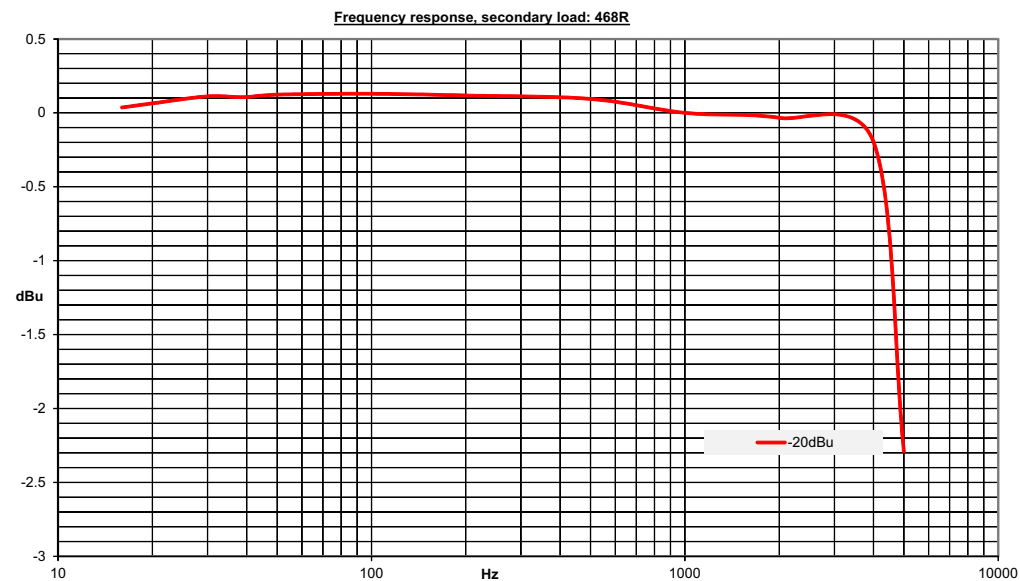
Scale: 2 to 1

DESCRIPTION
Specification for OEP1200
page 1 of 2



All dimensions in mm unless stated otherwise

ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
1	31/07/03	CS		OEP1200
16	14/10/22	CS		
17	16/02/23	CS		
18	05/03/24	CS		



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Scale: nts

DESCRIPTION
Specification for OEP1200
page 2 of 2

All dimensions in mm unless stated otherwise

ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
1	31/07/03	CS		OEP1200
16	14/10/22	CS		
17	16/02/23	CS		
18	05/03/24	CS		